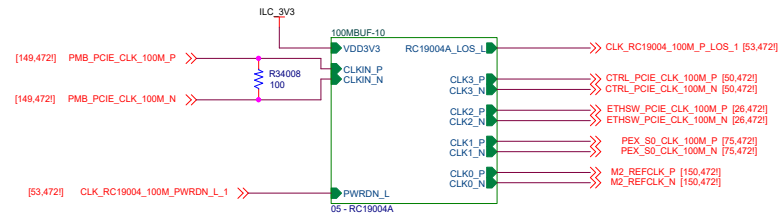
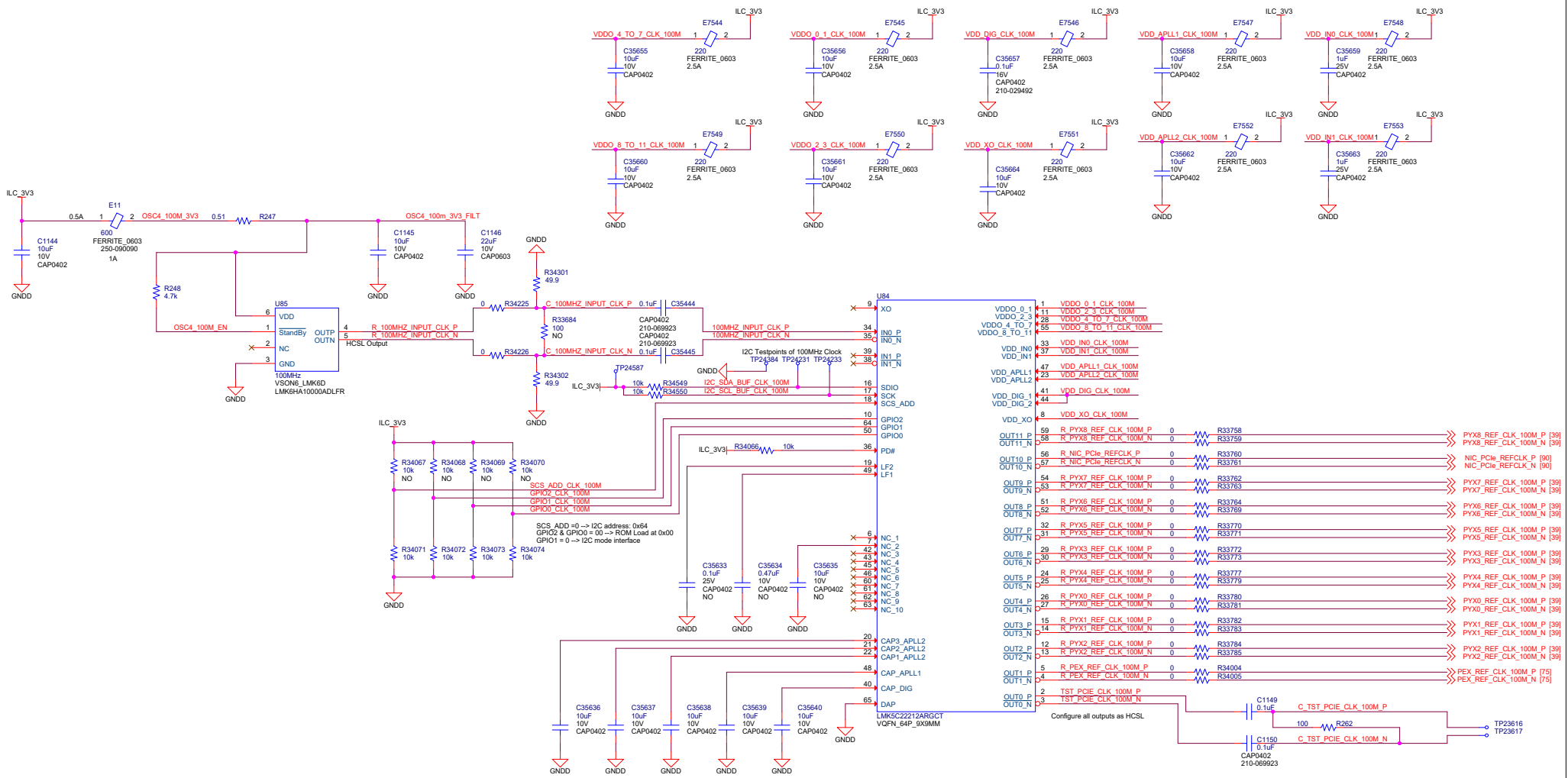
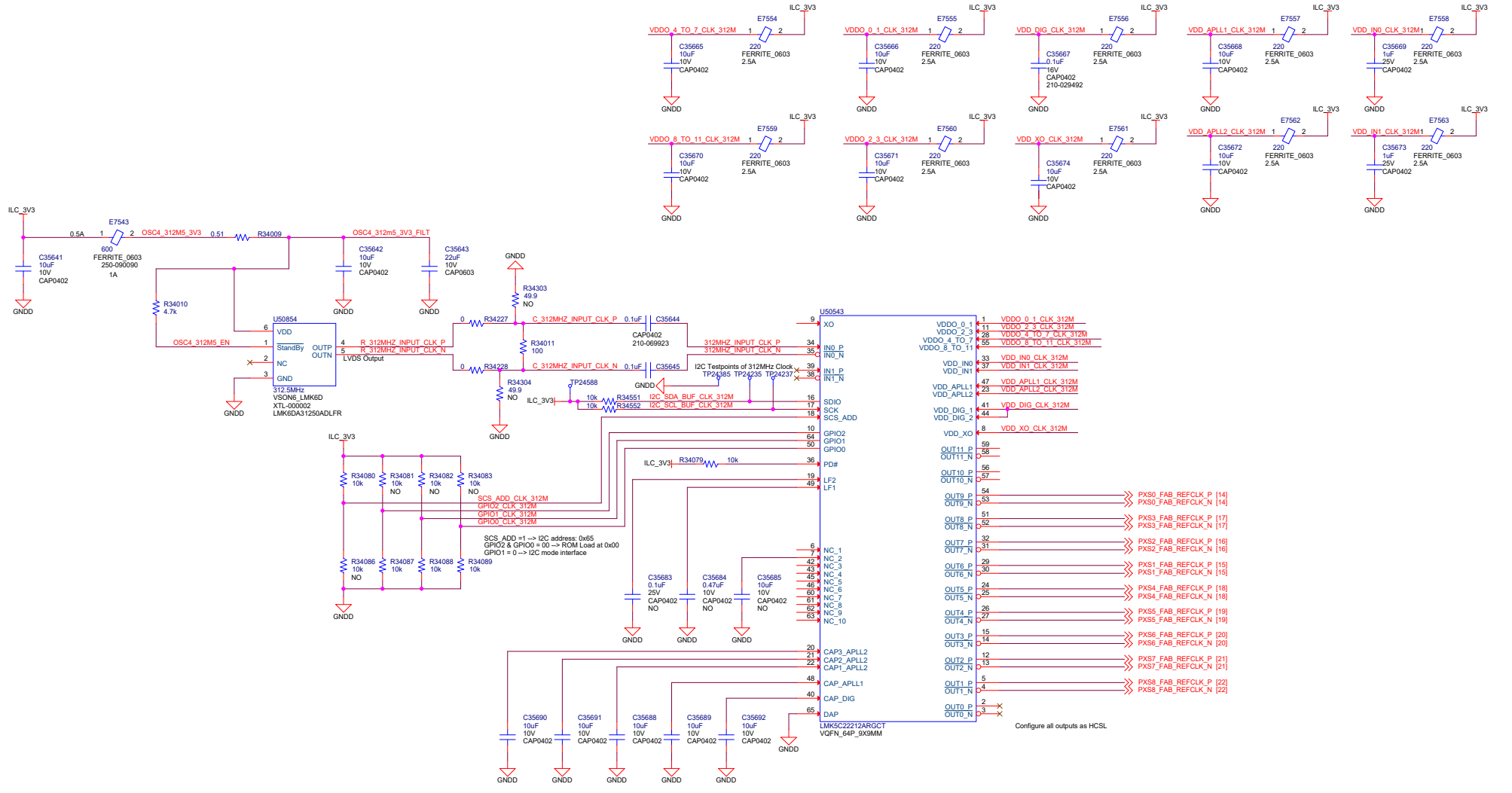


PCIe 100M CLOCK DISTRIBUTION



312.5M CLOCK DISTRIBUTION



Configure all outputs as HCSL

PROJECT PYXIS TEST CARD		SECTION 312.5MHZ CLOCK	
recogni	PART NO.	SCH-186765	DATE
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