

USB VOLTAGE REG 3.3V @1500mA Power Use 900mA

The diagram illustrates a USB voltage regulator circuit and a 200ms delay circuit. The main circuit uses a TPS7A4533KTTR (IC?) to regulate the 5V_SECOND input to 3.3V_USB_HUB. The regulator's IN pin (2) is connected to 5V_SECOND through a 10kF/0402/1% resistor (R?). The OUT pin (4) is connected to 3.3V_USB_HUB. The SHDN pin (1) is connected to the SENSE/ADJ pin (5) through a 10kF/0402/1% resistor (R?). The TAB pin (6) is connected to GND. The GND pin (3) is connected to GND. The delay circuit (TLV803RDBZR IC?) is connected to the 3.3V_USB_HUB line. The RESET pin (2) is connected to TP? (Test Point). The VDD pin (3) is connected to 3.3v_CM4. The GND pin (1) is connected to GND. The delay circuit is used to provide a 200ms delay.

5V_SECOND

10nF/0805/10V/X7R

100nF/0402/50V/X7R

10kF/0402/1%

R?

IC?

IN

OUT

SHDN

SENSE/ADJ

TAB

GND

TPS7A4533KTTR

3.3V_USB_HUB

10nF/0805/10V/X7R

100nF/0402/50V/X7R

TP?

Test Point

RESET

VDD

GND

TLV803RDBZR IC?

3.3v_CM4

200ms Delay Circuit

[illegible]