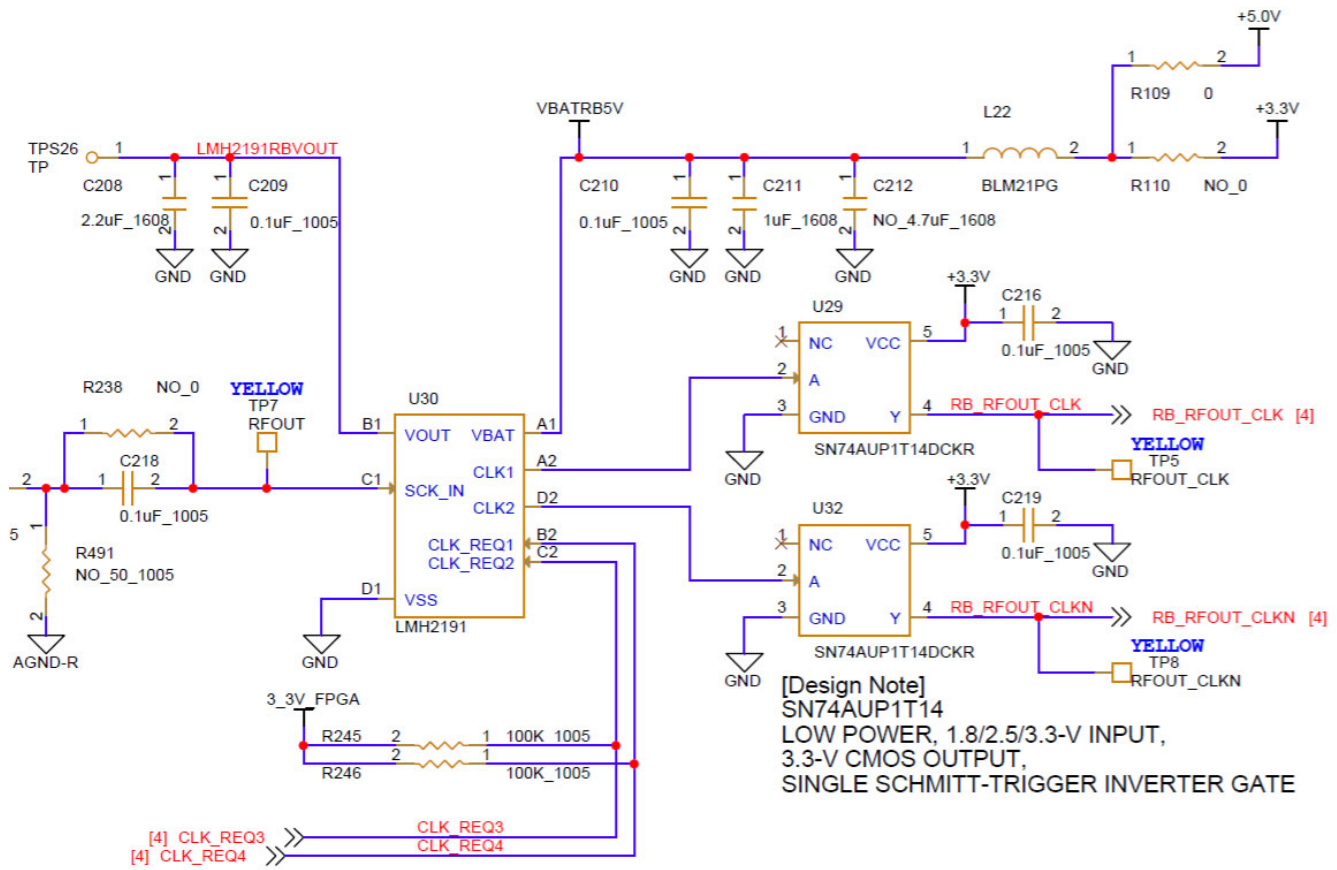




[Design Note]
 SN74AUP1T14
 LOW POWER, 1.8/2.5/3.3-V INPUT,
 3.3-V CMOS OUTPUT,
 SINGLE SCHMITT-TRIGGER INVERTER GATE

[Design Note]
 CLK_REQ:
 Clock Request Input = ON at high level