

● Question

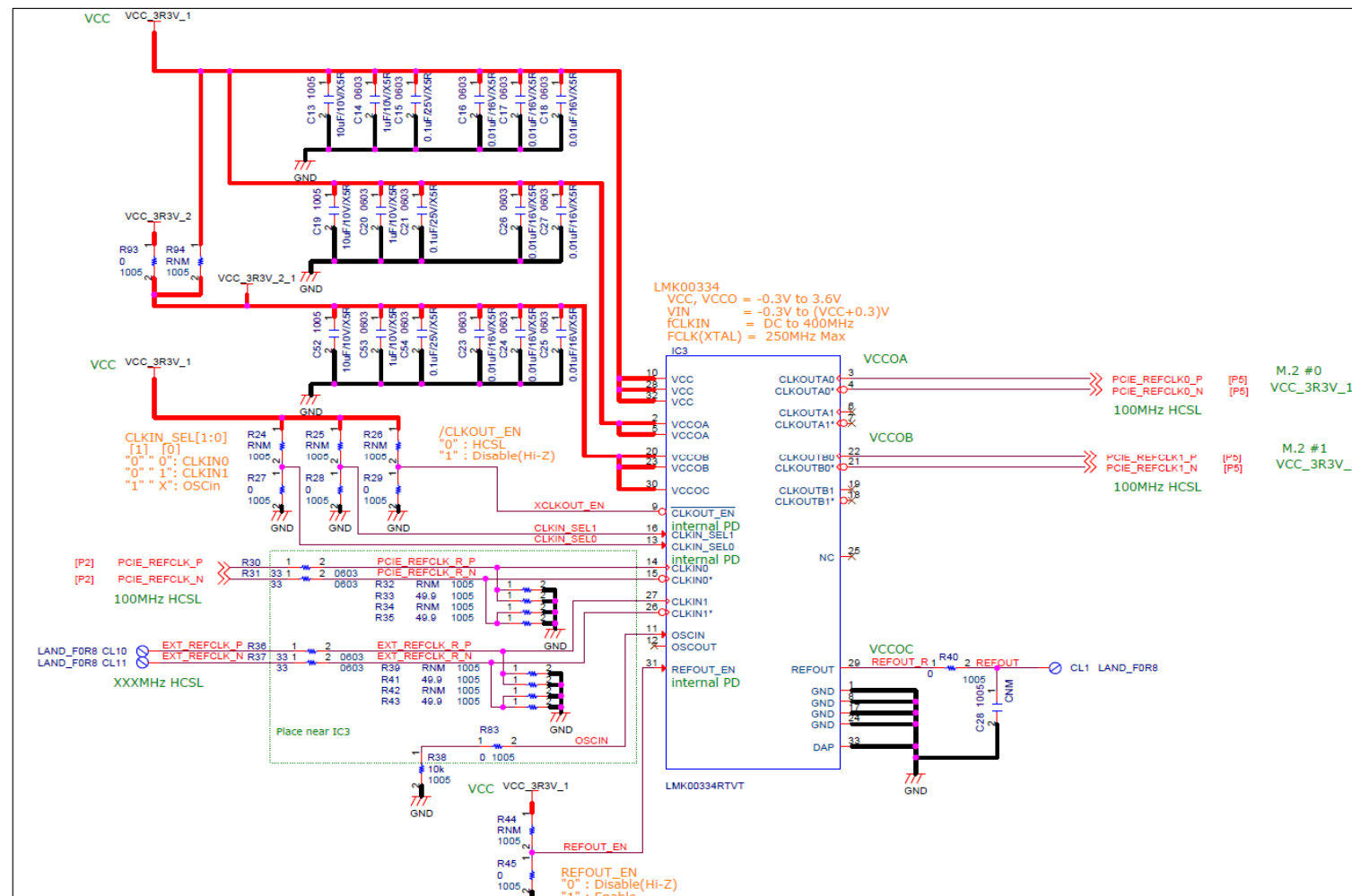
1. Is there a difference in output behavior between using the output separately for bankA and bankB and using it together for bankA?
 2. Is there any problem with the input processing of CLKIN [0/1] and OSCIN?
 3. Is there any problem with the power connection of VCC, VCCOA, VCCOB, VCCOC?
 4. Do you have any concerns by looking at the schematic?
- * For details, refer to the circuit diagram and block diagram.

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● Prerequisites

REFCLK : Input(HCSL) x1, Output (HCSL) x2

circuit diagram



Block Diagram

