**TI Question on LMK00804BQWRGTRQ1**

I am working with LMK00804BQWRGTRQ1 and am having issues with false input clock triggers. I think it might have something to do with the input stage.

Does this input (LVCMOS\_CLK) have BJT input stages or are these FET inputs?

I do understand that the datasheet indicates that this input accepts LVCMOS/LVTTL outputs from other devices. However, I am wondering how the input stage on the IC itself (not the external driver) is constructed. Does this clock input have a bipolar input stage or a CMOS input stage? I added some pictures of different input stages (BJT vs FET).



