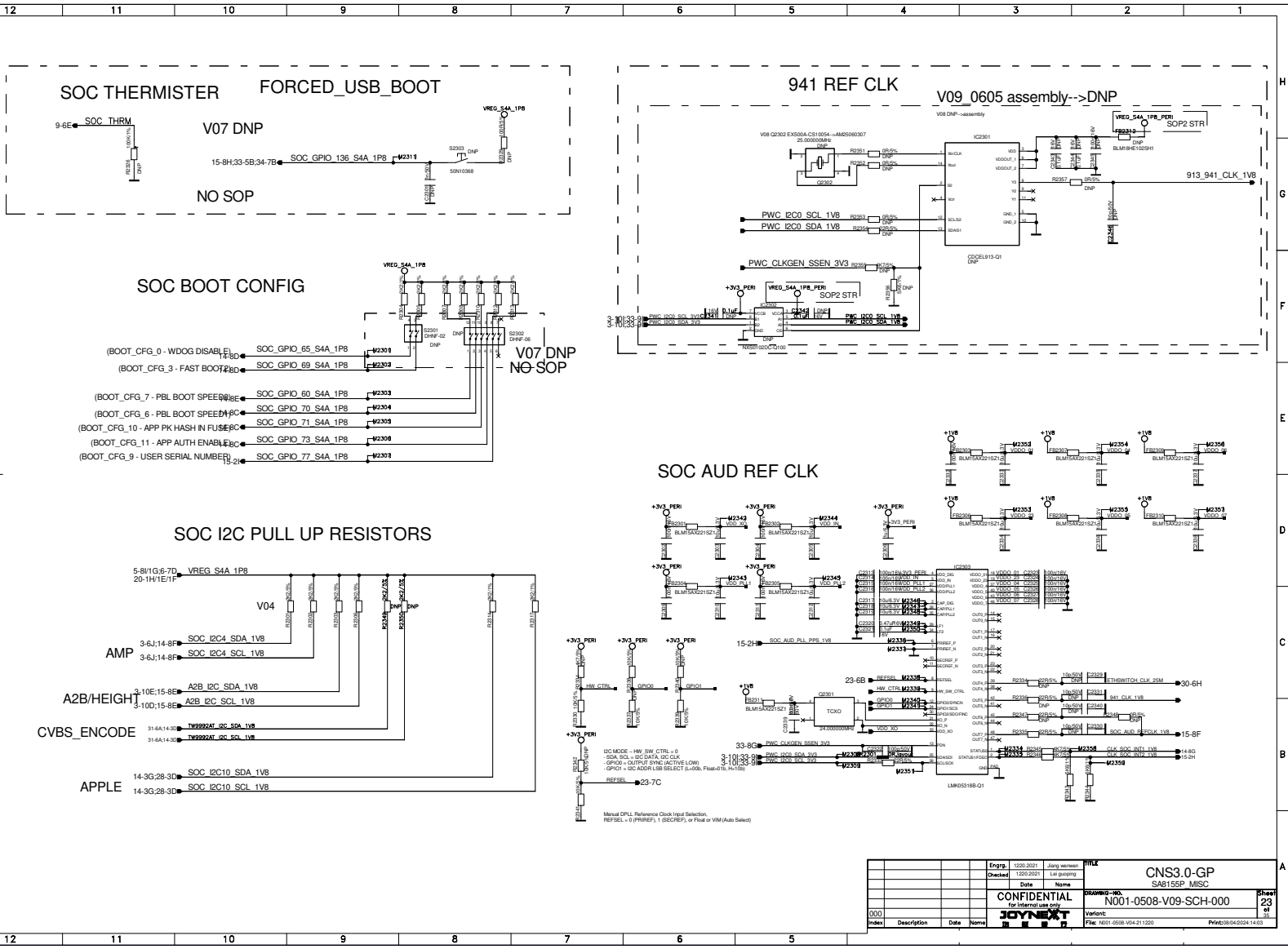
SOC THERMISTER
FORCED_USB_BOOT
V07 DNP
NO SOP
SOC_THRM
SOC_GPIO_136_S4A_1P8
941 REF CLK
V09_0605 assembly-->DNP
913_941_CLK_1V8
PWC_I2C0_SCL_1V8
PWC_I2C0_SDA_1V8
PWC_CLKGEN_SSEN_3V3
SOP2 STR
SOC BOOT CONFIG
V07 DNP
NO SOP
(BOOT_CFG_0 - WDOG DISABLE)
(BOOT_CFG_3 - FAST BOOT)
(BOOT_CFG_7 - PBL BOOT SPEED)
(BOOT_CFG_6 - PBL BOOT SPEED)
(BOOT_CFG_10 - APP PK HASH IN FUSE)
(BOOT_CFG_11 - APP AUTH ENABLE)
(BOOT_CFG_9 - USER SERIAL NUMBER)
SOC_GPIO_65_S4A_1P8
SOC_GPIO_69_S4A_1P8
SOC_GPIO_60_S4A_1P8
SOC_GPIO_70_S4A_1P8
SOC_GPIO_71_S4A_1P8
SOC_GPIO_73_S4A_1P8
SOC_GPIO_77_S4A_1P8
SOC AUD REF CLK
SOC_AUD_PLL_PPS_1V8
SOC_AUD_REFCLK_1V8
ETHSWITCH_CLK_25M
941_CLK_1V8
CLK_SOC_INT1_1V8
CLK_SOC_INT2_1V8
SOC I2C PULL UP RESISTORS
AMP
A2B/HEIGHT
CVBS_ENCODE
APPLE
SOC_I2C4_SDA_1V8
SOC_I2C4_SCL_1V8
A2B_I2C_SDA_1V8
A2B_I2C_SCL_1V8
TW9992AT_I2C_SDA_1V8
TW9992AT_I2C_SCL_1V8
SOC_I2C10_SDA_1V8
SOC_I2C10_SCL_1V8
Manual DPLL Reference Clock Input Selection.
REFSEL = 0 (PRIREF), 1 (SECREF), or Float or VIM (Auto Select)
CONFIDENTIAL
for internal use only
JOYNEXT
CNS3.0-GP
SA8155P_MISC
N001-0508-V09-SCH-000
Sheet 23 of 35
Engrg. 1220.2021 Jiang wenwen
Checked 1220.2021 Lei guoqing
Date Name
Index Description Date Name
File: N001-0508-V04-211220
Print: 08/04/2024 14:03