



Logic I/O definitions for EEPROM stat-up mode
 I2C mode - HW_SW_CTRL = 0
 - SDA,SCL = I2C DATA, I2C CLK
 - GPIO0 = OUTPUT SYNC
 - GPIO1 = I2C ADDR LSB (L=00b, F=01b, H=10b)
 SPI mode - HW_SW_CTRL = STATUS[1:0] = F
 - SDA,SCL = SPI DATA IN, SPI CLK
 - GPIO1 = SPI SCS
 - GPIO2 = SPI DATA OUT
 L = low, F = float, H = high

