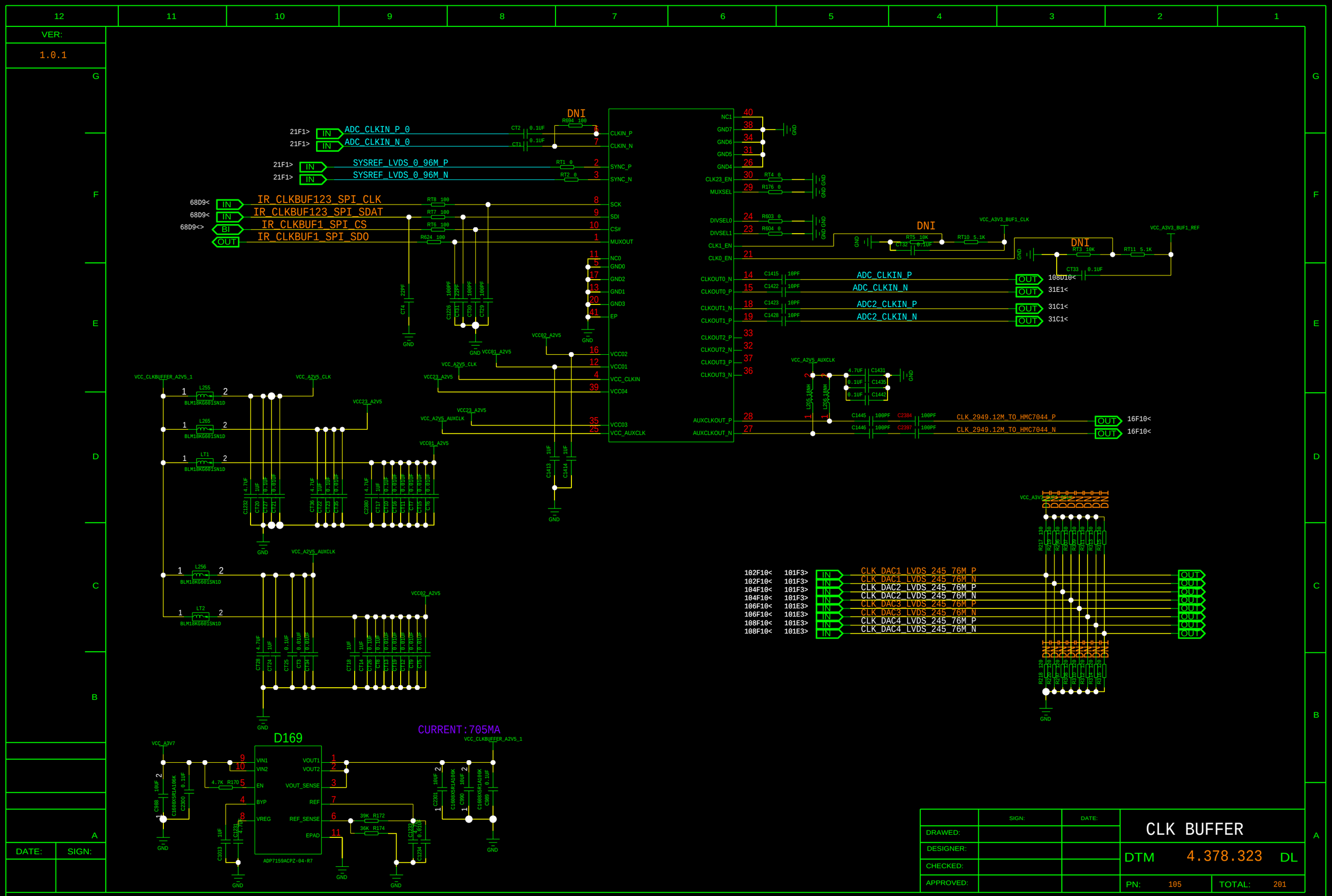
VER:
1.0.1
DNI
ADC_CLKIN_P_0
ADC_CLKIN_N_0
SYSREF_LVDS_0_96M_P
SYSREF_LVDS_0_96M_N
IR_CLKBUF123_SPI_CLK
IR_CLKBUF123_SPI_SDAT
IR_CLKBUF1_SPI_CS
IR_CLKBUF1_SPI_SDO
CLKIN_P
CLKIN_N
SYNC_P
SYNC_N
SCK
SDI
CS#
MUXOUT
NC0
GND0
GND2
GND1
GND3
EP
VCC02
VCC01
VCC_CLKIN
VCC04
VCC03
VCC_AUXCLK
NC1
GND7
GND6
GND5
GND4
CLK23_EN
MUXSEL
DIVSEL0
DIVSEL1
CLK1_EN
CLK0_EN
CLKOUT0_N
CLKOUT0_P
CLKOUT1_N
CLKOUT1_P
CLKOUT2_P
CLKOUT2_N
CLKOUT3_P
CLKOUT3_N
AUXCLKOUT_P
AUXCLKOUT_N
ADC_CLKIN_P
ADC_CLKIN_N
ADC2_CLKIN_P
ADC2_CLKIN_N
CLK_2949.12M_TO_HMC7044_P
CLK_2949.12M_TO_HMC7044_N
CLK_DAC1_LVDS_245_76M_P
CLK_DAC1_LVDS_245_76M_N
CLK_DAC2_LVDS_245_76M_P
CLK_DAC2_LVDS_245_76M_N
CLK_DAC3_LVDS_245_76M_P
CLK_DAC3_LVDS_245_76M_N
CLK_DAC4_LVDS_245_76M_P
CLK_DAC4_LVDS_245_76M_N
D169
ADP7159ACPZ-04-R7
CURRENT:705MA
VIN1
VIN2
EN
BYP
VREG
VOUT1
VOUT2
VOUT_SENSE
REF
REF_SENSE
EPAD
DATE:
SIGN:
DRAWED:
DESIGNER:
CHECKED:
APPROVED:
CLK BUFFER
DTM 4.378.323 DL
PN: 105
TOTAL: 201
FORMAT: III
SIZE:A3