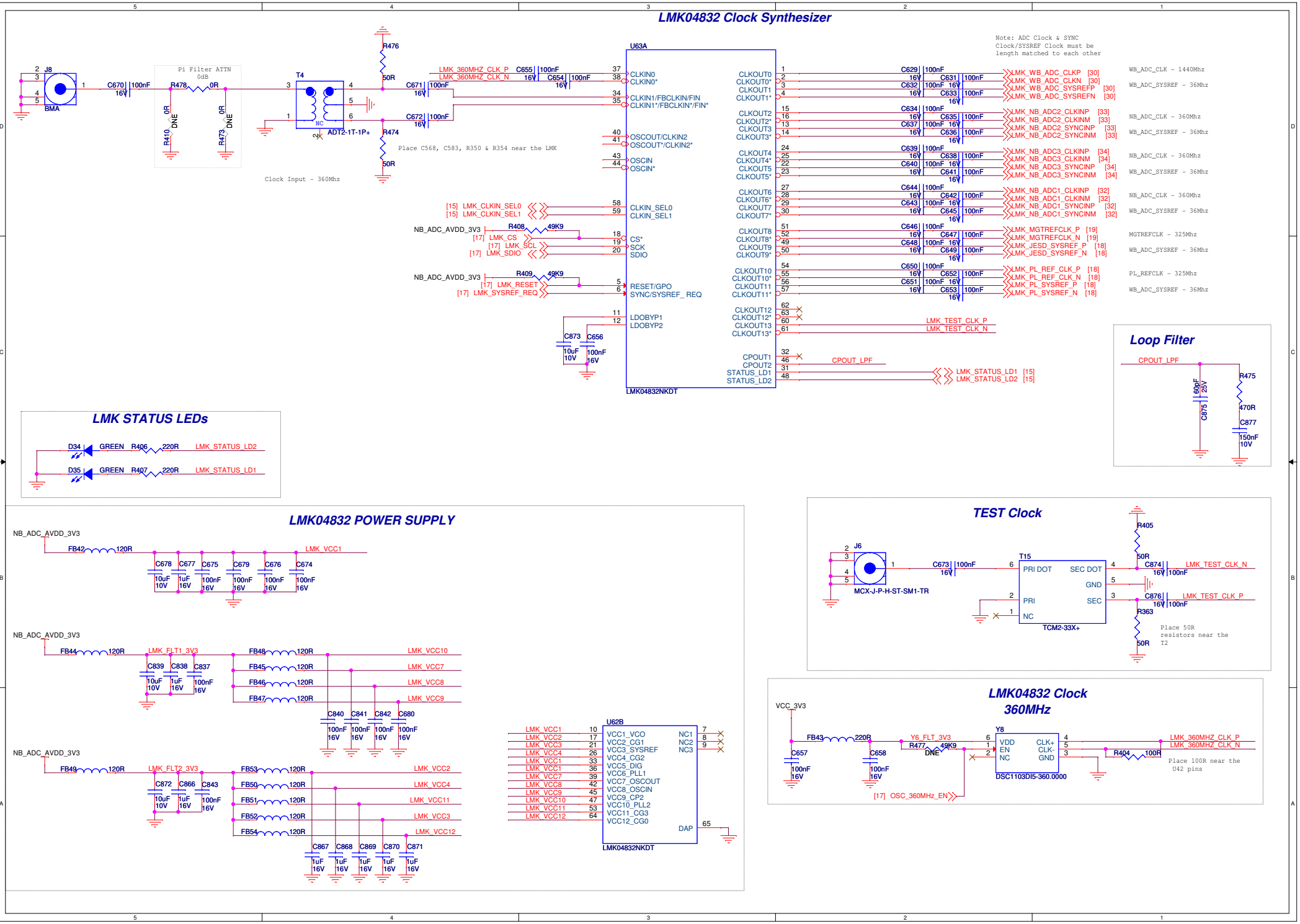
LMK04832 Clock Synthesizer
Note: ADC Clock & SYNC Clock/SYSREF Clock must be length matched to each other
J8
BMA
C670 100nF 16V
Pi Filter ATTN 0dB
R478 0R
R410 0R DNE
R473 0R DNE
T4
ADT2-1T-1P+
R476 50R
R474 50R
C671 100nF 16V
C672 100nF 16V
Clock Input - 360Mhz
LMK_360MHZ_CLK_P
LMK_360MHZ_CLK_N
C655 100nF 16V
C654 100nF 16V
Place C568, C583, R350 & R354 near the LMK
U63A
CLKIN0
CLKIN0*
CLKIN1/FBCLKIN/FIN
CLKIN1*/FBCLKIN*/FIN*
OSCOUT/CLKIN2
OSCOUT*/CLKIN2*
OSCIN
OSCIN*
CLKIN_SEL0
CLKIN_SEL1
CS*
SCK
SDIO
RESET/GPO
SYNC/SYSREF_REQ
LDOBYP1
LDOBYP2
CPOUT1
CPOUT2
STATUS_LD1
STATUS_LD2
LMK04832NKDT
[15] LMK_CLKIN_SEL0
[15] LMK_CLKIN_SEL1
NB_ADC_AVDD_3V3
R408 49K9
[17] LMK_CS
[17] LMK_SCL
[17] LMK_SDIO
R409 49K9
[17] LMK_RESET
[17] LMK_SYSREF_REQ
C873 10uF 10V
C656 100nF 16V
CLKOUT0
CLKOUT0*
CLKOUT1
CLKOUT1*
CLKOUT2
CLKOUT2*
CLKOUT3
CLKOUT3*
CLKOUT4
CLKOUT4*
CLKOUT5
CLKOUT5*
CLKOUT6
CLKOUT6*
CLKOUT7
CLKOUT7*
CLKOUT8
CLKOUT8*
CLKOUT9
CLKOUT9*
CLKOUT10
CLKOUT10*
CLKOUT11
CLKOUT11*
CLKOUT12
CLKOUT12*
CLKOUT13
CLKOUT13*
LMK_WB_ADC_CLKP [30]
LMK_WB_ADC_CLKN [30]
LMK_WB_ADC_SYSREFP [30]
LMK_WB_ADC_SYSREFN [30]
LMK_NB_ADC2_CLKINP [33]
LMK_NB_ADC2_CLKINM [33]
LMK_NB_ADC2_SYNCINP [33]
LMK_NB_ADC2_SYNCINM [33]
LMK_NB_ADC3_CLKINP [34]
LMK_NB_ADC3_CLKINM [34]
LMK_NB_ADC3_SYNCINP [34]
LMK_NB_ADC3_SYNCINM [34]
LMK_NB_ADC1_CLKINP [32]
LMK_NB_ADC1_CLKINM [32]
LMK_NB_ADC1_SYNCINP [32]
LMK_NB_ADC1_SYNCINM [32]
LMK_MGTREFCLK_P [19]
LMK_MGTREFCLK_N [19]
LMK_JESD_SYSREF_P [18]
LMK_JESD_SYSREF_N [18]
LMK_PL_REF_CLK_P [18]
LMK_PL_REF_CLK_N [18]
LMK_PL_SYSREF_P [18]
LMK_PL_SYSREF_N [18]
WB_ADC_CLK - 1440Mhz
WB_ADC_SYSREF - 36Mhz
NB_ADC_CLK - 360Mhz
WB_ADC_SYSREF - 36Mhz
NB_ADC_CLK - 360Mhz
WB_ADC_SYSREF - 36Mhz
NB_ADC_CLK - 360Mhz
WB_ADC_SYSREF - 36Mhz
MGTREFCLK - 325Mhz
WB_ADC_SYSREF - 36Mhz
PL_REFCLK - 325Mhz
WB_ADC_SYSREF - 36Mhz
LMK_TEST_CLK_P
LMK_TEST_CLK_N
CPOUT_LPF
LMK_STATUS_LD1 [15]
LMK_STATUS_LD2 [15]
Loop Filter
CPOUT_LPF
C875 680pF 25V
R475 470R
C877 150nF 10V
LMK STATUS LEDs
D34 GREEN R406 220R LMK_STATUS_LD2
D35 GREEN R407 220R LMK_STATUS_LD1
LMK04832 POWER SUPPLY
NB_ADC_AVDD_3V3
FB42 120R LMK_VCC1
C678 10uF 10V
C677 1uF 16V
C675 100nF 16V
C679 100nF 16V
C676 100nF 16V
C674 100nF 16V
FB44 120R LMK_FLT1_3V3
C839 10uF 10V
C838 1uF 16V
C837 100nF 16V
FB48 120R LMK_VCC10
FB45 120R LMK_VCC7
FB46 120R LMK_VCC8
FB47 120R LMK_VCC9
C840 100nF 16V
C841 100nF 16V
C842 100nF 16V
C680 100nF 16V
FB49 120R LMK_FLT2_3V3
C872 10uF 10V
C866 1uF 16V
C843 100nF 16V
FB53 120R LMK_VCC2
FB50 120R LMK_VCC4
FB51 120R LMK_VCC11
FB52 120R LMK_VCC3
FB54 120R LMK_VCC12
C867 1uF 16V
C868 1uF 16V
C869 1uF 16V
C870 1uF 16V
C871 1uF 16V
U62B
VCC1_VCO
VCC2_CG1
VCC3_SYSREF
VCC4_CG2
VCC5_DIG
VCC6_PLL1
VCC7_OSCOUT
VCC8_OSCIN
VCC9_CP2
VCC10_PLL2
VCC11_CG3
VCC12_CG0
NC1
NC2
NC3
DAP
LMK04832NKDT
TEST Clock
J6
MCX-J-P-H-ST-SM1-TR
C673 100nF 16V
T15
PRI DOT
SEC DOT
GND
PRI
SEC
NC
TCM2-33X+
R405 50R
C874 100nF 16V
C876 100nF 16V
R363 50R
Place 50R resistors near the T2
LMK04832 Clock 360MHz
VCC_3V3
FB43 220R
C657 100nF 16V
C658 100nF 16V
Y6_FLT_3V3
R477 49K9 DNE
[17] OSC_360MHz_EN
Y8
VDD
EN
NC
CLK+
CLK-
GND
DSC1103DI5-360.0000
R404 100R
Place 100R near the U42 pins