

LMK04610

User Controls

Raw Registers

Operating Modes

Generic

EVM

PLL1

OSCin OSCout

LOS

Holdover

PLL2

Outputs

GPIO Pin: SPI

GPIO Pin: Status

GPIO Pin: Sync

GPIO Pin: CLKInSEL

Tool: PLL1 Loop Filter

Tool: PLL2 Loop Filter

Tool: Frequency Planner

Burst Mode

Register Map

Register Name	Address/Value	2	2	2	2	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0
		3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5
R0	0x000000	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R1	0x000100	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R2	0x000200	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
R3	0x000346	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	0	0
R4	0x000438	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	0
R5	0x000503	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0
R6	0x000611	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	1
R7	0x000700	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	0
R8	0x000800	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
R9	0x000900	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
R10	0x000A00	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0
R11	0x000B00	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0	0
R12	0x000C51	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	1
R13	0x000D08	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1	0
R14	0x000E00	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0
R15	0x000F00	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	0
R16	0x00101E	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1
R17	0x001100	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
R18	0x001204	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0
R19	0x001310	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
R20	0x001480	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
R21	0x001508	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0
R22	0x001648	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1	0
R23	0x001700	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	0
R24	0x001800	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0
R25	0x001929	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0
R26	0x001A3A	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	1
R31	0x001F00	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	0
R32	0x002078	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R33	0x002100	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R34	0x002278	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R39	0x278204	0	0	1	0	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0
R40	0x280000	0	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R41	0x290001	0	0	1	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0	0
R42	0x2A0000	0	0	1	0	1	0	1	0	0	0	0	0	0	0	0	0	0	0	0
R43	0x2B0000	0	0	1	0	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0
R44	0x2C0000	0	0	1	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0
R45	0x2D0001	0	0	1	0	1	1	0	1	0	0	0	0	0	0	0	0	0	0	0
R46	0x2E10A3	0	0	1	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0
R47	0x2F00CF	0	0	1	0	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0
R48	0x3003FD	0	0	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R49	0x00310C	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R50	0x003200	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R51	0x003300	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R52	0x003463	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R53	0x003518	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
R54	0x003603	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

Data

0x

Write Register

Read Register

Read All Registers

Write All Registers

Import Register Map

Export Register Map

Register/Field Name

Name

Value

Read

Address Bits

Welcome to TICS Pro. Version -> 1.6.7.0, 04-Jan-18

Loading Device LMK04610...

Detected 0 USB2ANY interfaces

Wrote Register R0x56 as 0x00 560D

Wrote Register R0x56 as 0x00 560D

Wrote Register R0x1F as 0x00 1F00

Wrote Register R0x20 as 0x00 2078

Protocol: SPI_CLKLOW

Connection Mode: Device Not Connected



TEXAS INSTRUMENT