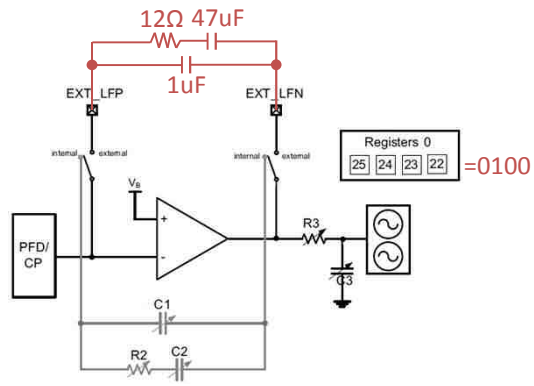
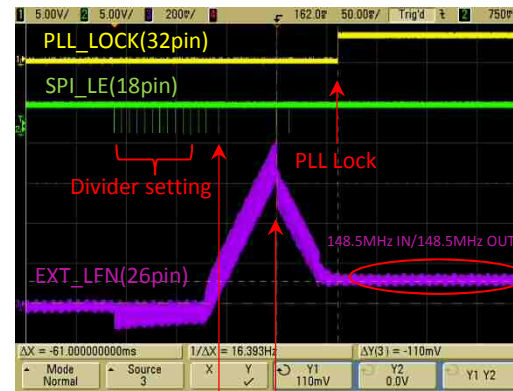


Loop BW=1.612kHz(WEBBENCH calculated)



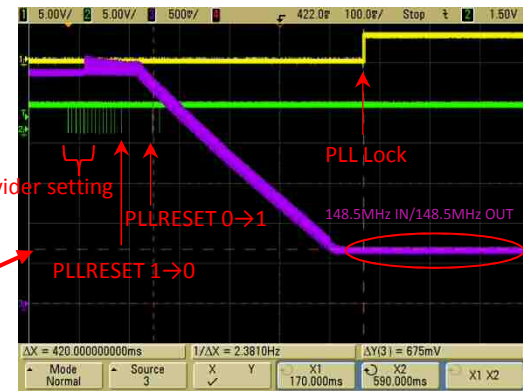
Case1



PLLRESET 0→1

PLLRESET 1→0

Case2



The ultimate divider setting is the same between Case1 and Case2.(148.5MHz In/ 148.5MHz Out)