

Figure 1-1. Phase Synchronization Concept

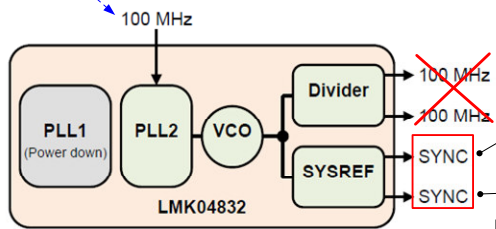


Figure 3-2. LMK04832 Configuration

When need to synchronize among two LMX2595,
 1) Should we use LMK04832(IC chip) to make "SYSREF" ?
 2) if yes, FPGA command needs three types such as LMK04832, LMX2595(1), and LMX2595(2). Is that correct?

