





LMX2581

File Keyboard Controls Select Device Options Mode LPT/USB Help

Port Setup Registers Bits/Pins BurstMode PLL

General Settings

- RESET
- FWDN\_MODE
- Powered Up
- MODE
- Full Synthesizer
- ZERO\_DELAY
- uWireLock

Channel Divider

- OUTA\_MUX
- Channel Divider
- OUTB\_MUX
- Channel Divider

Charge Pump

- CPG\_BLEED
- 0

FastLock

- FL\_CPG
- Tri-State
- FL\_TOC
- 0
- FL\_FRCE

Digital Lock Detect

- DLD\_TOL
- 18 ns
- DLD\_PASS\_CNT
- 32
- DLD\_ERR\_CNT
- 4

Fractional Settings

- FRAC\_DITHER
- Strong
- FRAC\_ORDER
- 2nd Order Modulator
- PFD\_DLY
- 370 ps

MUX/LD Pins

- LD\_PINMODE
- Push-Pull
- LD\_INV
- LD\_SELECT
- DLD & Vtune Good
- MUXOUT\_PINMODE
- Push-Pull
- MUX\_INV
- MUXOUT\_SELECT
- Readback Data
- FL\_INV
- FL\_SELECT
- GND
- FL\_PINMODE
- Tri-state

VCO Frequency Calibration

- NO\_FCAL
- VCO\_SEL
- VCO 3
- OSC\_FREQ
- <64 MHz
- VCO\_SEL\_MODE
- Start with VCO\_SEL Choir
- VCO\_CAPCODE
- 128
- VCO\_CAPCODE\_MAN

Output Buffer

- OUTA\_PD
- OUTB\_PD
- OUTA\_PWR
- 15
- OUTB\_PWR
- 15
- AUTO\_MUTE
- BUFEN\_DIS

Diagnostics

- ID
- RDADDR
- 6
- RD\_DIAGNOSTICS
- 0

Program Pins

- BUFEN
- CE
- TRIGGER

COMM Mode: USB Selected device: LMX2581

TEXAS INSTRUMENTS

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Export register values in hex to text file

MSB → 3 3 2 2 2 2 2 2 2 2 2 1 1 1 1 1 1 1 1 1 1 1 1 1 0 0 0 0 0 0 0 0 0 0  
 1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0 9 8 7 6 5 4 3 2 1 0

Register	Hex Value
R5 (INIT)	0x4087 0010
R15	0x021F E80F
R13	0x4082 C10D
R10	0x2100 50CA
R9	0x03C7 C039
R8	0x207D DBF8
R7	0x0008 2317
R6	0x0000 04C6
R5	0x0010 A805
R4	0x0000 0004
R3	0x2018 F3C3
R2	0x0FD0 9002
R1	0xC400 2011
R0	0x40A8 0000

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