

Code Loader

Device : LMK03328

TAB : Output

The screenshot displays the configuration interface for the LMK03328 device, specifically the Output tab. It shows settings for two outputs, OUT0 and OUT1. Red arrows with circled numbers point to specific settings:

- ①: Auto Mute (Enable)
- ②: DIFF
- ④: DIFF(LVDS)/CMOS(Hi-Z)
- ③: DIFF(LVDS)/CMOS(Hi-Z)
- ⑤: HCSL(ExtTerm)/CMOS(Hi-Z)

Output	Auto Mute	Mute Level	Output Format	Frequency
OUT0	Enable	DIFF(Vcm)/CMOS(V+/Vol)	DIFF(LVDS)/CMOS(Hi-Z)	233.3333 MHz
OUT1	Enable	DIFF(Vcm)/CMOS(V+/Vol)	HCSL(ExtTerm)/CMOS(Hi-Z)	233.3333 MHz

Please let me know that how to set output for LVDS format.