

# Hard fault exception

Montag, 14. Oktober 2019 09:27

Fault exception viewer

HardFault exception.

The processor has escalated a configurable-priority exception to HardFault.

An instruction executed with an invalid EPSR.T or EPSR.IT field (CFSR.INVSTATE).

Exception occurred at PC = 0x20001590, LR = 0x7525

See the call stack for more information.

Call Stack

➔ [ti\_sysbios\_family\_arm\_m3\_Hwi\_return + 0x1b]

ti\_sysbios\_family\_arm\_m3\_Hwi\_excHandlerAsm\_I:

➔ 0x1b4f8: 0xf01e 0x0f04 TST.W LR, #4

0x1b4fc: 0xbf14 ITE NE

0x1b4fe: 0xf3ef 0x8009 MRSNE R0, PSP

0x1b502: 0x4668 MOVEQ R0, SP

0x1b504: 0x4685 MOV SP, R0

0x1b506: 0xe92d 0x0ff0 PUSH.W {R4-R11}

0x1b50a: 0x4668 MOV R0, SP

0x1b50c: 0x4671 MOV R1, LR

0x1b50e: 0xf8df 0xf004 LDR.W PC, [PC, #0x4] ...

0x1b512: 0xbf00 NOP

0x1b514: 0x00007477 DC32 ti\_sysbios\_family\_...