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#if defined(CC26X2) SDK3.1 ble_user_config.c
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// Differential Output
// Tx Power Values (Pout, Tx Power)
const txPwrVal_t TxPowerTable[] =
{ { TX_POWER_MINUS_21_DBM, 0x06C7 },
  { TX_POWER_MINUS_18_DBM, 0x06C9 },
  { TX_POWER_MINUS_15_DBM, 0x0C88 },
  { TX_POWER_MINUS_12_DBM, 0x108A },
  { TX_POWER_MINUS_9_DBM, 0x0A8D },
  { TX_POWER_MINUS_6_DBM, 0x204D },
  { TX_POWER_MINUS_3_DBM, 0x2851 },
  { TX_POWER_0_DBM, 0x3459 },
  { TX_POWER_1_DBM, 0x385C },
  { TX_POWER_2_DBM, 0x440D },
  { TX_POWER_3_DBM, 0x5411 },
  { TX_POWER_4_DBM, 0x6C16 },
  { TX_POWER_5_DBM, 0x941E } };
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422
423 #if defined(CC26X2) SDK3.2 ble_user_config.c
424
425 // Differential Output
426 // Tx Power Values (Pout, Tx Power)
427 const txPwrVal_t TxPowerTable[] =
428 { {TX_POWER_MINUS_20_DBM, RF_TxPowerTable_DefaultPAEntry(6, 3, 0, 2) },
429   {TX_POWER_MINUS_15_DBM, RF_TxPowerTable_DefaultPAEntry(10, 3, 0, 3) },
430   {TX_POWER_MINUS_10_DBM, RF_TxPowerTable_DefaultPAEntry(15, 3, 0, 5) },
431   {TX_POWER_MINUS_5_DBM, RF_TxPowerTable_DefaultPAEntry(22, 3, 0, 9) },
432   {TX_POWER_0_DBM, RF_TxPowerTable_DefaultPAEntry(19, 1, 0, 20) },
433   {TX_POWER_1_DBM, RF_TxPowerTable_DefaultPAEntry(22, 1, 0, 20) },
434   {TX_POWER_2_DBM, RF_TxPowerTable_DefaultPAEntry(25, 1, 0, 25) },
435   {TX_POWER_3_DBM, RF_TxPowerTable_DefaultPAEntry(29, 1, 0, 28) },
436   {TX_POWER_4_DBM, RF_TxPowerTable_DefaultPAEntry(35, 1, 0, 39) },
437   {TX_POWER_5_DBM, RF_TxPowerTable_DefaultPAEntry(23, 0, 0, 57) } };
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44
45 // TX Power table SDK3.3 ble_user_config.c
46 // The RF_TxPowerTable_DEFAULT_PA_ENTRY and RF_TxPowerTable_HIGH_PA_ENTRY macros are defined in RF.h.
47 // The following arguments are required:
48 // RF_TxPowerTable_DEFAULT_PA_ENTRY(bias, gain, boost coefficient)
49 // RF_TxPowerTable_HIGH_PA_ENTRY(bias, ibboost, boost, coefficient, ldoTrim)
50 // See the Technical Reference Manual for further details about the "txPower" Command field.
51 // The PA settings require the CCFG FORCE_VDDR_HH = 0 unless stated otherwise.
52 RF_TxPowerTable_Entry RF_BLE_TxPowerTable[RF_BLE_TX_POWER_TABLE_SIZE] =
53 {
54   {-20, RF_TxPowerTable_DEFAULT_PA_ENTRY(6, 3, 0, 2) },
55   {-15, RF_TxPowerTable_DEFAULT_PA_ENTRY(10, 3, 0, 3) },
56   {-10, RF_TxPowerTable_DEFAULT_PA_ENTRY(15, 3, 0, 5) },
57   {-5, RF_TxPowerTable_DEFAULT_PA_ENTRY(22, 3, 0, 9) },
58   {0, RF_TxPowerTable_DEFAULT_PA_ENTRY(19, 1, 0, 20) },
59   {1, RF_TxPowerTable_DEFAULT_PA_ENTRY(22, 1, 0, 20) },
60   {2, RF_TxPowerTable_DEFAULT_PA_ENTRY(25, 1, 0, 25) },
61   {3, RF_TxPowerTable_DEFAULT_PA_ENTRY(29, 1, 0, 28) },
62   {4, RF_TxPowerTable_DEFAULT_PA_ENTRY(35, 1, 0, 39) },
63   {5, RF_TxPowerTable_DEFAULT_PA_ENTRY(23, 0, 0, 57) },
64   RF_TxPowerTable_TERMINATION_ENTRY
65 };
```