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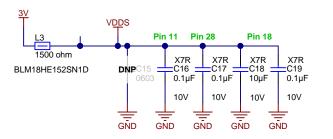
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Number: TIDA-00757 | Rev: E1 | Sheet Title: Schematic
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Drawn By: Aramis P. Alvarez | File: TIDA-00757 Smart-Lock Sheet1.SchDoc | Size: B

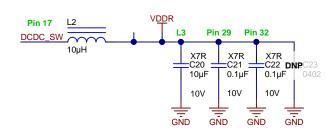
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LPRF Bypass Capacitors & DC-DC Passives



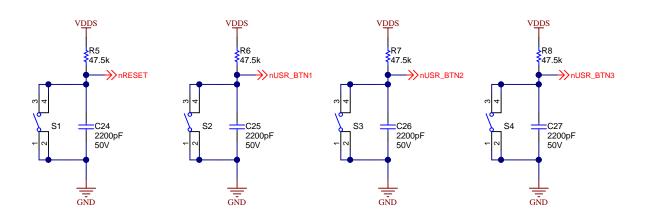


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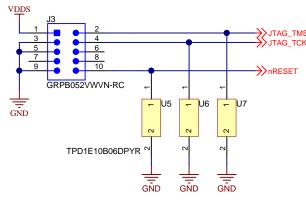
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Reset Switch and User Buttons



JTAG Programming Interface





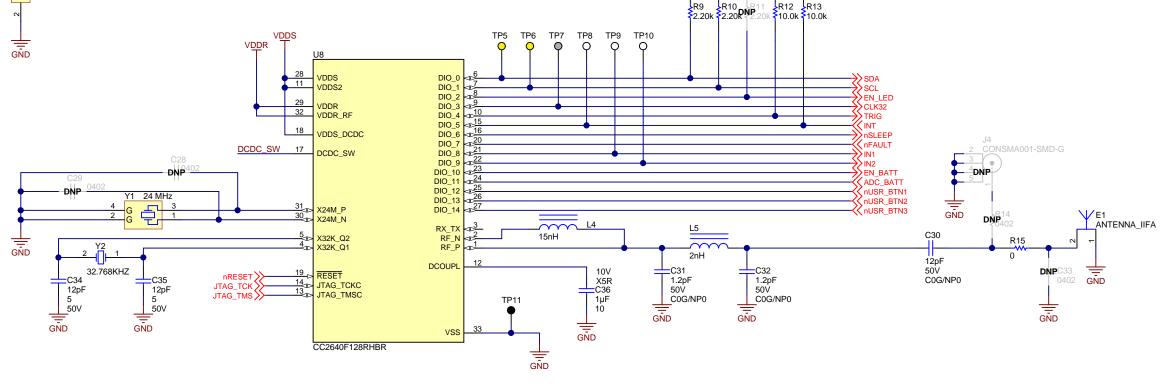
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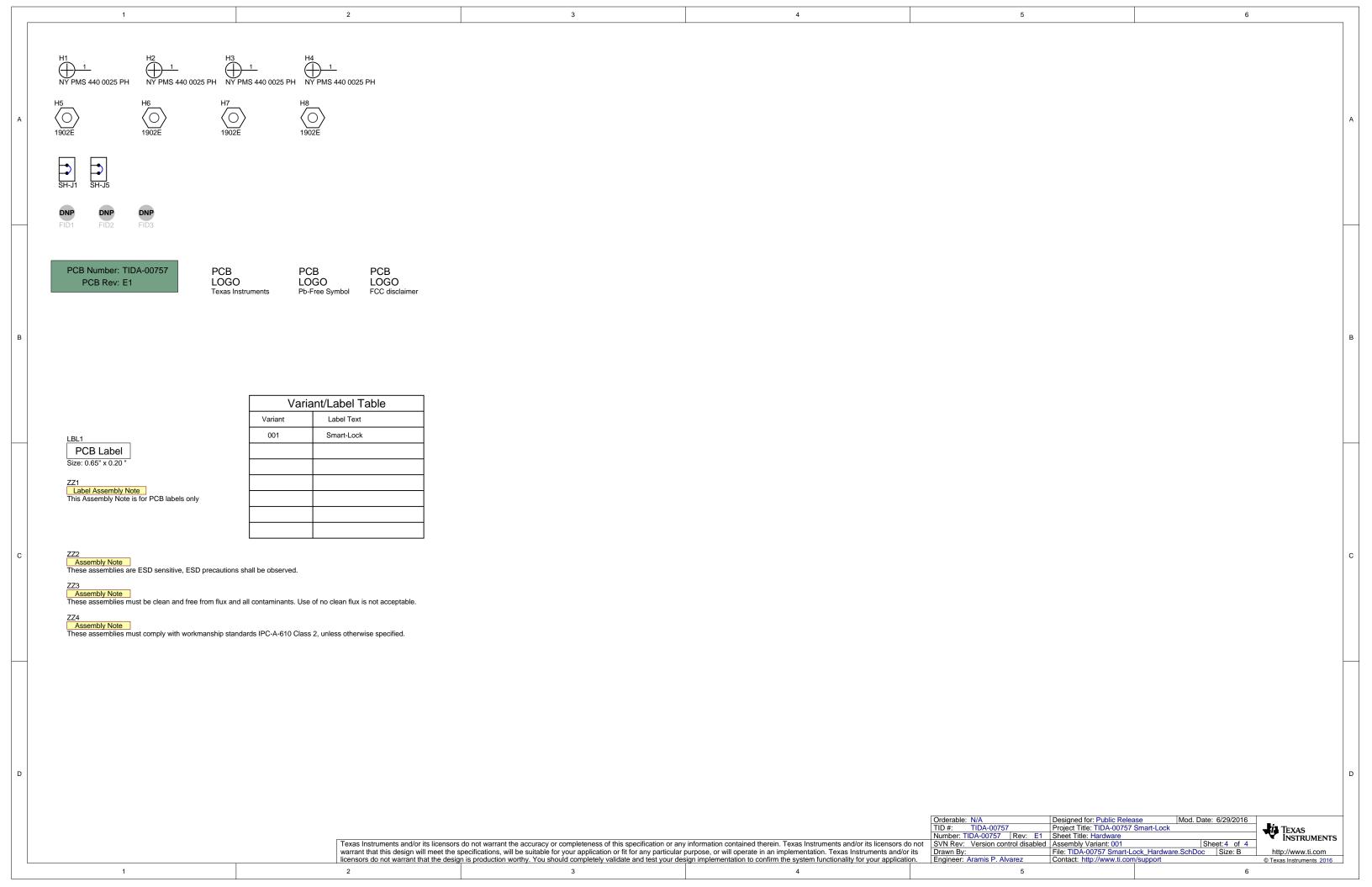
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 Project Title: TIDA-007

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