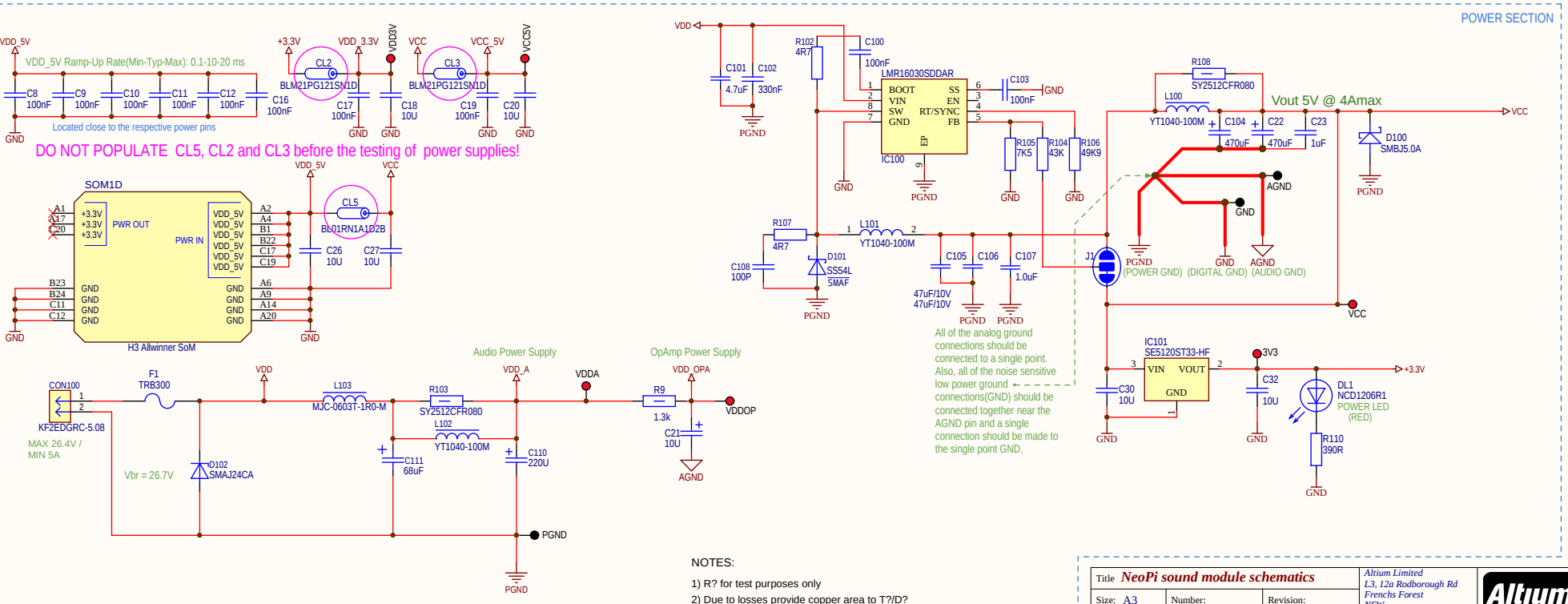
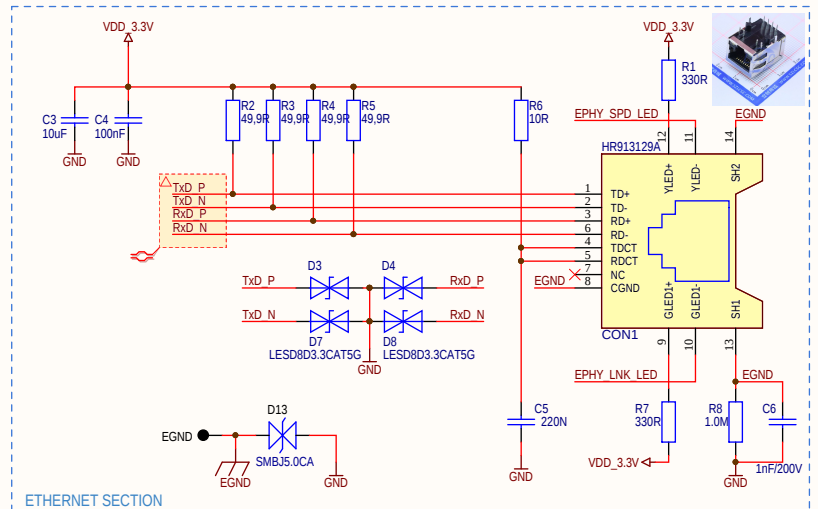
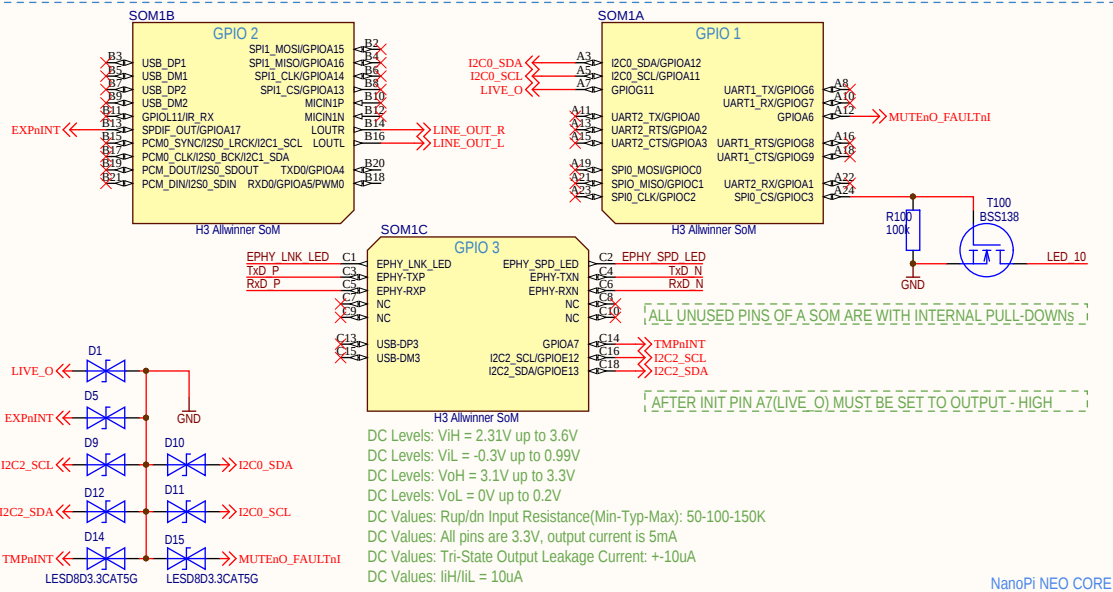


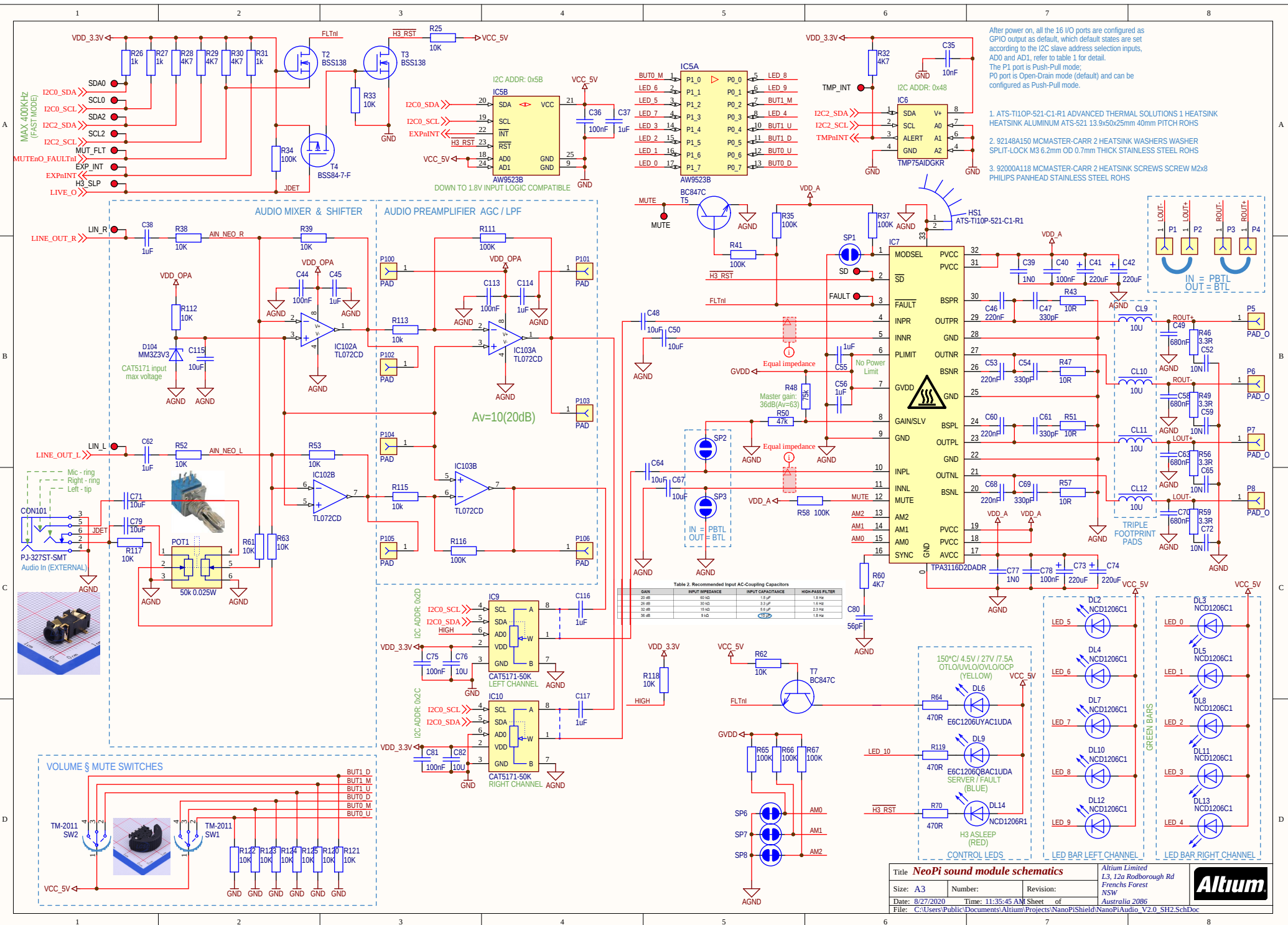
Revision History

Revision	Notes
1.0	* First release
2.0	* Removing audio noise



Title NeoPi sound module schematics			Altium Limited L3, 12a Rodborough Rd Frenchs Forest NSW Australia 2086	
Size: A3	Number:	Revision:		
Date: 8/27/2020	Time: 11:35:43 AM	Sheet of		
File: C:\Users\Public\Documents\Altium\Projects\NanoPiShield\NanoPiAudio_V2.0_SH1.SchDoc				

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- After power on, all the 16 I/O ports are configured as GPIO output as default, which default states are set according to the I2C slave address selection inputs, ADO and AD1, refer to table 1 for detail. The P1 port is Push-Pull mode; P0 port is Open-Drain mode (default) and can be configured as Push-Pull mode.
1. TS-T10P-521-C1-R1 ADVANCED THERMAL SOLUTIONS 1 HEATSINK HEATSINK ALUMINUM ATS-521 13.9x50x25mm 40mm PITCH ROHS
 2. 92148A150 MCMaster-CARR 2 HEATSINK WASHERS WASHER SPLIT-LUCK M3 6.2mm OD 0.7mm THICK STAINLESS STEEL ROHS
 3. 92000A118 MCMaster-CARR 2 HEATSINK SCREWS SCREW M2x8 PHILIPS PAN-HEAD STAINLESS STEEL ROHS

Table 2. Recommended Input AC-Coupling Capacitors

GAIN	INPUT IMPEDANCE	INPUT CAPACITANCE	HIGH-PASS FILTER
20 dB	10 kΩ	1.0 μF	1.0 nF
20 dB	10 kΩ	3.3 μF	1.0 nF
30 dB	10 kΩ	3.3 μF	2.2 nF
30 dB	10 kΩ	3.3 μF	1.0 nF