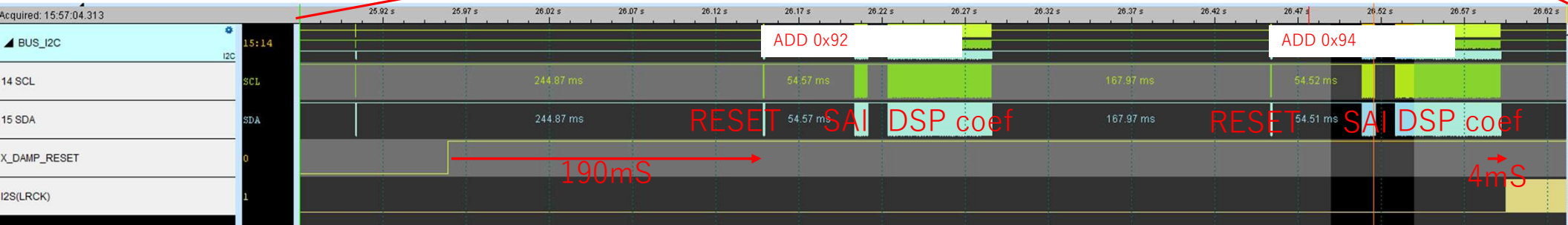
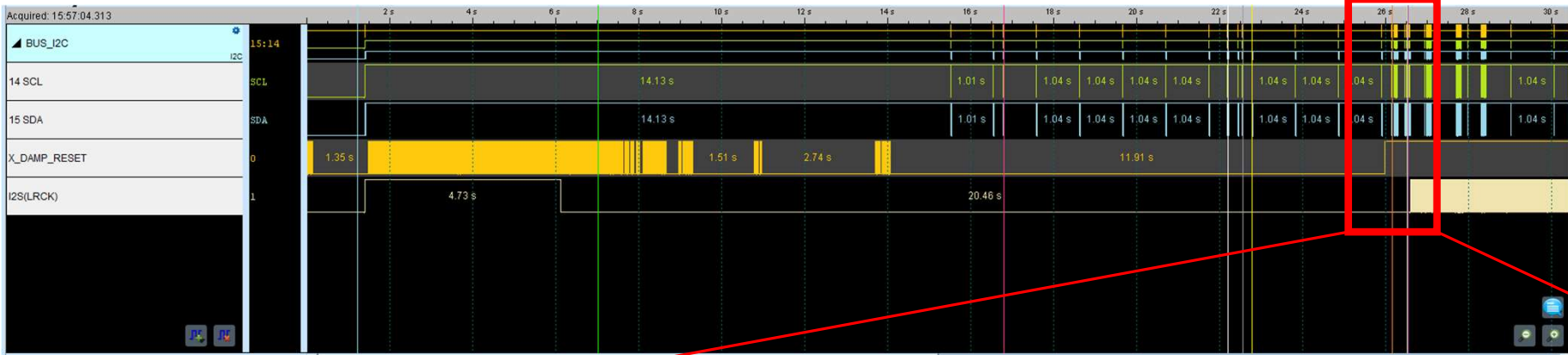


Revised Sequence



RESET to I2C 190mSec
 I2C DSP coef finish to I2S 4mSec