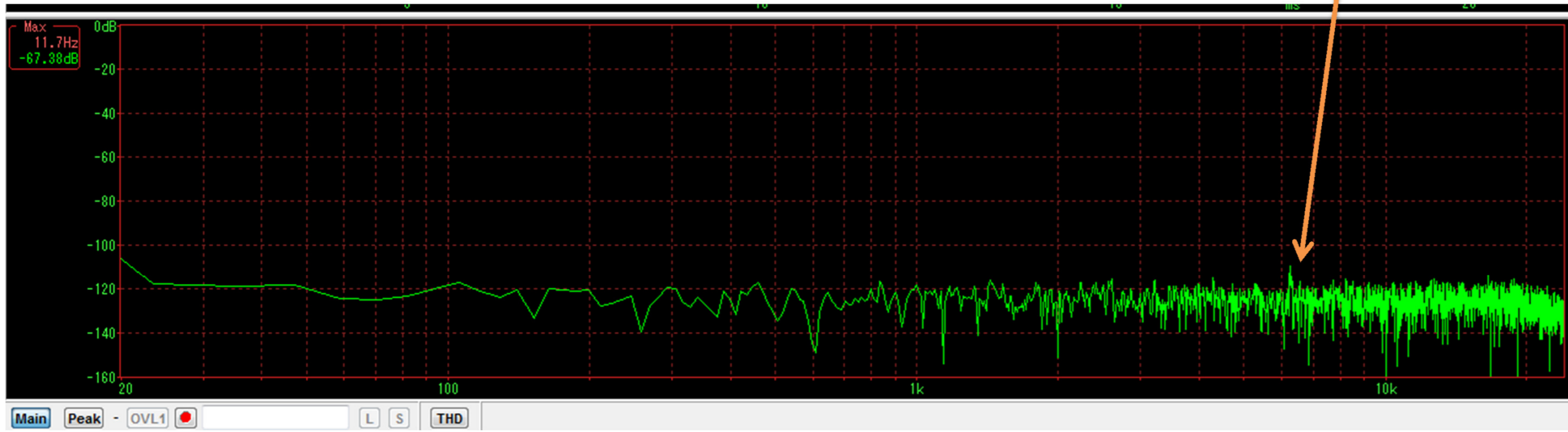
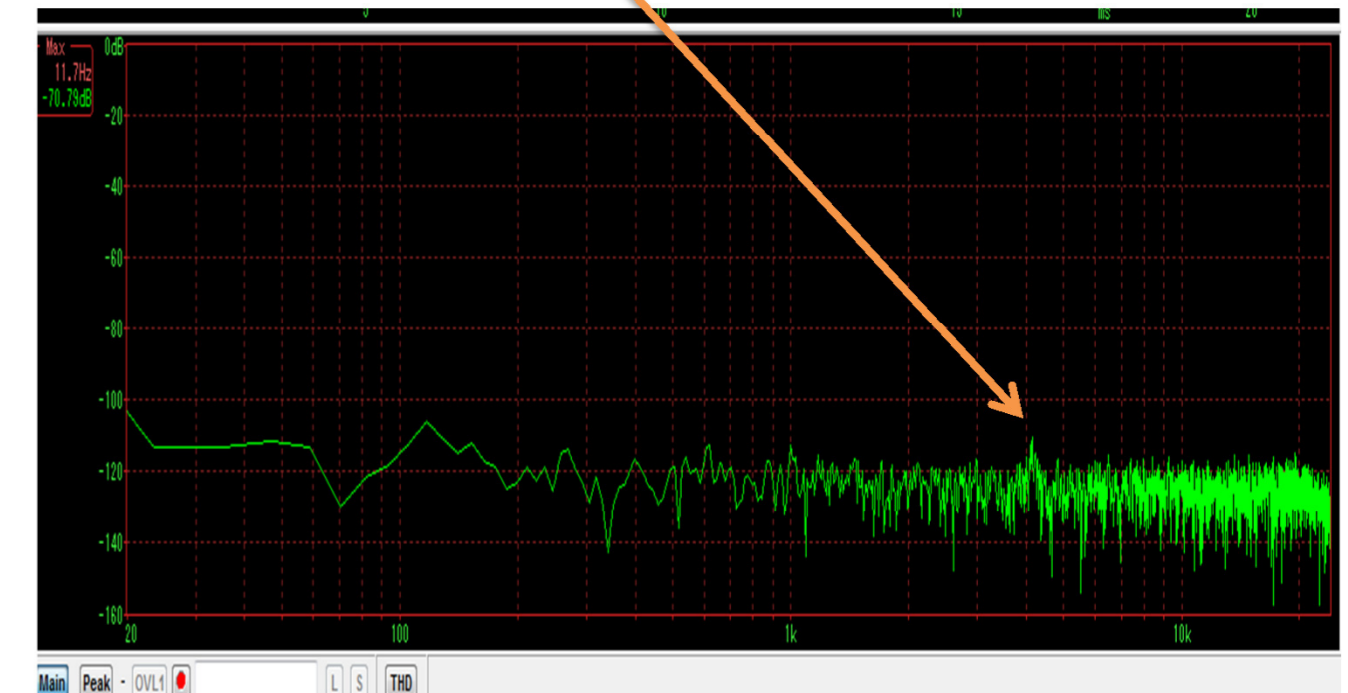
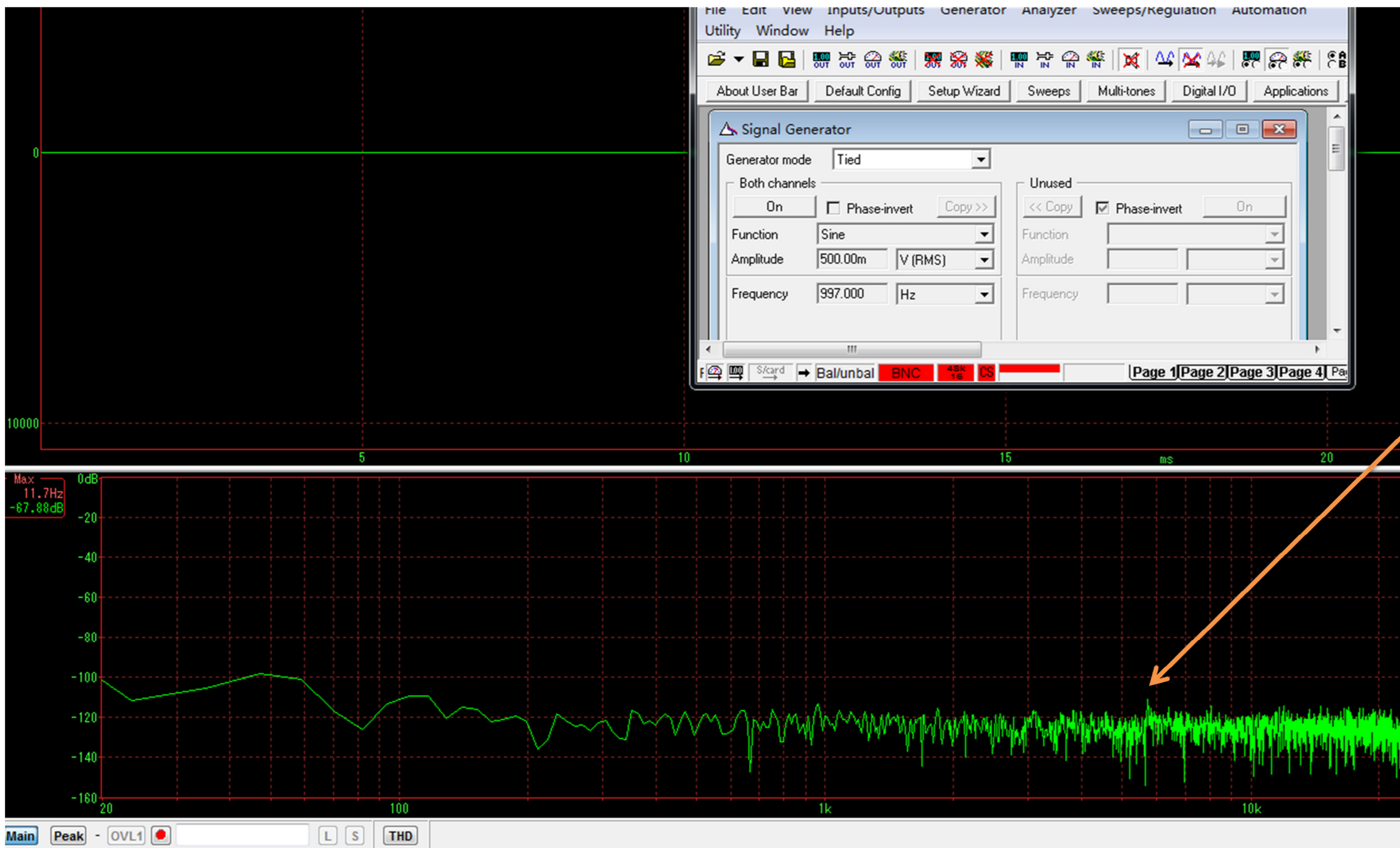


Please remove capacitors from pin 8 and 11.
Please place 22 ohm series termination on MCLK,DOUT,BCLK and LRCK. Is device operated as a slave? please send me the details of your timing.
It looks that there are long connections on input signal paths.These need to be minimized
Please set up the board as single ended. Please a 2.2u from pin 13 to ground very close to the IC pins. apply a sine wave of 1Khz 0.5v though 2.2u capacitor on pin 12.
Keep PGA Gain =0db
Check FFT.

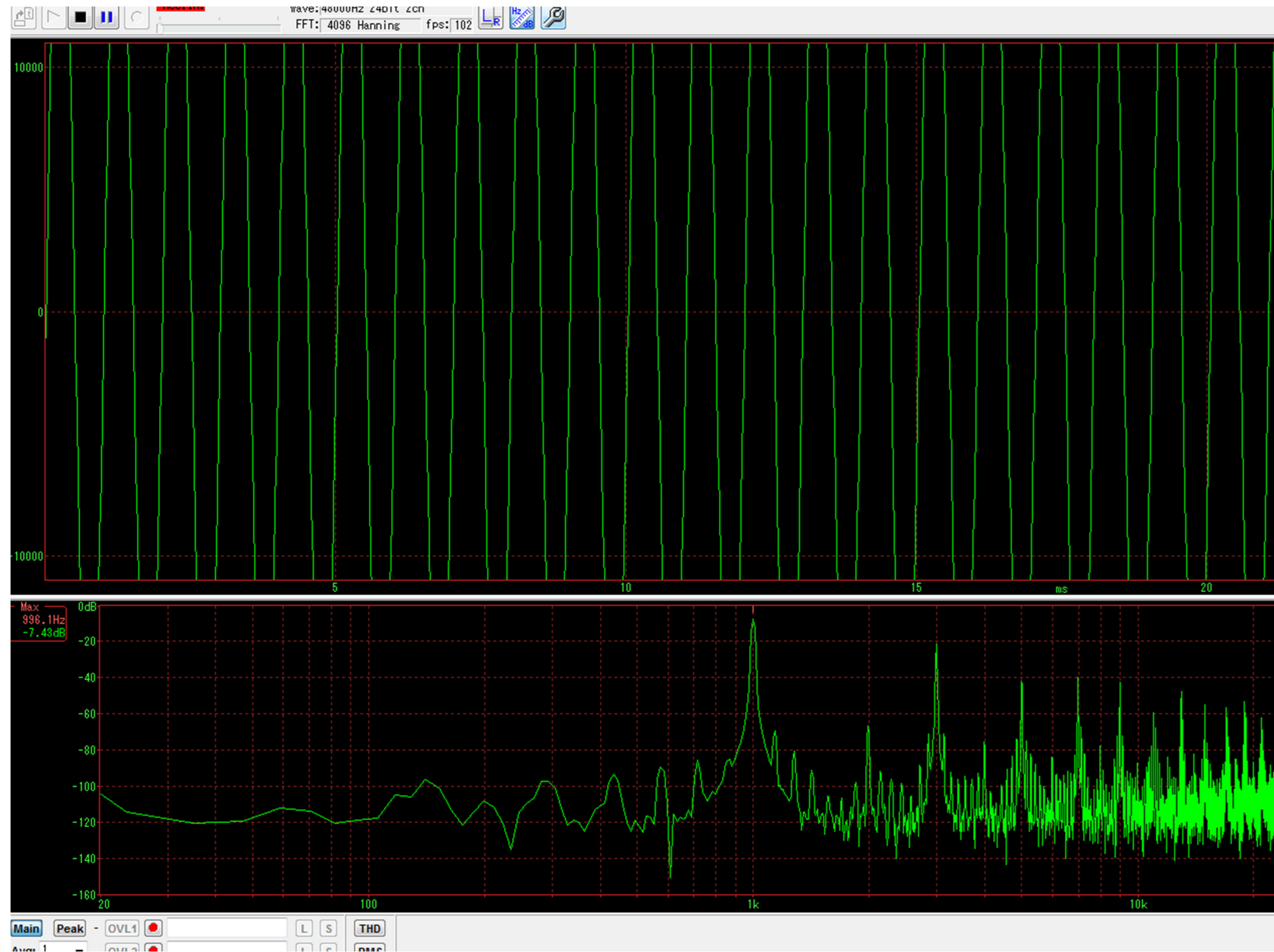
Before change



After change : Pin12 In = 0.5V 1KHZ OFF (Has the FFT Noise)



After change : Pin12 In = 0.5V 1KHZ ON



After change : Pin12 In = 0.25V 1KHZ ON

