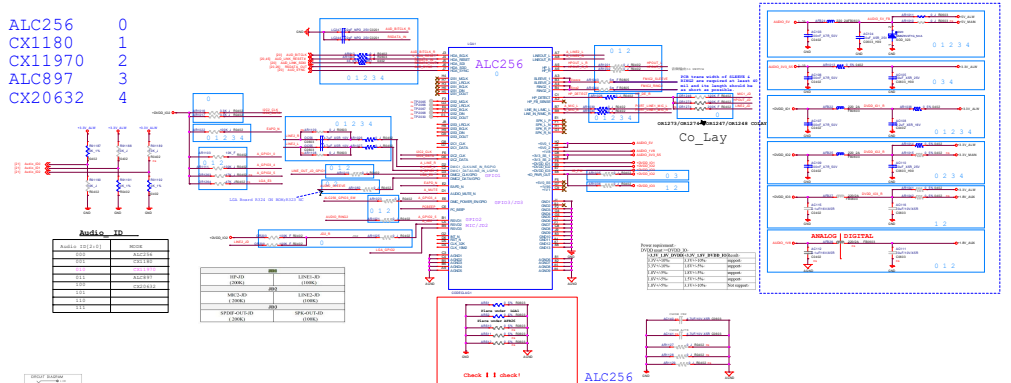


- ALC256 0
- CX1180 1
- CX11970 2
- ALC897 3
- CX20632 4



Audio_ID

Audio_ID	MODE
000	STEREO
001	STEREO
011	4CH2CH
010	STEREO
100	STEREO
101	STEREO
110	STEREO
111	STEREO

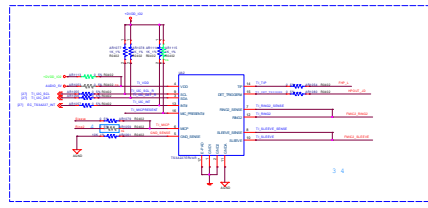
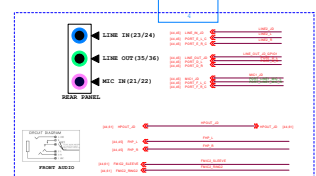
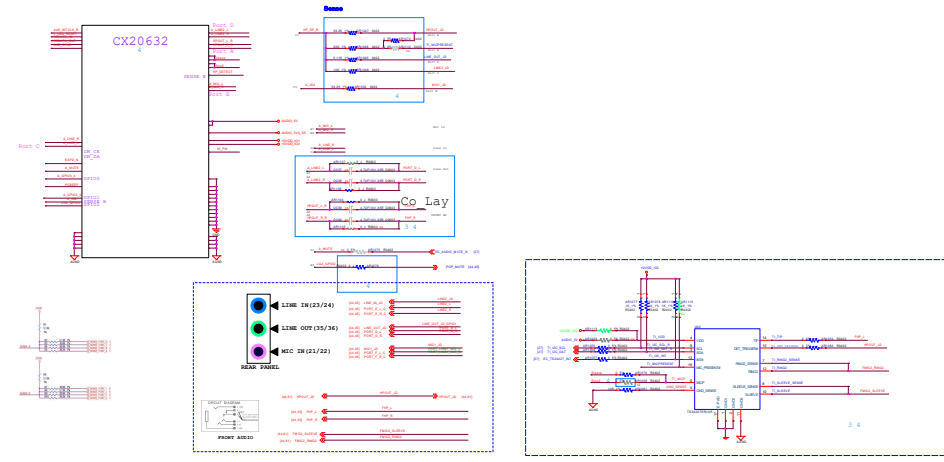
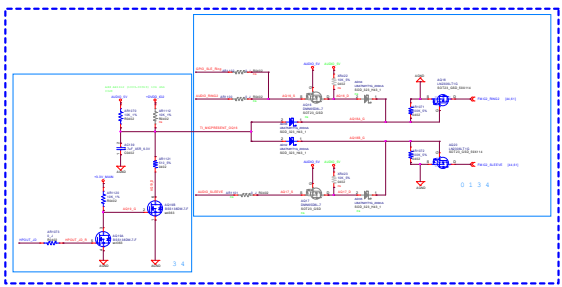
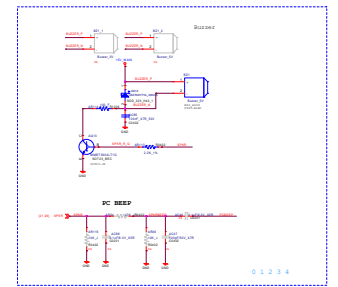
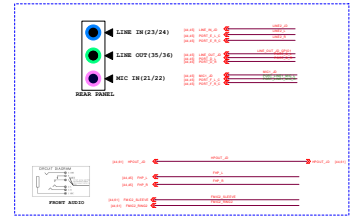
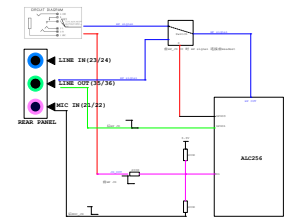
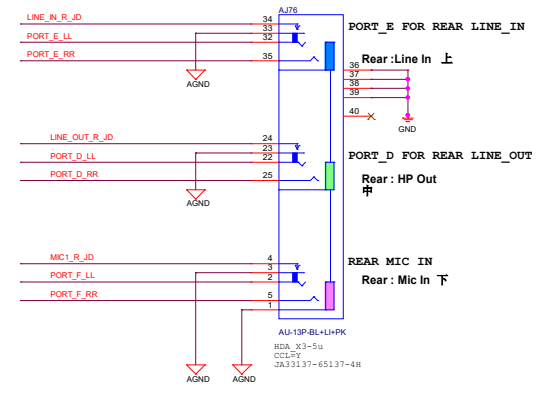
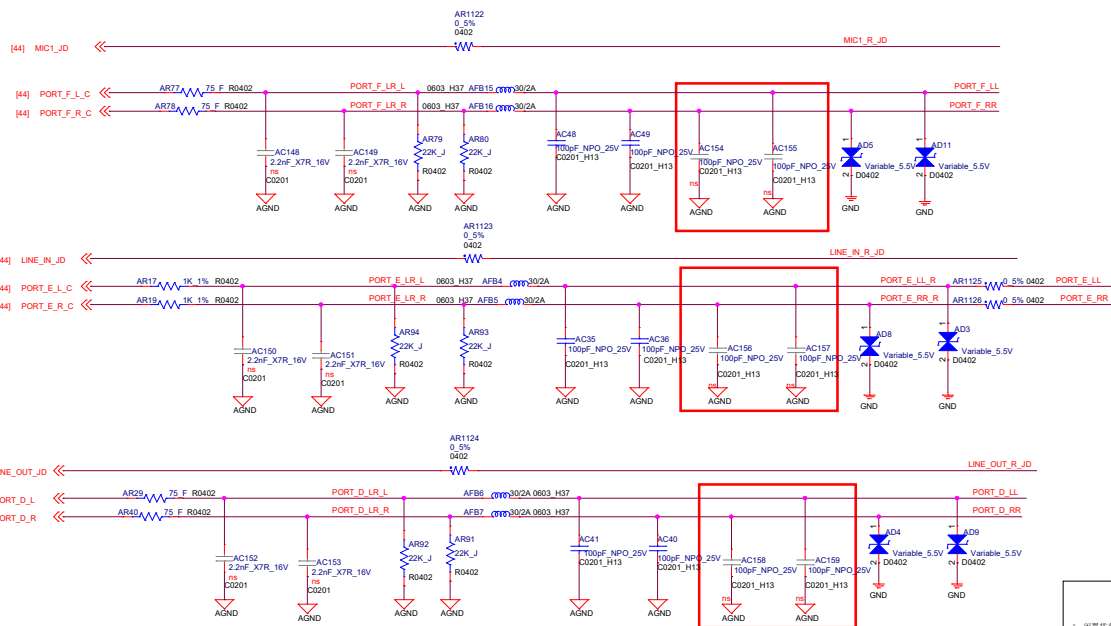


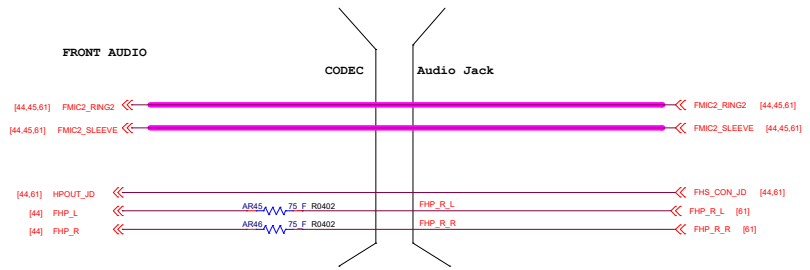
Table 1. Jack Detection Resistor Matrix

	J0
FRONT-0D R1(20K)	SURR-0D R3(10K)
MIC1-0D R1(20K)	J02
MIC2-0D R1(20K)	J03
LINE1-0D R1(20K)	J04

45 AUDIO IO



状态	耳机制式	GPIO A 控制sleeve	GPIO B控制Ring
1. 闲置状态: 耳机插入耳机, 耳机插孔保持 检测到(经分压器)各脚设置各脚输入脚的大概值)		L	L
2. 插入耳机到线序检测完成阶段: 耳机插入耳机, 耳机插孔保持 检测到(经分压器)各脚设置各脚输入脚的大概值)	默认线序	L延时100ms 状态变为H	L
3. 线序检测完成或工作完成: 检测检测到的信号并输出到... 如果检测到信号, 它不能检测到...的开关。 同步把...的引脚也打开。 反之亦然)	双耳	L	H
	单耳	H	L
	三段式	L	L
4. 拔出耳机恢复闲置状态:		L	L



De-pop circuit

