

Frameworks

Overview

The first component of any process flow is the Framework. Currently there are 3 available Frameworks, 8x4x, 4x2x, 2x1x. Each Framework supports a specific range of sampling rates and instruction cycle capabilities through a specified set of device register settings. For this reason, each Framework must be partnered with a matching set of interpolators, decimators, and inter-processor communication components.

Framework	Sample Rates Supported (Fs) Ksps	EVM Default Sample Rate (Using EVM on-board clock/PLL) Ksps	Matching Components	Description
PCM3070 8x4x	48, 44.1, 32, 24, 16, 8	44.1	Dec4xIn, Int8xOut, DSP_D_DSP_A DSP_A_DSP_D	8x Interpolation, 4x Decimation
PCM3070 4x2x	96, 88.2	88.2	Dec2xIn, Int4xOut, DSP_D_DSP_A DSP_A_DSP_D	4x Interpolation, 2x Decimation
PCM3070 2x1x	192, 176.4	176.4	Dec1xIn, Int2xOut, DSP_D_DSP_A DSP_A_DSP_D	2x Interpolation, 1x Decimation

Note: Changing the CurrentRate property in the GDE affects only the calculation of coefficient values. It does not modify the sampling rate on the EVM. In order to modify the EVM's sampling rate, the firmware on the EVM must be reprogrammed and the EVM re-enumerated on the USB. See the online help Reference Information section for information on changing the EVM sampling rate.

Configurable Properties

Property	Description
None	

I2C Interface

Property	Description
None	