

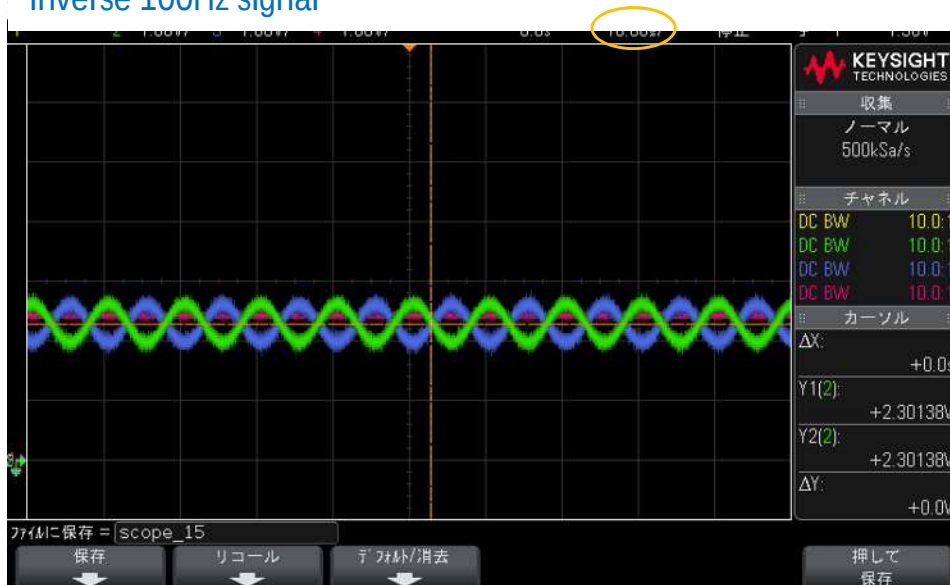
PCM1681 Bi-Pore Zero noise

1. Unstable BPZ level output at Vout5



Blue: Vout5 pin20
Magenta: Vcom pin25
Green: Vout6 pin19

2. Same device but correct function by chance, Vout5 and Vout6 are inputted inverse 100Hz signal



Blue: Vout5 pin20
Magenta: Vcom pin25
Green: Vout6 pin19

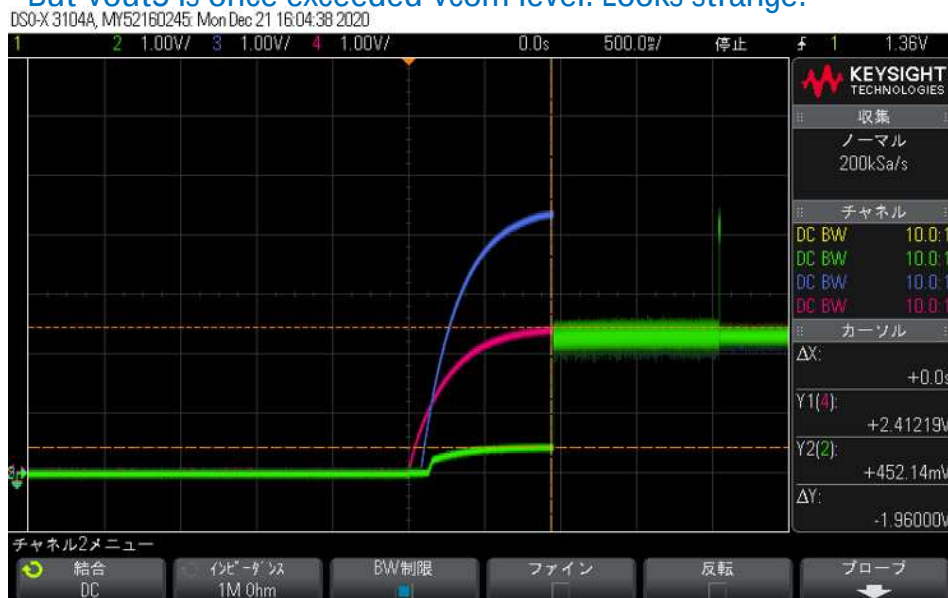
Power on transition

3. Vout5 is already unstable noisy and it jump up over Vcom.
No matter what is I2S DATA value. It seems analog bias circuit is damaged.



Blue: Vout5 pin20
Magenta: Vcom pin25
Green: Vout6 pin19

4. Correct function correspond pic.2 condition.
But Vout5 is once exceeded Vcom level. Looks strange.



Blue: Vout5 pin20
Magenta: Vcom pin25
Green: Vout6 pin19

Power supply rump up

5. It is general information. Capture Vdd, Vcc, Vcom and ch output



Yellow: Vdd pin9
Blue: Vout5 pin20
Magenta: Vcom pin25
Green: Vcc1 pin17

6. Zoom up



Yellow: Vdd pin9
Blue: Vout5 pin20
Magenta: Vcom pin25
Green: Vcc1 pin17