

U70000

- ACN\_GPIO\_00 ~~V26~~
- ACN\_GPIO\_01 ~~V25~~
- ACN\_GPIO\_02 ~~V27~~
- ACN\_GPIO\_03 ~~Y28~~
- ACN\_GPIO\_04 ~~R26~~
- ACN\_GPIO\_05 ~~T27~~
- ACN\_GPIO\_06 ~~P26~~
- ACN\_GPIO\_07 ~~M25~~
- ACN\_GPIO\_08 ~~ZX28~~
- ACN\_GPIO\_09 ~~AB28~~
- ACN\_GPIO\_10 ~~Y28~~
- ACN\_GPIO\_11 ~~T25~~
- ACN\_GPIO\_12 ~~U27~~
- ACN\_GPIO\_13 ~~AX28~~
- ACN\_GPIO\_14 ~~R28~~
- ACN\_GPIO\_15 ~~B25~~
- ACN\_GPIO\_16 ~~T27~~
- ACN\_GPIO\_17 ~~U26~~
- ACN\_GPIO\_18 ~~Y24~~
- ACN\_GPIO\_19 ~~AA27~~
- ACN\_GPIO\_20 ~~P27~~
- ACN\_GPIO\_21 ~~V27~~
- ACN\_GPIO\_22 ~~W27~~
- ACN\_GPIO\_23 ~~AX27~~
- ACN\_GPIO\_24 ~~V25~~
- ACN\_GPIO\_25 ~~V28~~
- ACN\_GPIO\_26 ~~V25~~
- ACN\_GPIO\_27 ~~W26~~
- STB\_PVTMON\_ADC ~~AF28~~
- STB\_PVTMON\_DAC ~~AF27~~
- CPU\_PVTMON\_DAC ~~AG27~~
- CPU\_PVTMON\_ADC ~~AG28~~

W24

ACN\_IR\_IN0

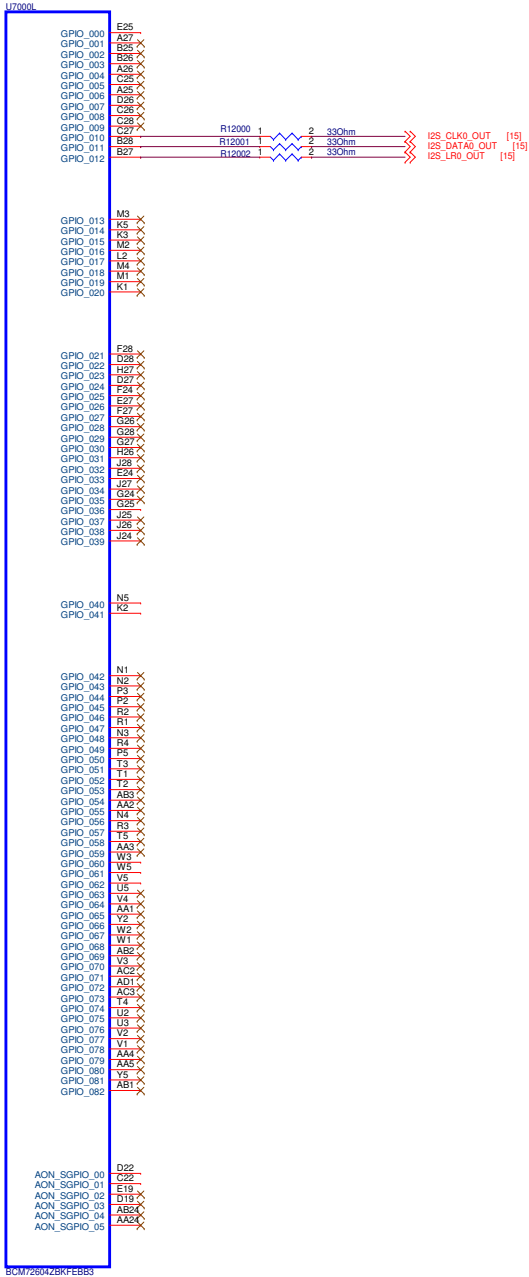
BCM128042BRFE883



# AON\_GPIO & GPIO PORTS CONFIGURED FOR 72604 SDIO (MULTI) + MTSIF (MULTI) + I2S + SC + SPI-M

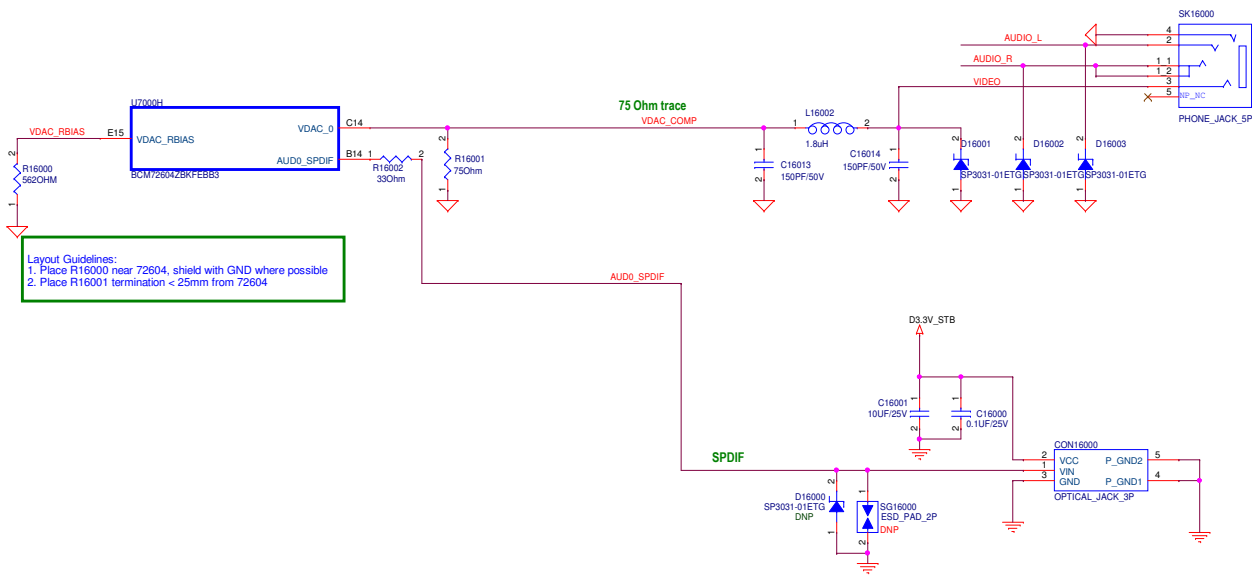
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GPIO_000/Strap/ENET0_RGMII_MDC_ALT / RSV0_2 / SD_CARD1_1_LED / PWM2 / EXT_USB_HUB_CLK / TSIO_VCTRL / ENET0_LINK_ALT2
GPIO_001/ENET0_RGMII_RX_CLK_ALT / PKT_CLK0_ALT2 / SD_CARD1_1_CMD / MTSIF1_CLK / SC1_VCC / SPI_M_MOSI / UART_RXD_1
GPIO_002/ENET0_RGMII_RX_EN_CTL_ALT / PKT_DATA0_ALT2 / SD_CARD1_1_CLK / MTSIF1_DATA0 / SC1_CLK / SPI_M_SS0b / UART_TXD_1
GPIO_003/ENET0_RGMII_RXD_00_ALT / PKT_SYNC0_ALT2 / SD_CARD1_1_DAT0 / MTSIF1_SYNC / SC1_RST / SPI_M_SCK / UART_CTS_1
GPIO_004/ENET0_RGMII_RXD_01_ALT / PKT_CLK2_ALT2 / SD_CARD1_1_DAT1 / MTSIF1_DATA3 / SC1_IO / SPI_M_MISO / UART_RTS_1
GPIO_005/ENET0_RGMII_RXD_02_ALT / PKT_CLK1_ALT2 / SD_CARD1_1_DAT2 / MTSIF1_DATA5 / SC1_PRES / SPI_M_SS1b / UART_RXD_2 / I2S_ST_LR0_IN
GPIO_006/ENET0_RGMII_RXD_03_ALT / PKT_CLK1_ALT2 / SD_CARD1_1_DAT3 / MTSIF1_DATA1 / SC1_AUX1 / CPU_TRACE_DATA1 / UART_TXD_2 / I2S_ST_LR0_IN
GPIO_007/ENET0_RGMII_TX_CLK_ALT / PKT_DATA1_ALT2 / SD_CARD1_1_CLK_IN / RSV0_4 / SC1_AUX2 / AUD_FS_CLK0 / UART_CTS_2 / I2S_MR_SCLK0_OUT
GPIO_008/ENET0_RGMII_TX_EN_CTL_ALT / PKT_SYNC1_ALT2 / SD_CARD1_1_PRES / MTSIF1_DATA2 / SC1_VPP / RMX_VALID1 / UART_RTS_2 / I2S_MR_LR0_OUT
GPIO_009/ENET0_RGMII_TXD_00_ALT / PKT_SYNC2_ALT2 / SD_CARD1_1_WPROT / MTSIF1_DATA5 / AUD_FS_CLK1 / RMX_PAUSE / RMX_CLK0 / I2S_MR_MSCLK_OUT
GPIO_010/ENET0_RGMII_TXD_01_ALT / PKT_DATA2_ALT2 / SD_CARD1_1_PWRO / MTSIF1_DATA4 / I2S_CLK1_OUT / RMX_CLK1 / RMX_DATA0 / I2S_CLK0_OUT
GPIO_011/ENET0_RGMII_TXD_02_ALT / PKT_DATA3_ALT / SD_CARD1_1_VOLT / MTSIF1_DATA7 / I2S_DATA1_OUT / RMX_DATA1 / RMX_SYNC0 / I2S_DATA0_OUT
GPIO_012/ENET0_RGMII_TXD_03_ALT / PKT_SYNC3_ALT / ENET0_RGMII_IRQ / RSV0_4 / I2S_LR1_OUT / RMX_SYNC1 / RMX_VALID0 / I2S_LR0_OUT
GPIO_013/SC0_VCC / TEST_CHIP / UART_RXD_1 / CPU_TRACE_DATA12 / PKT_CLK3 / SPI_M_MOSI / RMX_CLK0 / LED_LD_8
GPIO_014/SC0_CLK / EXT_IRQ0b_0 / UART_TXD_1 / CPU_TRACE_DATA13 / PKT_DATA3 / SPI_M_SS0b / RMX_DATA0 / LED_LD_9
GPIO_015/SC0_RST / EXT_IRQ0b_1 / UART_RTS_1 / CPU_TRACE_DATA14 / PKT_SYNC3 / SPI_M_SCK / RMX_SYNC0 / LED_LD_10
GPIO_016/SC0_IO / IR_IN1 / UART_CTS_1 / CPU_TRACE_DATA15 / PKT_VALID3 / SPI_M_MISO / RMX_VALID0 / LED_LD_11
GPIO_017/SC0_PRES / EXT_IRQ0b_2 / ENET0_RGMII_RX_OK_ALT / AUD_FS_CLK0 / PKT_ERROR3 / SPI_M_SS1b / RMX_PAUSE0 / LED_LD_12
GPIO_018/SC0_AUX1 / I2S_CLK0_OUT / I2S_CLK0_IN / PKT_CLK2_ALT / EXT_IRQ0b_3 / ENET0_RGMII_START_STOP_ALT / EXT_IRQ0b_0 / LED_LD_13
GPIO_019/SC0_AUX2 / I2S_DATA0_OUT / I2S_DATA0_IN / PKT_DATA2_ALT / ENET0_RGMII_START_STOP / RSV0_5 / EXT_IRQ0b_1 / LED_LD_14
GPIO_020/SC0_VPP / I2S_LR0_OUT / I2S_LR0_IN / PKT_SYNC2_ALT / ENET0_RGMII_RX_OK / RSV0_6 / EXT_IRQ0b_2 / LED_LD_15
GPIO_021/ENET0_RGMII_RX_CLK / PKT_CLK0_ALT / V00_656_0 / IR_IN1 / MTSIF0_CLK / SD_CARD0_0_CMD / CPU_TRACE_CLK
GPIO_022/ENET0_RGMII_RX_EN_CTL / PKT_DATA0_ALT / V00_656_1 / RSV0_4 / MTSIF0_DATA0 / SD_CARD0_0_CLK / CPU_TRACE_DATA0
GPIO_023/ENET0_RGMII_RXD_00 / PKT_SYNC0_ALT / V00_656_2 / UART_CTS_0 / MTSIF0_SYNC / SD_CARD0_0_DATA / CPU_TRACE_DATA1
GPIO_024/ENET0_RGMII_RXD_01 / PKT_ERROR0_ALT / V00_656_3 / UART_RTS_0 / MTSIF0_DATA1 / SD_CARD0_0_DAT1 / CPU_TRACE_DATA2
GPIO_025/ENET0_RGMII_RXD_02 / PKT_VALID0_ALT / V00_656_4 / UART_RXD_1 / RSV0_5 / SD_CARD0_0_DATA2 / CPU_TRACE_DATA3
GPIO_026/ENET0_RGMII_RXD_03 / PKT_CLK1_ALT / V00_656_5 / UART_TXD_1 / MTSIF0_DATA2 / SD_CARD0_0_DATA3 / CPU_TRACE_DATA4
GPIO_027/ENET0_RGMII_TX_CLK / PKT_DATA1_ALT / V00_656_6 / UART_CTS_1 / MTSIF0_DATA3 / SD_CARD0_0_CLK_IN / CPU_TRACE_DATA5
GPIO_028/ENET0_RGMII_TX_EN_CTL / PKT_SYNC1_ALT / V00_656_7 / UART_RTS_1 / MTSIF0_DATA4 / SD_CARD0_0_PRES / CPU_TRACE_DATA6 / ENET0_LINK_ALT8
GPIO_029/ENET0_RGMII_TXD_00 / PKT_ERROR1_ALT / V00_656_CLK / UART_RXD_2 / MTSIF0_DATA5 / CPU_TRACE_DATA7 / ENET0_ACTIVITY_ALT6
GPIO_030/ENET0_RGMII_TXD_01 / PKT_VALID1_ALT / TTX0_DATA / UART_TXD_2 / MTSIF0_DATA6 / RSV0_6 / CPU_TRACE_DATA8
GPIO_031/ENET0_RGMII_TXD_02 / VEC_HSYNC / TTX0_REQ / UART_CTS_2 / MTSIF0_DATA7 / RSV0_6 / CPU_TRACE_DATA9
GPIO_032/ENET0_RGMII_TXD_03 / VEC_VSYNC / RSV0_3 / UART_RTS_2 / RSV0_5 / RSV0_6 / CPU_TRACE_DATA10
GPIO_033/ENET0_RGMII_MDIO / SD_CARD1_1_WPROT / RSV0_3 / UART_RXD_0 / MTSIF_ATS_INC7 / SD_CARD0_0_WPROT / CPU_TRACE_DATA11 / SD_CARD0_0_LED
GPIO_034/ENET0_RGMII_MDC / SD_CARD1_1_PRES / I2S_LR0_OUT / UART_TXD_0 / MTSIF_ATS_RST / SD_CARD0_0_PRES / SD_CARD0_0_PWRO / SD_CARD0_0_VOLT
GPIO_035/ENET0_RGMII_RX_ERR / SD_CARD1_1_VOLT / SPI_M_MOSI / I2S_CLK0_OUT / RMX_CLK0_ALT2 / ENET0_LINK_ALT / SD_CARD0_0_PWRO / ENET0_RGMII_MDIO_ALT
GPIO_036/Strap/ENET0_MII_TX_ERR / SD_CARD1_1_LED / SPI_M_SS0b / I2S_DATA0_OUT / RMX_DATA0_ALT2 / ENET0_ACTIVITY_ALT / SD_CARD0_0_LED
GPIO_037/ENET0_RGMII_IRQ / SD_CARD1_1_PWRO / SPI_M_SCK / I2S_LR0_OUT / RMX_SYNC0_ALT2 / SD_CARD0_0_VOLT / PKT_CLK2
GPIO_038/ENET0_MII_CRS / I2S_CLK0_OUT / SPI_M_MISO / ENET0_LINK / RMX_VALID0_ALT2 / UART_RXD_2 / PKT_DATA2
GPIO_039/ENET0_MII_COL / ENET0_RGMII_IRQ / SPI_M_SS1b / ENET0_ACTIVITY / RMX_PAUSE0_ALT2 / UART_TXD_2 / PKT_SYNC2
GPIO_040/Strap/UART_TXD_0 / SC_CLK_OUT / IR_INT / SPI_S_MISO
GPIO_041/UART_RXD_0 / EXT_SC_CLK / AUD_FS_CLK0 / SPI_S_SS0b
GPIO_042/POD2CHIP_MCLK1 / EBI_ADDR_14 / EXT_IRQ0b_0
GPIO_043/POD2CHIP_SDI / EXT_IRQ0b_1
GPIO_044/POD2CHIP_MISTR1 / RSV0_2 / EXT_IRQ0b_2
GPIO_045/POD2CHIP_MIVAL / RSV0_2 / EXT_IRQ0b_3
GPIO_046 / POD2CHIP_MD10
GPIO_047 / POD2CHIP_MD11
GPIO_048 / POD2CHIP_MD12
GPIO_049 / POD2CHIP_MD13
GPIO_050 / POD2CHIP_MD14
GPIO_051 / POD2CHIP_MD15
GPIO_052 / POD2CHIP_MD16
GPIO_053 / POD2CHIP_MD17
GPIO_054 / POD2CHIP_MICLK / EBI_ADDR_13
GPIO_055 / CHIP2POD_MCLK0
GPIO_056 / CHIP2POD_SCLK / EBI_ADDR_01
GPIO_057 / CHIP2POD_SDO / EBI_ADDR_01
GPIO_058 / CHIP2POD_SCTL / EBI_ADDR_02
GPIO_059 / CHIP2POD_M0VAL
GPIO_060 / CHIP2POD_M0STR1
GPIO_061 / CHIP2POD_M0D0
GPIO_062 / CHIP2POD_M0D1
GPIO_063 / CHIP2POD_M0D2
GPIO_064 / CHIP2POD_M0D3
GPIO_065 / CHIP2POD_M0D4
GPIO_066 / CHIP2POD_M0D5
GPIO_067 / CHIP2POD_M0D6
GPIO_068 / CHIP2POD_M0D7
GPIO_069 / CHIP2POD_M0CLK / EBI_ADDR_12
GPIO_070 / EBI_RWb
GPIO_071 / EBI_CS3b / EBI_ADDR_14
GPIO_072 / EBI_WE1b
GPIO_073 / EBI_WA1b
GPIO_074 / EBI_ADDR_03 / EXT_IRQ0b_0
GPIO_075 / EBI_ADDR_04 / EXT_IRQ0b_1
GPIO_076 / EBI_ADDR_05 / EXT_IRQ0b_2
GPIO_077 / EBI_ADDR_06 / EXT_IRQ0b_3
GPIO_078 / EBI_ADDR_07
GPIO_079 / EBI_ADDR_08 / RSV0_2 / SD_CARD1_0_DAT0
GPIO_080 / EBI_ADDR_09 / RSV0_2 / SD_CARD1_0_DAT1
GPIO_081 / EBI_ADDR_10 / RSV0_2 / SD_CARD1_0_DAT2
GPIO_082 / EBI_ADDR_11 / RSV0_2 / SD_CARD1_0_DAT3
    
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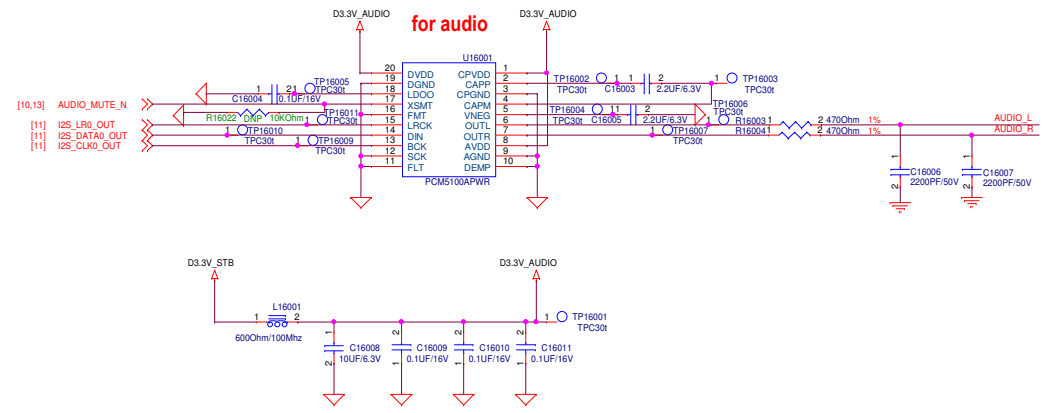


BSC Addresses				
BSC Port	Connected to	Function	BSC Address	Comments
BCM72604_BSC_M0	HDMI Output Connector	HDMI Output Port DDC		Don't share BSC with other devices.
BCM72604_BSC_M3				

Ref : 12000~12999



Layout Guidelines:  
 1. Place R16000 near 72604, shield with GND where possible  
 2. Place R16001 termination < 25mm from 72604



Ref : 16000~16999