

## Load Instability Problems with TPA3255 used in PBTL Modus

Hello,

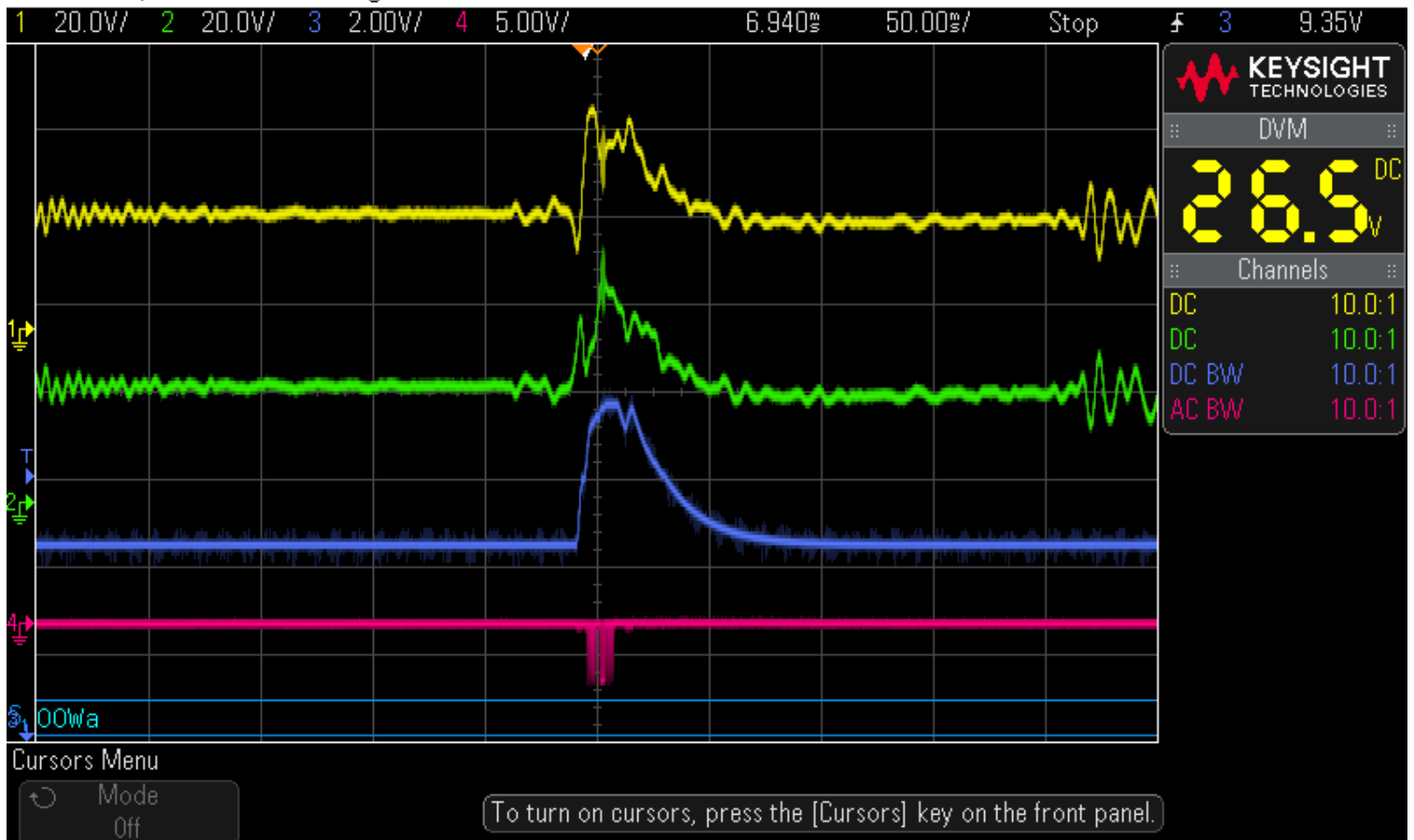
The PCB is developed in our Company and is similar to the EMV-PCB (see Pictures).  
TPA3255 works in PBTL mode.

- $+V_B = 52V$
- $V_{DD} = 12V$
- $AV_{DD} = 6V$  (with no Load connected to the Outputs, see Picture).
- Inputs of TPA3255 connected to OpAmp Outputs with ceramic Capacitors to get rid of polarity Problems.
- Peak Voltage of the Outputs:  $\pm 10V$
- Load resistance: 8 Ohm (Power Resistor, no complex Load)
- The Amp is used as Bassamp, max.  $F = 100Hz$
- The second TPA3255 used in Stereo Mode does not suffer from this Problems! (Frequencies  $> 100Hz$ )

### Oscilloscope Picture 1:

**Yellow/Green** = Outputs after Filter  
**Blue** =  $AV_{DD} = \text{Pin 14 of TPA 3255}$   
**Red** = Clip Output = Pin 21 of TPA3255

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Picture 1

The normal Modulation (in this case Blues Music) can be seen on the right and left Side of the Screenshot.

The Problem is to be seen in the Center. The huge Modulation is not the Signal, this is the strange Behavior of the IC.

As to be seen AVDD follows the Envelope of the Music.

We tested two very different Coils in the Output Filters: 1D17A-150M (15uH) from ICE Components and a Coil made for our bigger D-Amps 20uH/50A saturation Current (Iron Powder). So the Coils can't be the reason.

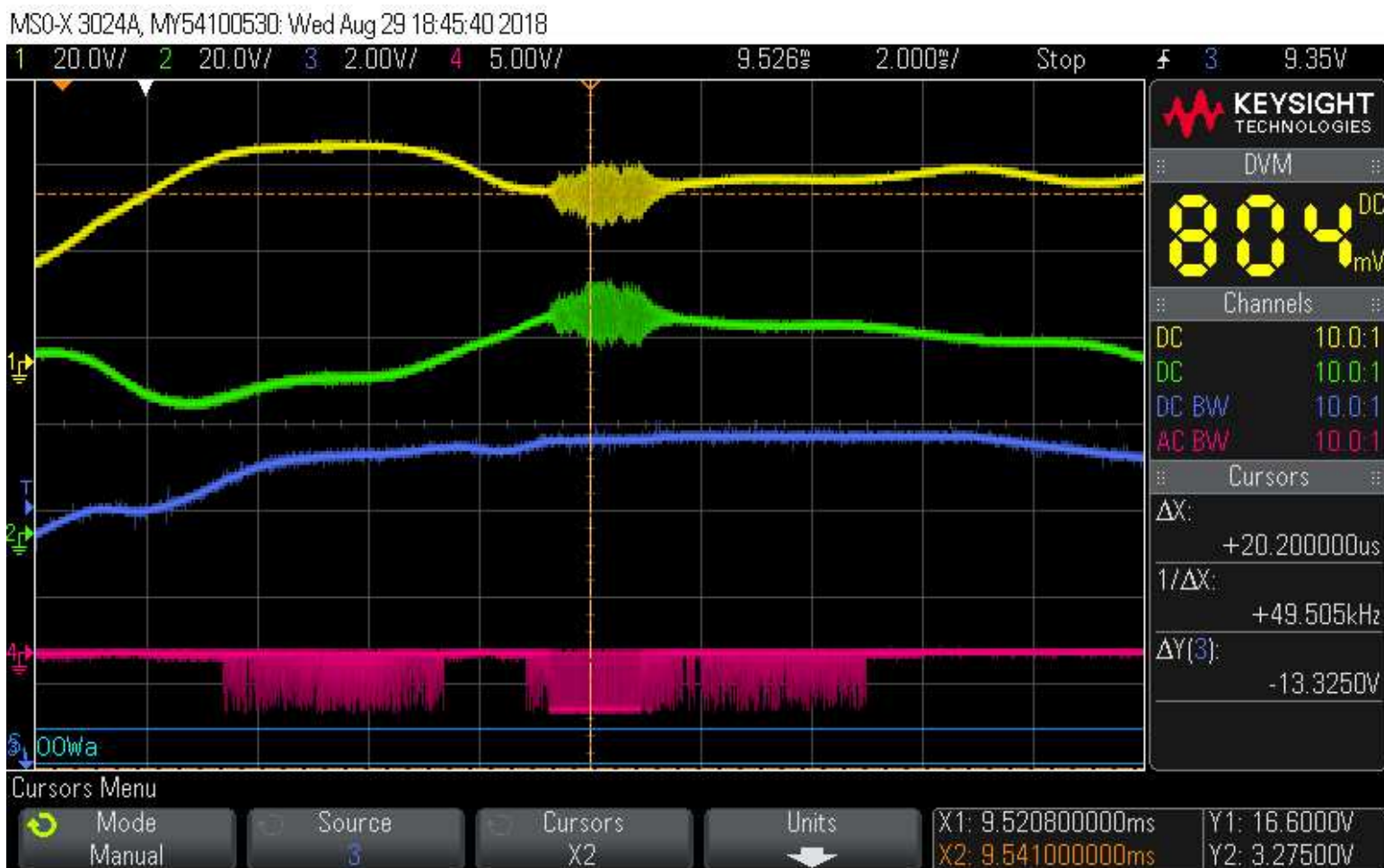
**Without Load the Amp works properly.**

The Valleys of the Clip Output are real. (Lower than GND caused by AC Coupling Input Oscilloscope)

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The Picture below is a enlarged area of Picture 1.

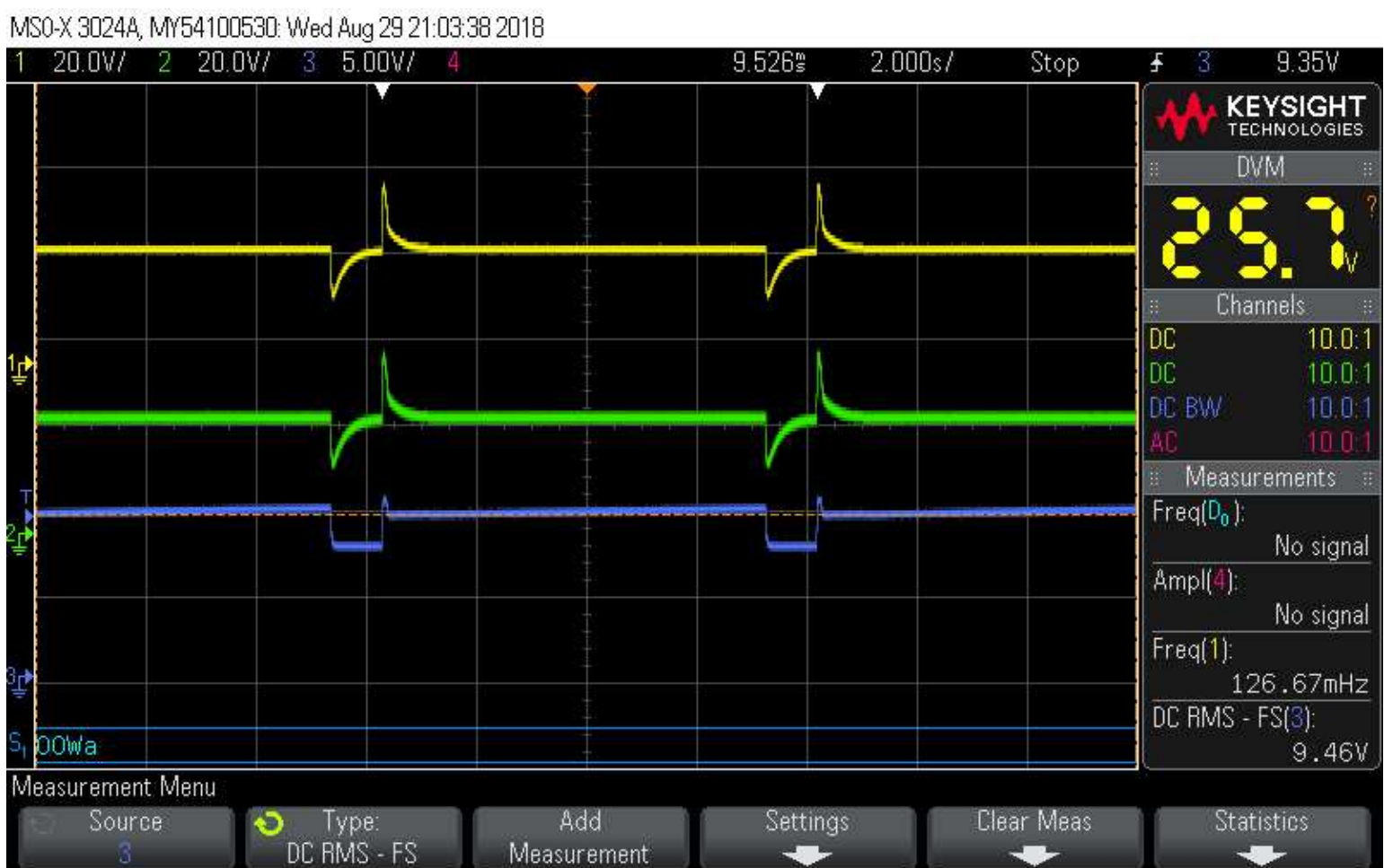
The Outputs show a ~50kHz Oscillation (= Pole Frequency of the output Filters) when the Power Mosfets are switched of due to (why? 8 Ohm Load) current limiting. The positiv Output (yellow) seems to clip 4 – 7 mSecs.



Picture 2

Special situations call for special measures...

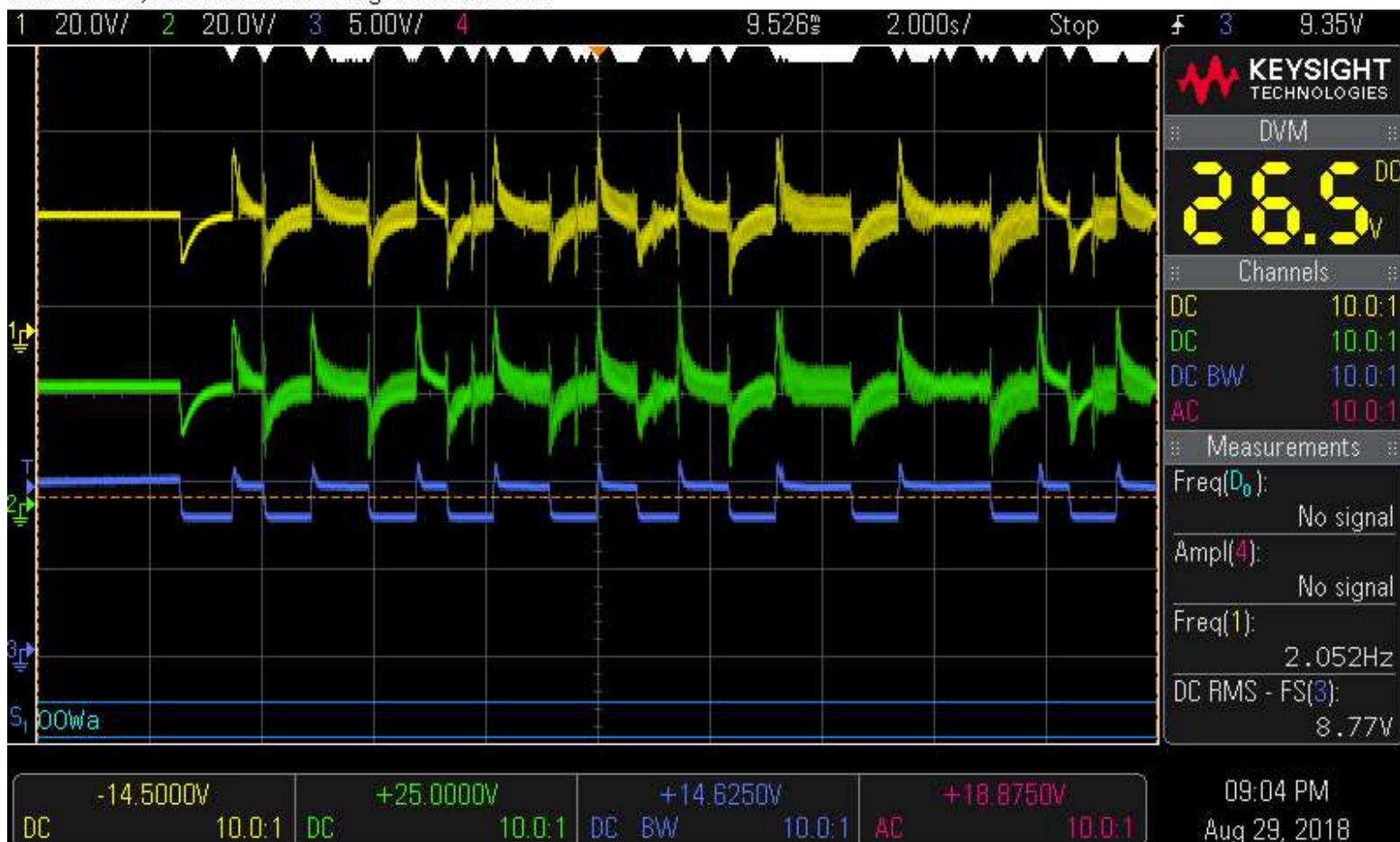
So we enlarged the Bootstrap Capacitors to 100nF each. Something changed, but the changes have not yet produced the desired results....



**Picture 3**

The Amp after switched on – no Audio Signal. With  $C_{\text{Bootstrap}} = 100\text{nF}$  the Amp oscillates.

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**Picture 4**

Same as Picture 3 but with Audio signal applied.

We tested and checked anything on the PCB - the placement and soldering seem to be o.k.

Did we make a big mistake we couldn't discover?

So we need Help.

With regards

Marc legen

**What is the mechanism that forces the AVDD voltage to follow V-Out from a certain power delivered by the TPA3255?**

