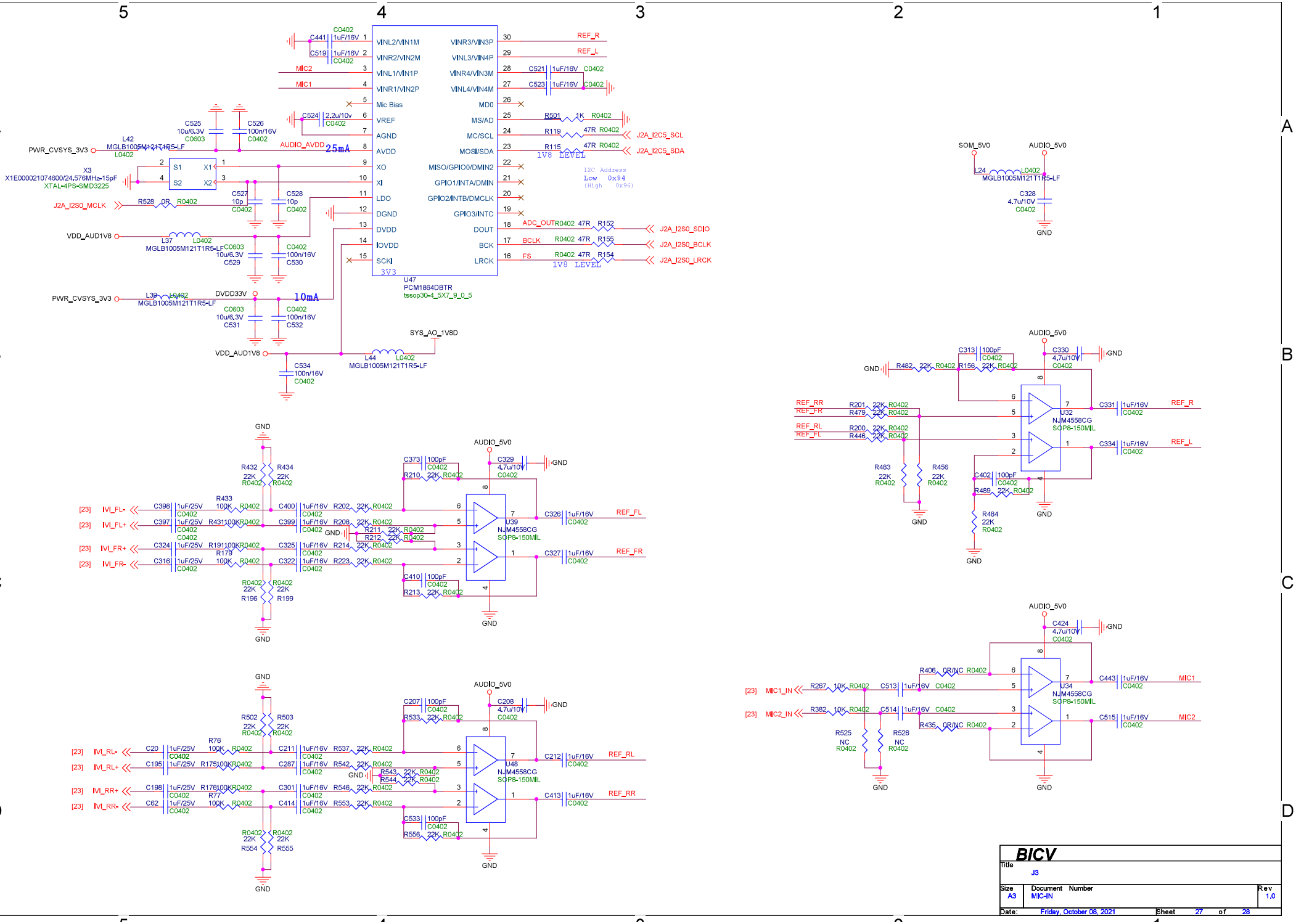
U47
PCM1864DBTR
tssop30-4_5X7_9_0_5
VINL2/VIN1M
VINR2/VIN2M
VINL1/VIN1P
VINR1/VIN2P
Mic Bias
VREF
AGND
AVDD
XO
XI
LDO
DGND
DVDD
IOVDD
SCKI
VINR3/VIN3P
VINL3/VIN4P
VINR4/VIN3M
VINL4/VIN4M
MD0
MS/AD
MC/SCL
MOSI/SDA
MISO/GPIO0/DMIN2
GPIO1/INTA/DMIN
GPIO2/INTB/DMCLK
GPIO3/INTC
DOUT
BCK
LRCK
3V3
REF_R
REF_L
MIC2
MIC1
AUDIO_AVDD
25mA
J2A_I2C5_SCL
J2A_I2C5_SDA
1V8 LEVEL
I2C Address
Low 0x94
(High 0x96)
J2A_I2S0_SDIO
J2A_I2S0_BCLK
J2A_I2S0_LRCK
ADC_OUT
BCLK
FS
J2A_I2S0_MCLK
PWR_CVSYS_3V3
VDD_AUD1V8
DVDD33V
10mA
SYS_AO_1V8D
X3
X1E000021074600/24,576MHz-15pF
XTAL-4PS-SMD3225
MGLB1005M121T1R5-LF
SOM_5V0
AUDIO_5V0
NJM4558CG
SOP8-150MIL
IVI_FL-
IVI_FL+
IVI_FR+
IVI_FR-
IVI_RL-
IVI_RL+
IVI_RR+
IVI_RR-
REF_FL
REF_FR
REF_RL
REF_RR
MIC1_IN
MIC2_IN
GND
BICV
Title
J3
Size
A3
Document Number
MIC-IN
Rev
1.0
Date:
Friday, October 08, 2021
Sheet
27
of
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