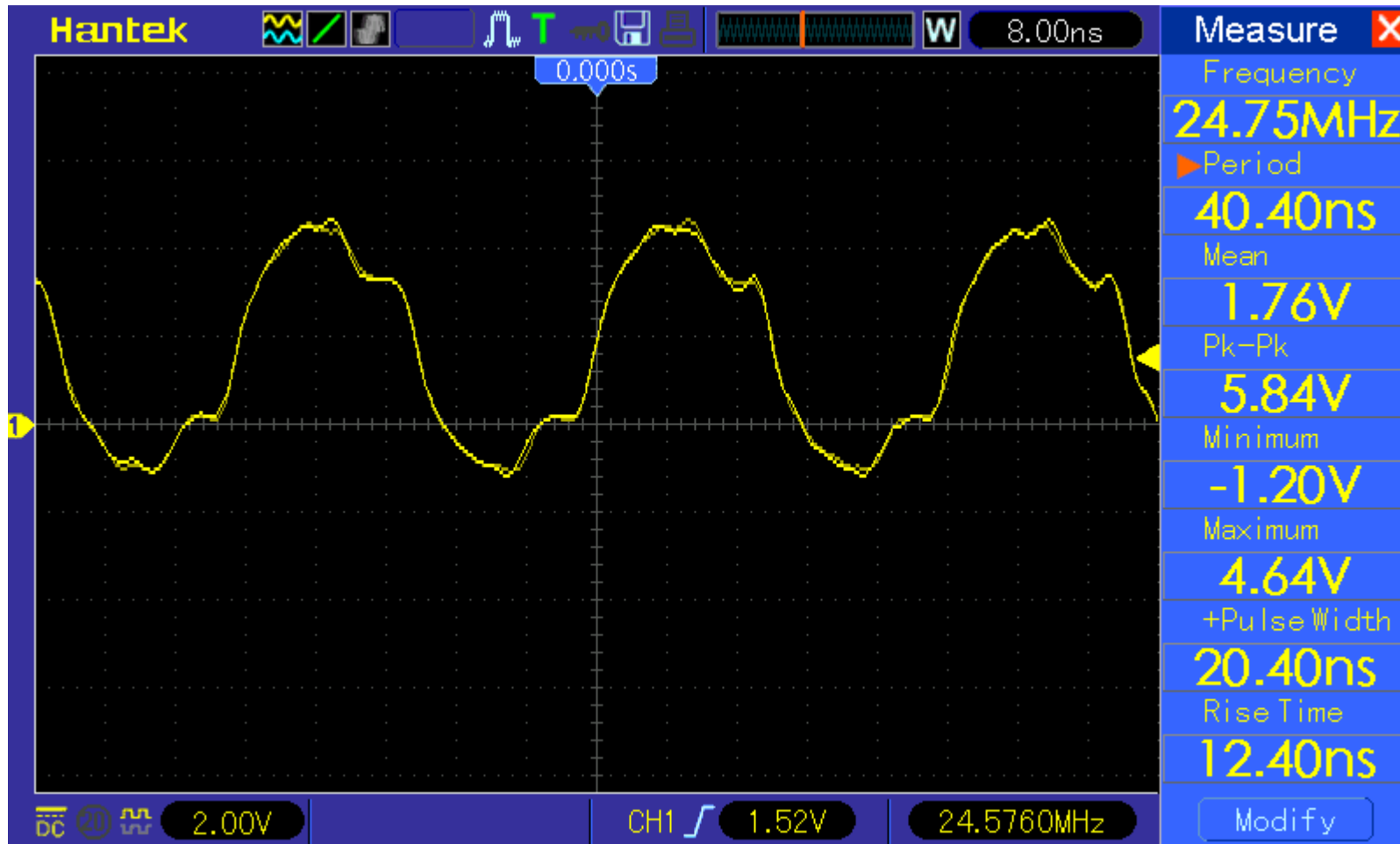
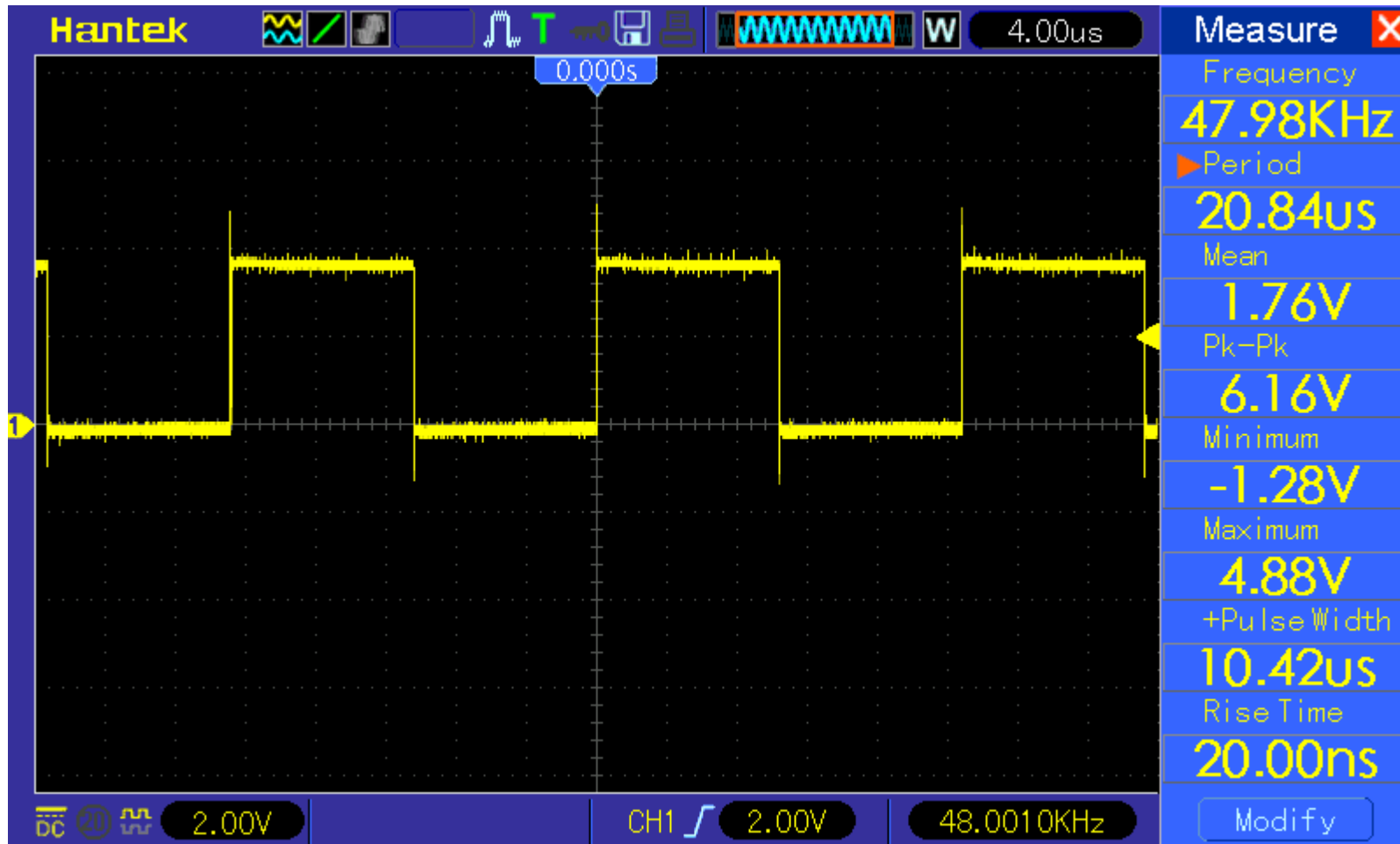


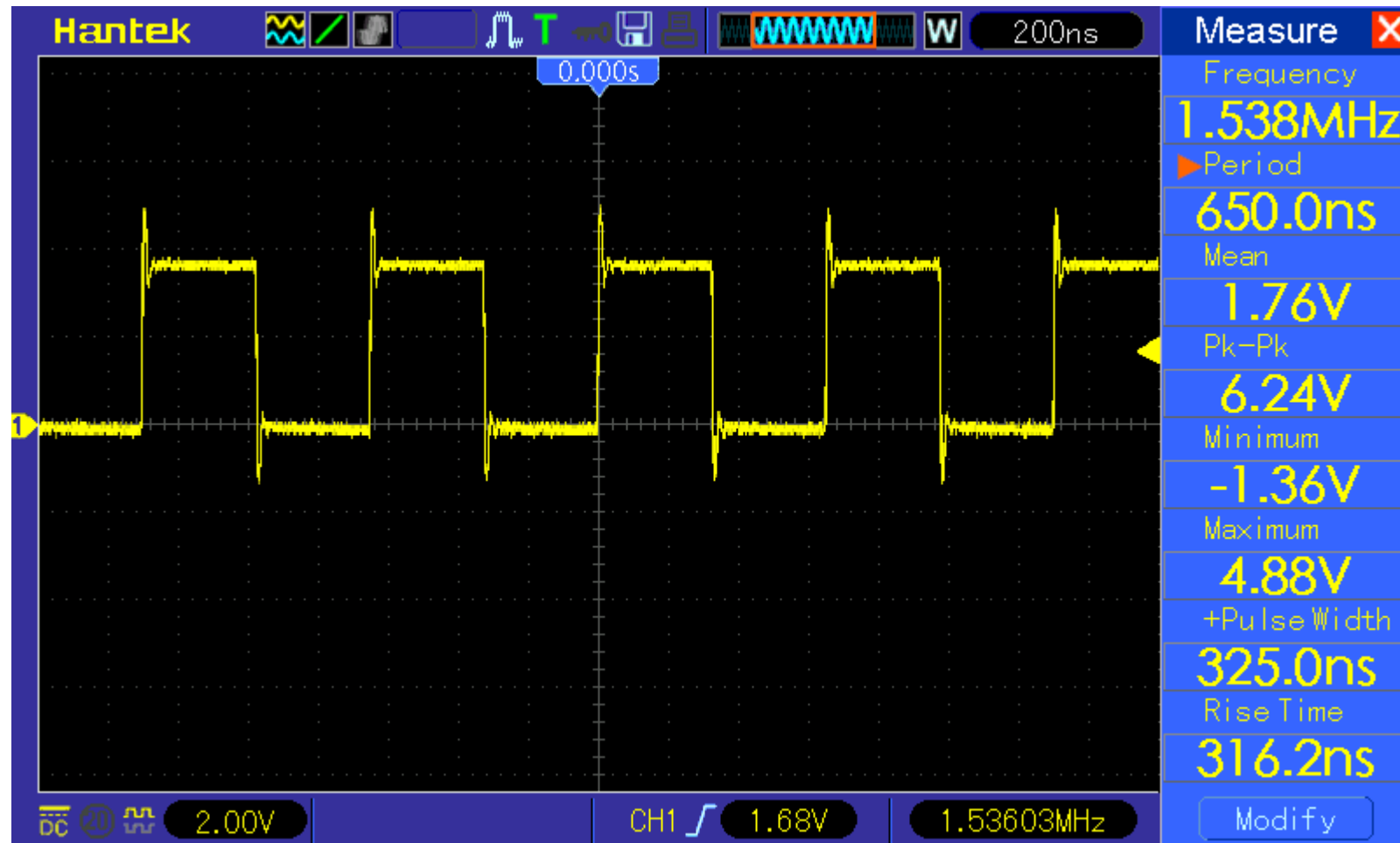
Serial Clock (SCK)



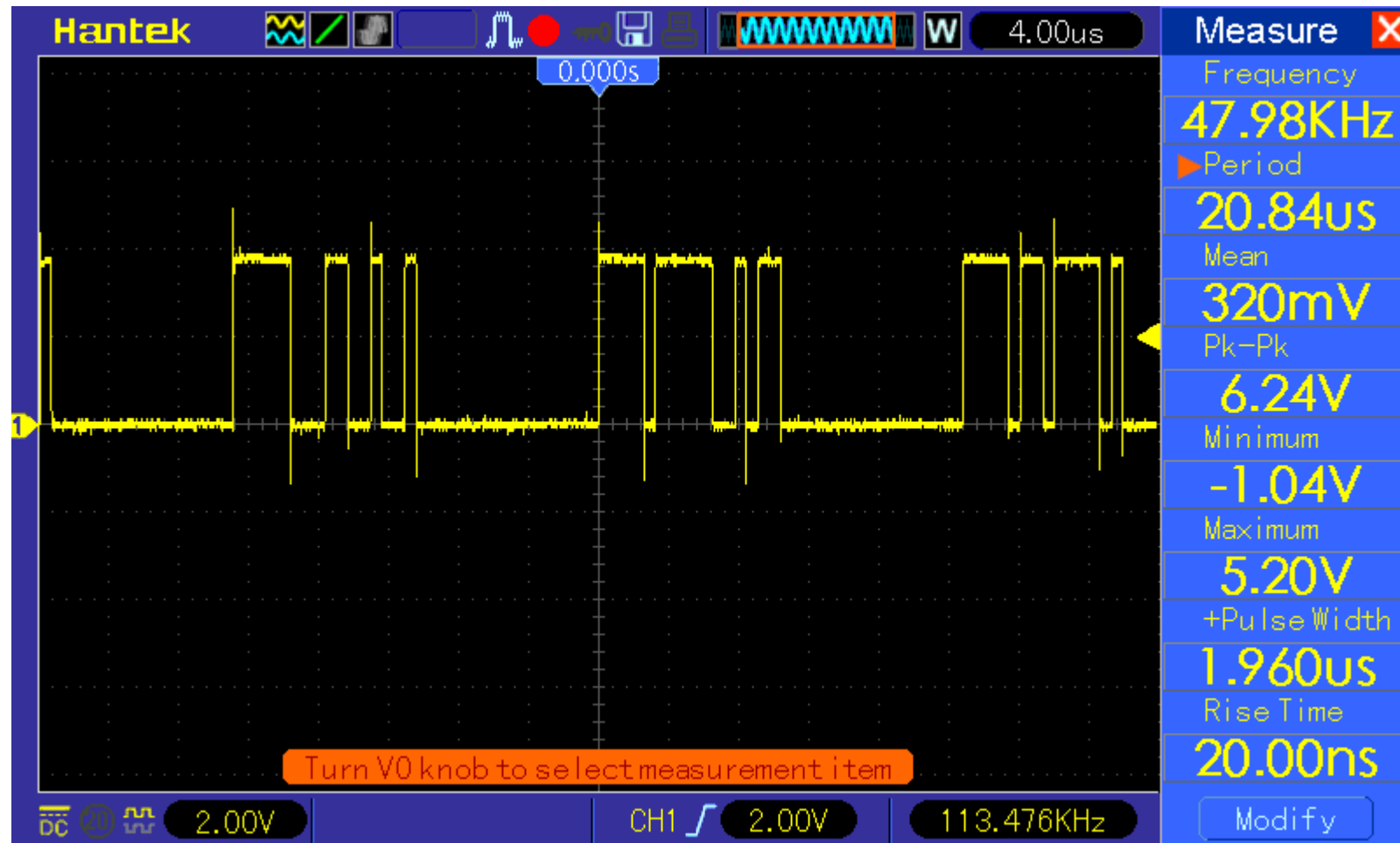
LR Clock (LRCK)



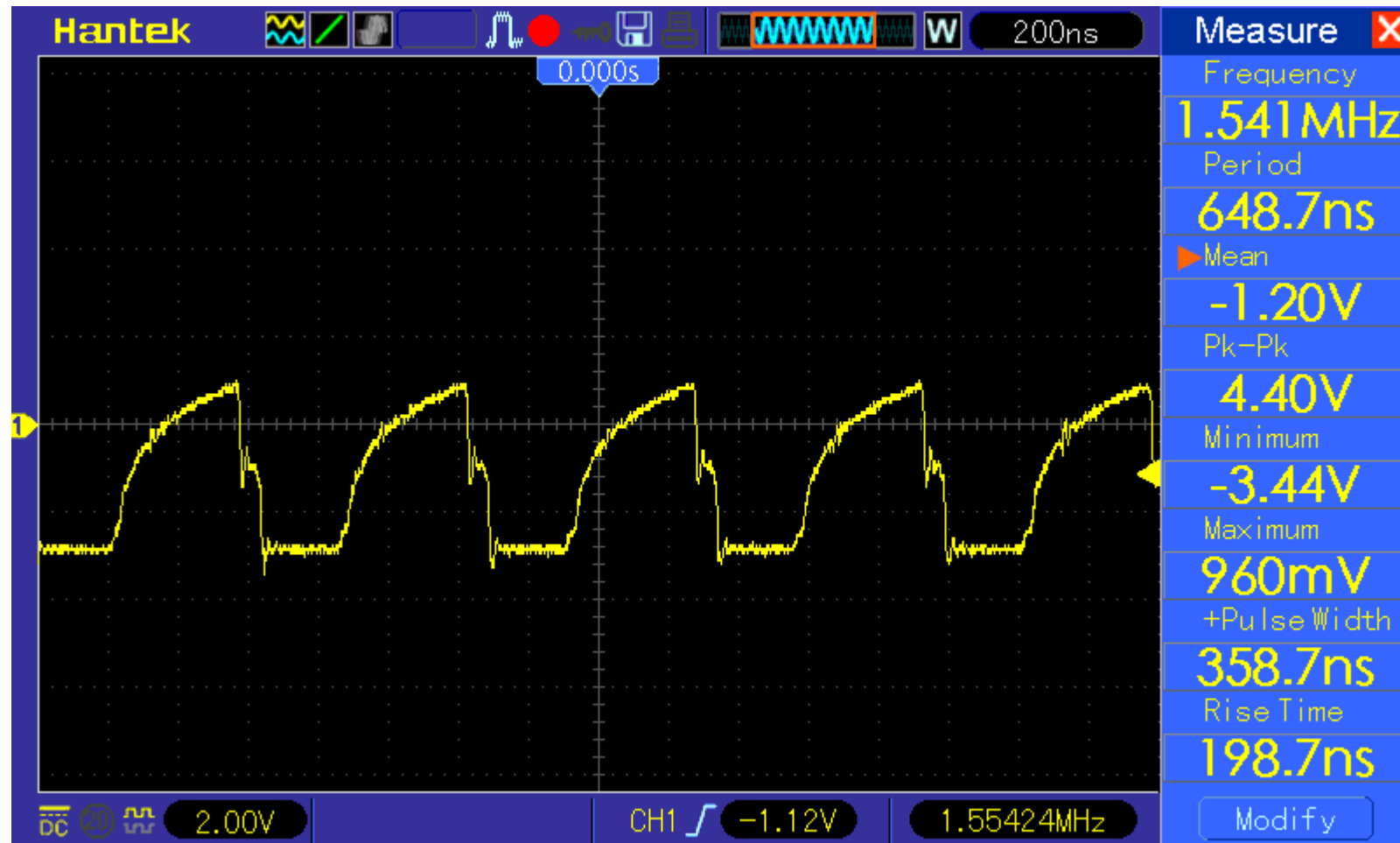
Bit Clock (BCK)



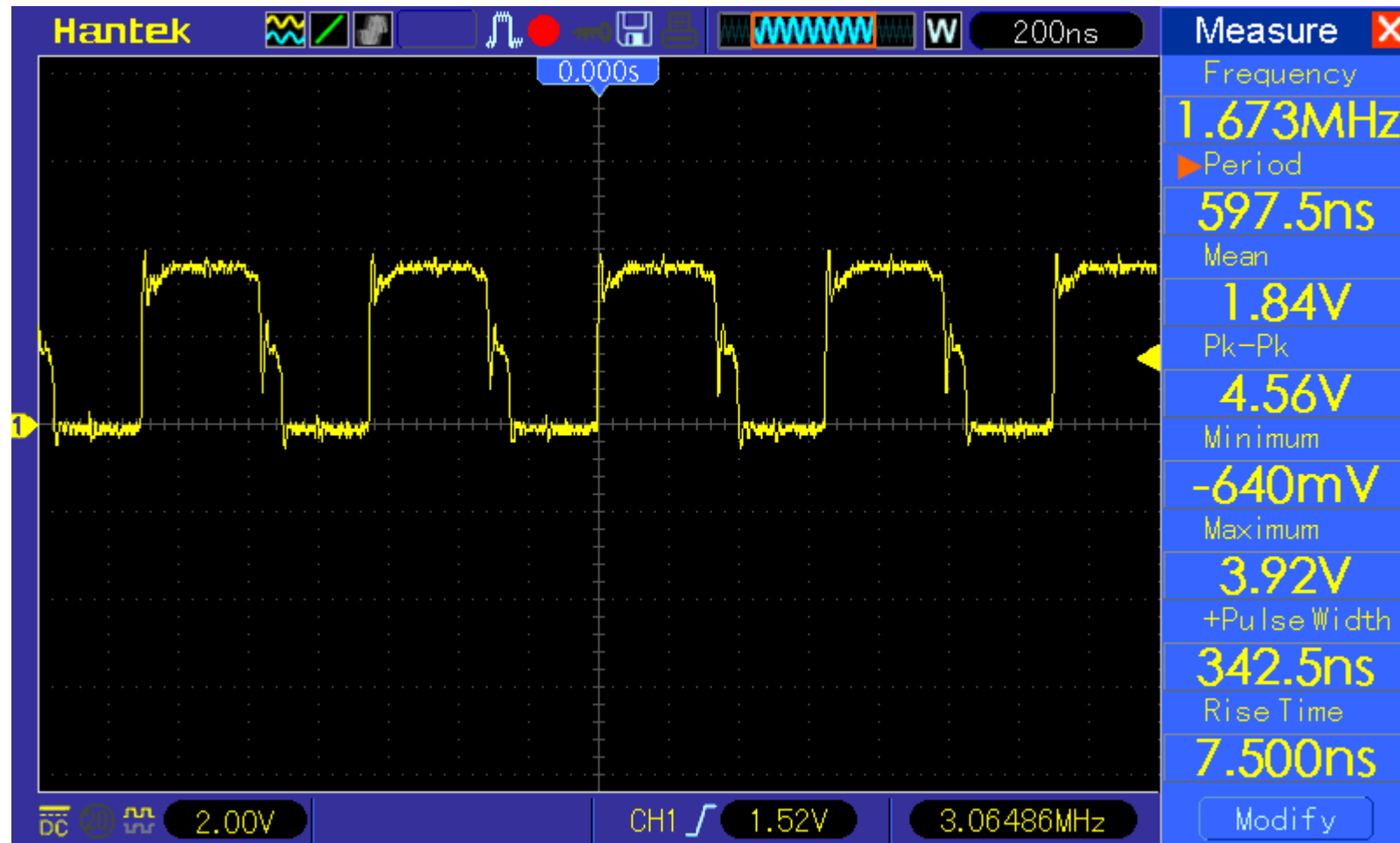
Audio Data input (DIN)



Output on Pin 9 (VNEG)



Pin 6 (CAPP)



Pin 8 (CAPM)

