

AMP VDD PIN 최대한 근접 배치 할것

VBAT : 14.4V/0.1A(MAX)  
9V/0.1A(MAX)

PVDD : 14.4V/4.472A(80W/4ohm)

U5700  
TAS6424QDKQRQ1

(3.3V/0.015A)

3V3\_ALIVE

C5714 105P/25V/1608

ICT5706

C5715 105P/25V/1608

C5719 105P/25V/1608

C5721 105P/25V/1608

C5724 225P/16V/2012

C5725 225P/16V/2012

3V3\_ALIVE

R5700

R5701

OPEN-104

OPEN-104

D AMP1 ADR1

D AMP1 ADR0

R5702

R5703

.104

.104

\*Device Address

WRITE : D4

READ : D5

[15,20] DSP\_D-AMP\_MCLK  
[20] DSP\_D-AMP1\_TDM\_BCLK  
[20] DSP\_D-AMP1\_TDM\_LRCLK  
[20] DSP\_D-AMP1\_TDM\_SDO

[9] MCU\_D-AMP1\_SCL  
[9] MCU\_D-AMP1\_SDA

D-AMP1\_ADR0  
D-AMP1\_ADR1

[10] MCU\_D-AMP1\_STBY  
[9] MCU\_D-AMP1\_MUTE

[9] D-AMP1\_DIAG\_MCU\_DET  
[9] D-AMP1\_WARN\_MCU\_DET

3V3\_ALIVE

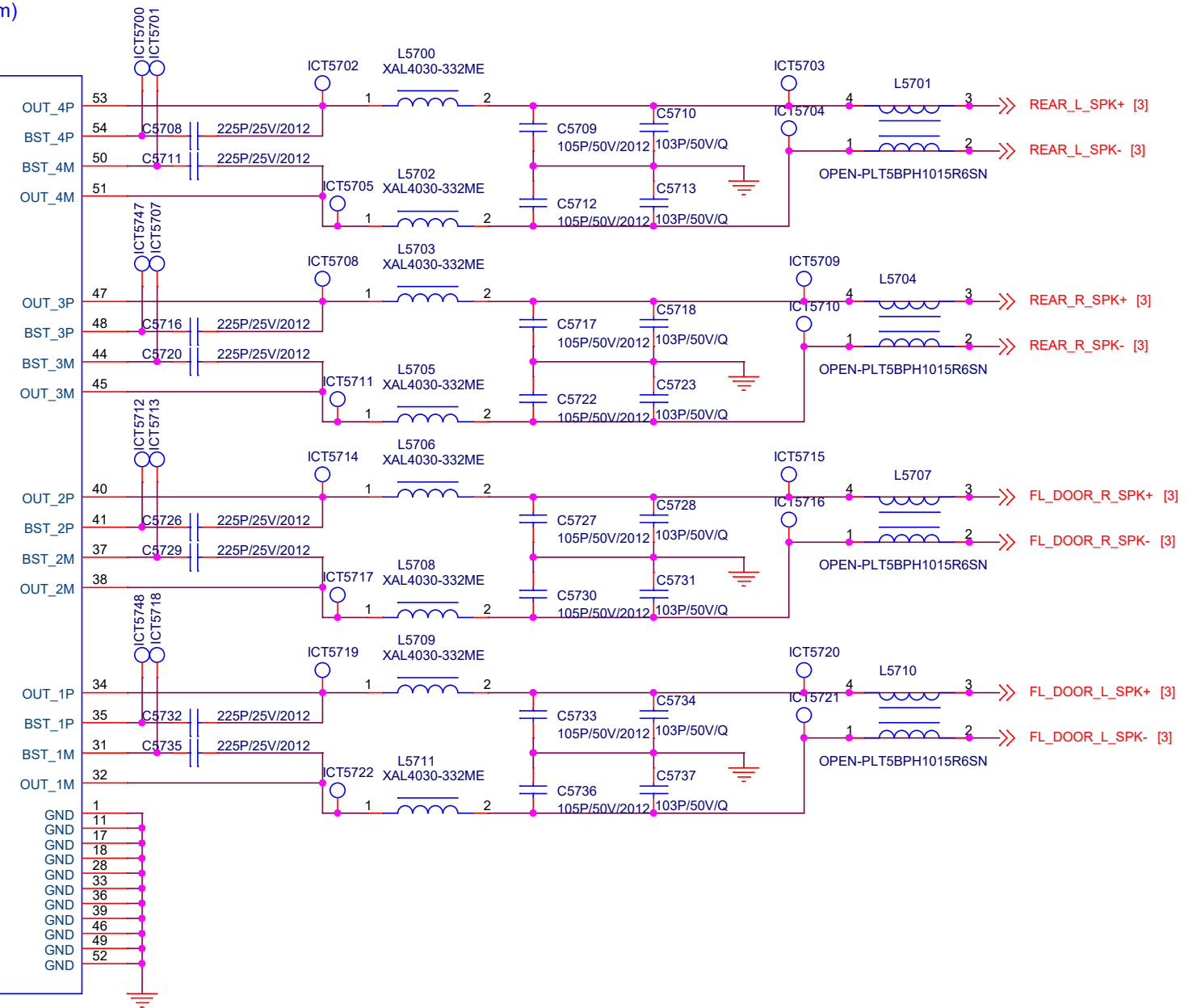
R5708

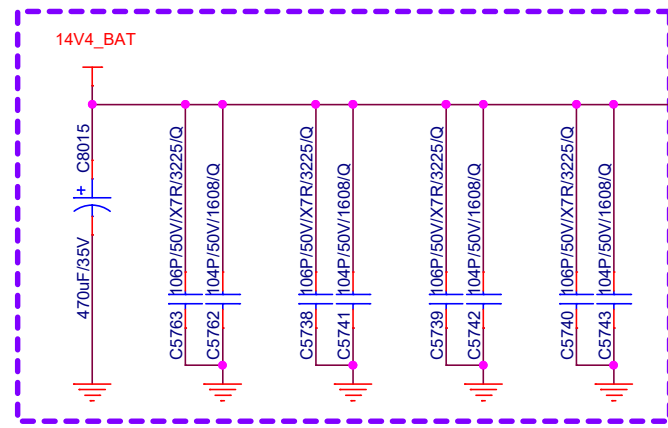
R5707

103

103

Internal Pull-down 100k





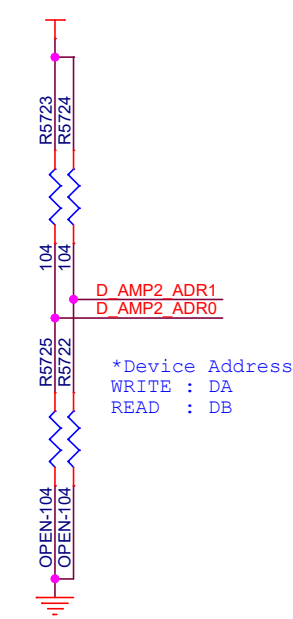
AMP VDD PIN 최대한 근접 배치 할것

(3.3V/0.02A)

VBAT : 14.4V/0.1A(MAX)  
9V/0.1A(MAX)  
PVDD : 14.4V/4.472A(80W/4ohm)

U5701  
TAS6424-Q1

3V3\_ALIVE

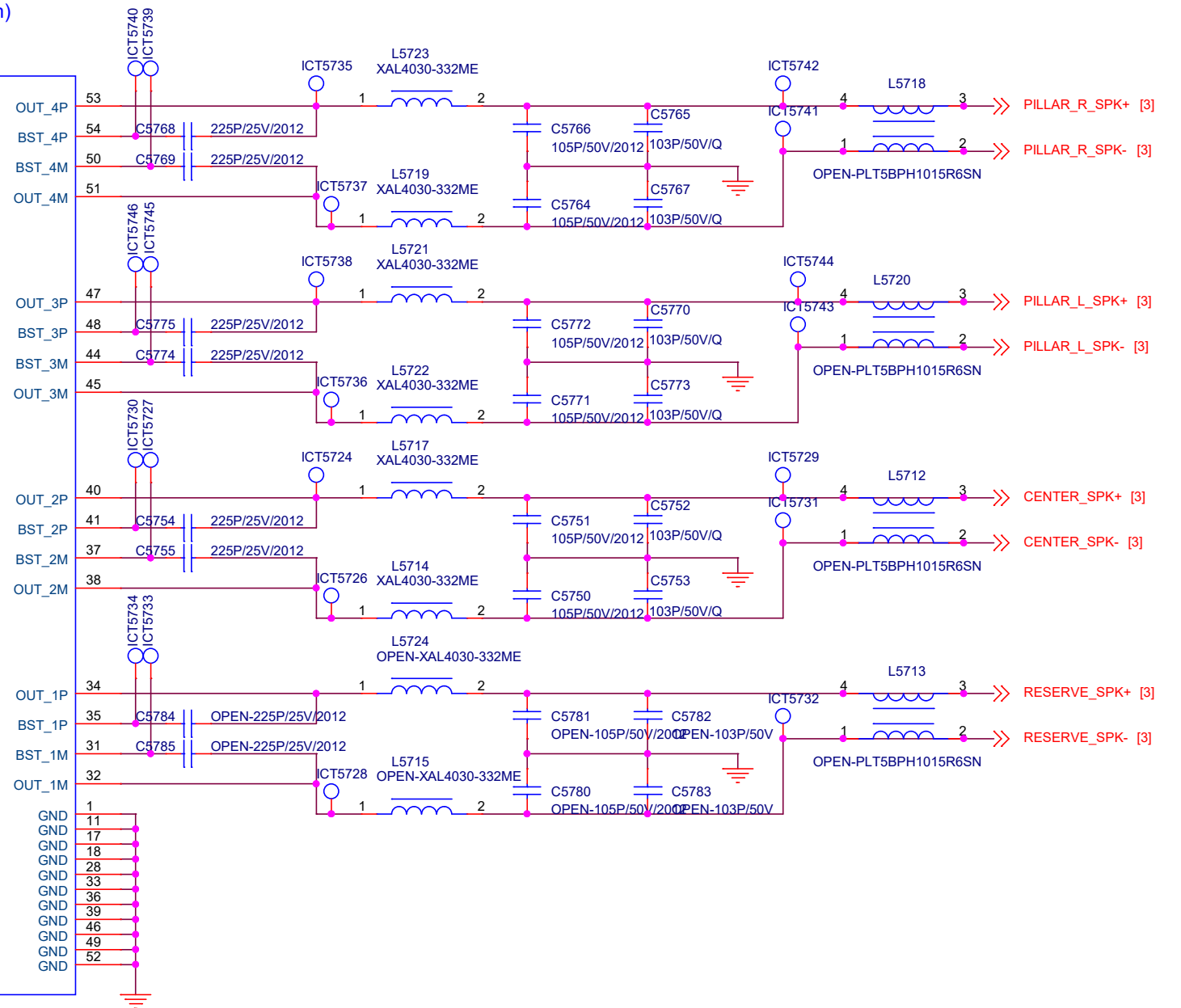
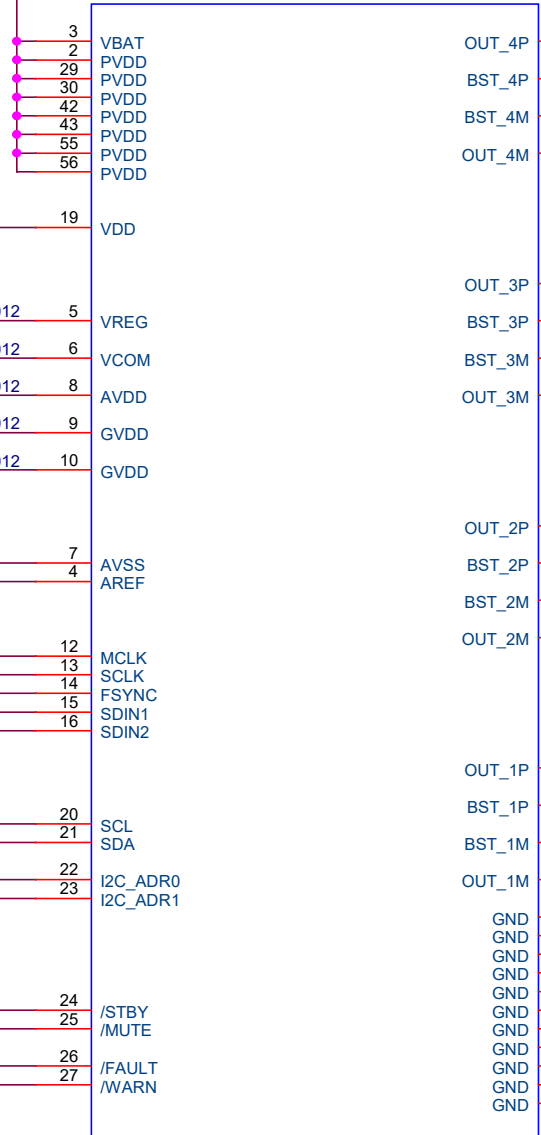
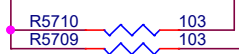


[14,20] DSP\_D-AMP\_MCLK  
[20] DSP\_D-AMP2\_TDM\_BCLK  
[20] DSP\_D-AMP2\_TDM\_LRCLK  
[20] DSP\_D-AMP2\_TDM\_SDO

[9] MCU\_D-AMP2\_SCL  
[9] MCU\_D-AMP2\_SDA

[10] MCU\_D-AMP2\_STBY  
[10] MCU\_D-AMP2\_MUTE  
[9] D-AMP2\_DIAG\_MCU\_DET  
[9] D-AMP2\_WARN\_MCU\_DET

3V3\_ALIVE



\*Device Address  
WRITE : DA  
READ : DB