

TAS5805 Register Map Introduction

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TAS5805m: Register structure Overview

❑ Overview

- Non-DSP (in book 0/
page 0)
- DSP (in other
book/page)

❑ TAS5805 register structure:

1. Book 0

- Page 0
- Page 1

2. Book 8C

- Page 0x00
- Page 0x01

3. Book 78

- Page 0x00
- Page 0x01

4. Book AA

- Page 0x00
- Page 0x01

Note: See Maps of TAS5805's datasheet

Note: See **Appendix** of [TAS5805 process flow](#) to the map

TAS5805m: Register structure Overview

❑ TAS5805 register structure:

1. Book 0
 - Page 0
 - Page 1
2. Book 8C
 - Page 0x00
 - Page 0x01
3. Book 78
 - Page 0x00
 - Page 0x01
4. Book AA
 - Page 0x00
 - Page 0x01

❑ Register operation:

1. Change Book
`{0x00, 0x00}` // must change to page 0 of current book
`{0x7f, 0xaa}` // Then change to book aa
`{0x00, 0x24}` // go to book aa/ page 24. If no operation, its default page is 0x00;
2. Change page under certain book
`{0x00, 0x24}` // change to page 24
3. Change address under certain book/page
`{0x00, 0x00}`
`{0x00, 0xaa}`
`{0x00, 0x24}` //page 24
`{0x7f, 0x24}` //change value of register 0x7f to 0x24



As 0x7f is in page 24, hence 0x7f is not a book change operation

❑ Book aa

It is a buff book, after it is finished, it will be loaded into book 8C automatically.

❑ Book 78

Level meter



Example to find registers for volume

1. Use I2C log

Step2: click log then click the dot to make it turn red

Step4: obtain the registers for volume

1. change to page 0 of current book;
2. change to book 8C
3. Enter page 2a of book 8C
4. Register 24 and 28 in page 2a of book 8C is for L/R volume

Step1: click on I2C

2. Double check by Appendix of [TAS5805 process flow](#) to the map as below:

Table 5. Memory Map — Book 0x8C

SUB ADDRESS	PAGE	REGISTER NAME	NUMBER OF BYTES/FORMAT	DEFAULT VALUE	DESCRIPTION
INPUT MIXER					
0x18	0x29	Left to Left	4 / 9.23	0x00800000	Left Channel Mixer Left Input Gain
0x1C	0x29	Right to Left	4 / 9.23	0x00000000	Left Channel Mixer Right Input Gain
0x20	0x29	Left to Right	4 / 9.23	0x00000000	Right Channel Mixer Left Input Gain
0x24	0x29	Right to Right	4 / 9.23	0x00800000	Right Channel Mixer Right Input Gain
VOLUME CONTROL					
0x24	0x2A	CH-L Volume	4 / 9.23	0x00800000	Left Channel Volume coefficient
0x28	0x2A	CH-R Volume	4 / 9.23	0x00800000	Right Channel Volume coefficient
0x30	0x2A	Softening Filter Alpha	4 / 1.31	0x00E2C46B	Volume Time constant

Use PPC3's I2C log function

完成调音设置后，PPC3导出.h文件：

```
51 cfg_reg registers[] = {  
52 //寄存器地址 寄存器数值  
53 { 0x00, 0x00 },  
54 { 0x7f, 0x00 },  
55 { 0x03, 0x02 },  
56 { 0x01, 0x11 },  
57 { 0x00, 0x00 },  
58 { 0x00, 0x00 },  
59 { 0x00, 0x00 },  
60 { 0x00, 0x00 },  
61 { 0x00, 0x00 },  
62 { 0x00, 0x00 },  
63 { 0x7f, 0x00 },  
64 { 0x03, 0x02 },  
65 { 0x00, 0x00 },  
66 { 0x7f, 0x00 },  
67 { 0x03, 0x00 },  
68 { 0x00, 0x00 },  
69 { 0x7f, 0x00 },  
70 { 0x46, 0x11 },  
71 { 0x00, 0x00 },  
72 { 0x00, 0x00 },  
73 { 0x7f, 0x00 },
```

如何判断哪些寄存器
对应DRC后者EQ呢？

