## TAS5825M Q&A

Control interface: I2C I<sup>2</sup>C

Support debugging interface, JTAG: Can't support Power supply range: 4.5V-26.4V

DVDD: 0.82mA (Deep Sleep Mode); 14.8mA (Play Mode) PVDD: 12.1uA (Deep Sleep Mode); 20.5mA (PVDD=13.5V,

LC filter=22uH+0.68uF, Fsw=384kHz, 1SPW mode)

External components PCB area (Device+ External

Compenents):

Power consumption, IQ:

3cm × 4cm (with LC filter)

Package: QFN32
Package Size: 5mm×5mm

Digital interface: I<sup>2</sup>S (TDM, I<sup>2</sup>S, LJ, RJ)

IO voltage of the digital interface: 1.8V or 3.3V SPK temperature protection: Can't support

Excursion protection: Support with Feedforward Smart Amp Algorithm,

With I<sup>2</sup>C hardware control

F0 tracker: Can't support Anti-Clipping: Can't support

Loadness( TI do not need to apply) N/A
Noise Floor(A WTD): <40uVrms

Output power on 8ohms load, 1% THD+N: 30W, PVDD=24V

Efficiency (2W/80hms, 6V supply, 1kHz):  $2\times2W$  into  $8\Omega$ , PVDD=7.4V, Efficiency>91% Efficiency (1W/80hms, 6V supply, 1kHz):  $2\times1W$  into  $8\Omega$ , PVDD=7.4V, Efficiency>89%

Efficiency by playing 'nothing to lose'

(1W/8ohms, 6V supply, 1kHz): (TI do not need to 2×1W

apply)

2×1W into 8Ω, PVDD=7.4V, Efficiency>89%

Sample rate support: 48K/96K 32kHz, 44.1kHz, 48kHz, 88.2kHz, 96kHz
Other requirement: No DSP DSP support with I<sup>2</sup>C hardware control

ESD (V) HBM 2000V latch -up (mA) +/-100mA

Antiflaming requirement (if contain any combustible material, like the plastic): For

example, oxygen index=28 N/A (Need check later)
MSL Level-2-260C-1 YEAR

ROHS ROHS Leaded free: YES

**Other Environmental** 

Requirement: N/A

Working Temperature Range -40°C to +85°C

-25°C to +85°C

Storage Temperature Range -55°C to +150°C

-40°C to +125°C