

$C = P / (V^2 * f_{sw})$   
 $= 0.1W * 50\% / (16V^2 * 2.1MHz)$   
 $= 33pF (MAX)$

LC filter (Snubber)  
 $F = C * V^2 * f_{sw}$   
 P : Resist Power and 50% derating  
 C : Snubber cap  
 V : Switching operating Max voltage  
 fsw : PWM Switching frequency

**VBAT : 14.4V/0.1A(MAX)**  
**9V/0.1A(MAX)**

**PVDD : 14.4V/3.52A(20W/2ohm)**  
**14.4V/6.24A(35W/2ohm/Max)**  
**9V/5.59A(20W/2ohm)**  
**9V/6.06A(31W/2ohm/Max)**

AMP VDD PIN 최대한 근접 배치 할것

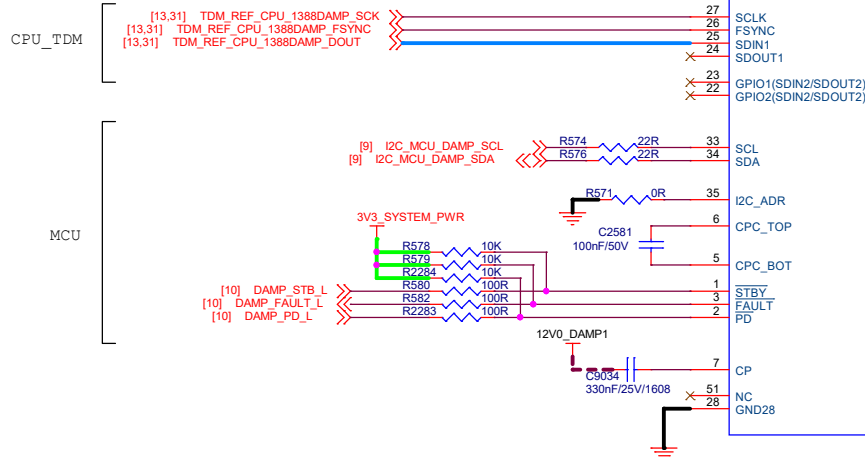
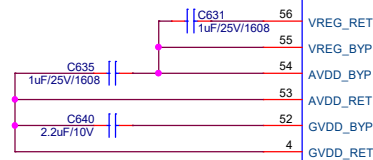
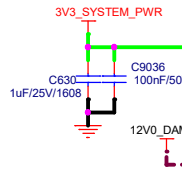
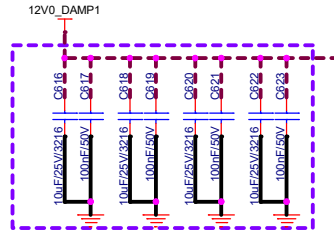


Table 8. I<sup>2</sup>C Addresses

DESCRIPTION	I <sup>2</sup> C ADDR1	I <sup>2</sup> C ADDR0	I <sup>2</sup> C Write	I <sup>2</sup> C Read
Device 0	0	0	0xD4	0xD5
Device 1	0	1	0xD6	0xD7
Device 2	1	0	0xD8	0xD9
Device 3	1	1	0xDA	0xDB

WRITE - 0xD4  
 READ - 0xD5