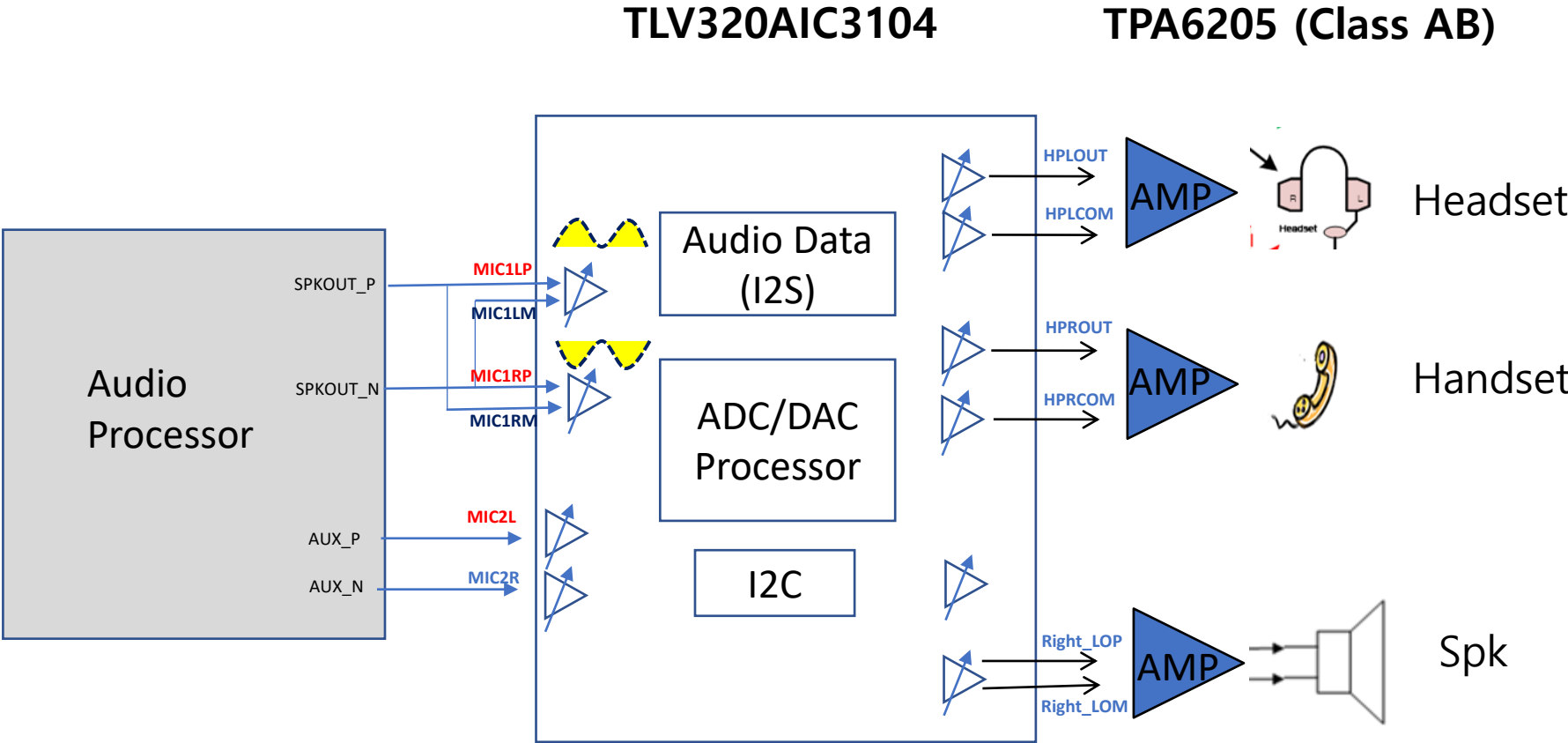


Block Diagram



Circuit Diagram

VAD_IO : 3.3V (I2S / I2S Digital Signal)

VCC 1V8 : 1.8V

Reset : Connect with VAD_IO (low during the power supply sequence)

