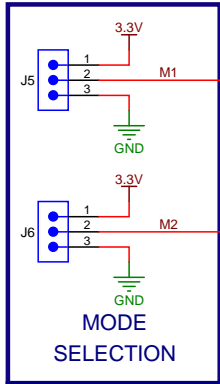
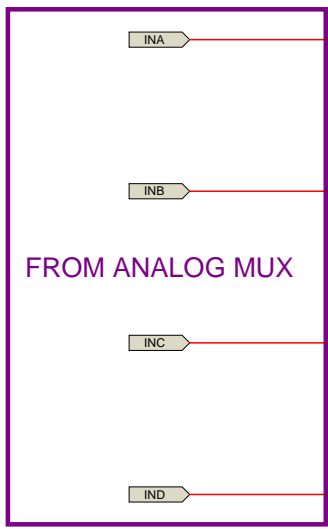
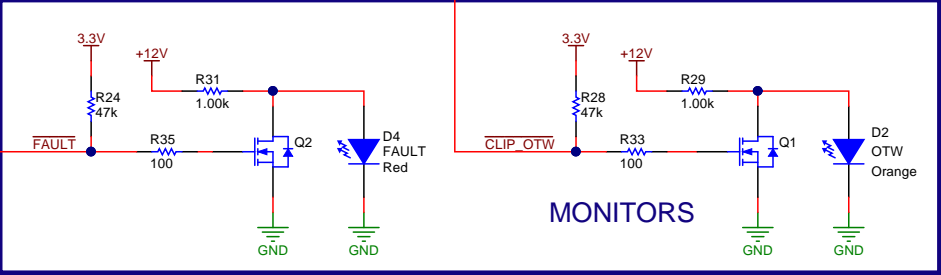
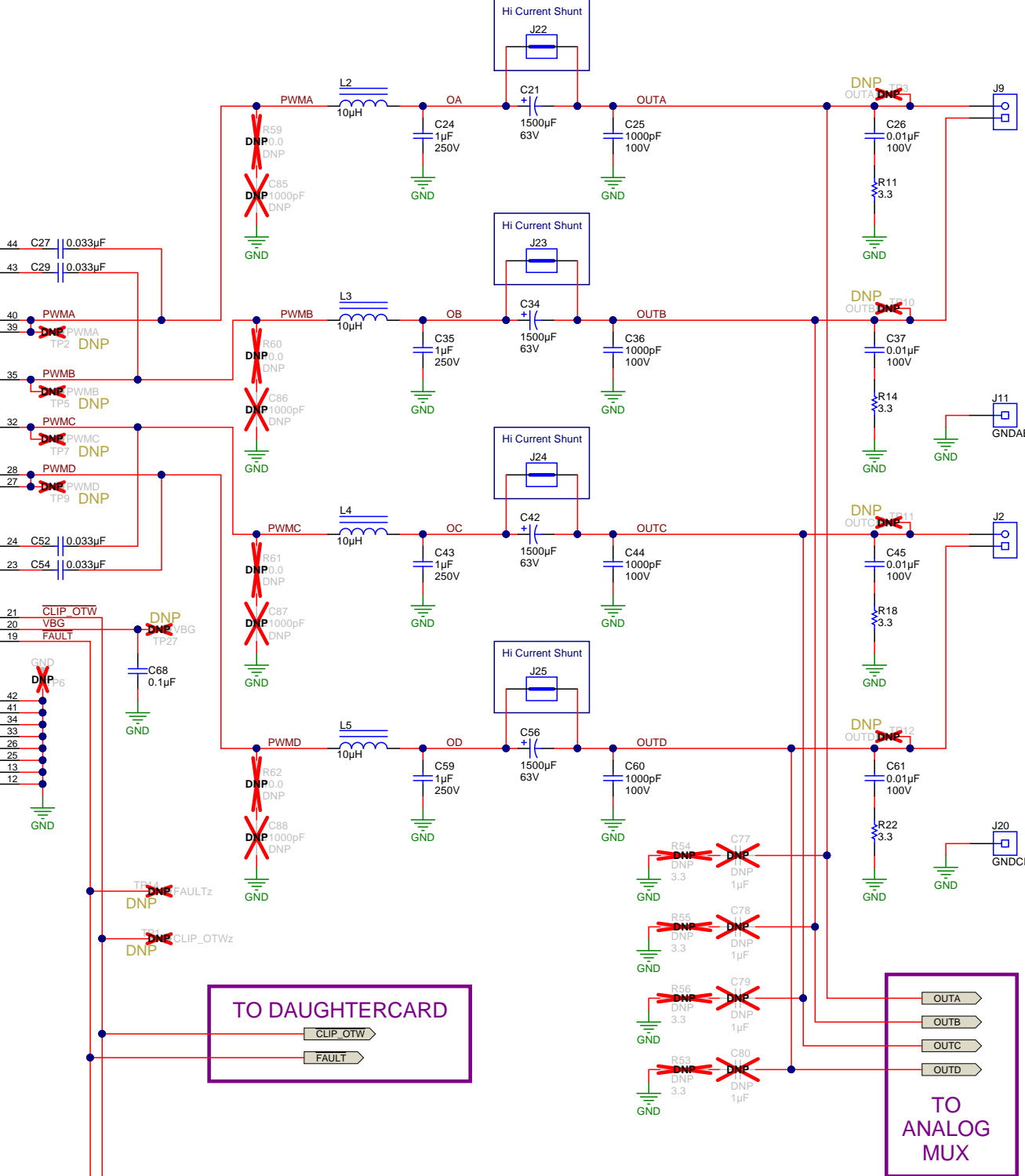
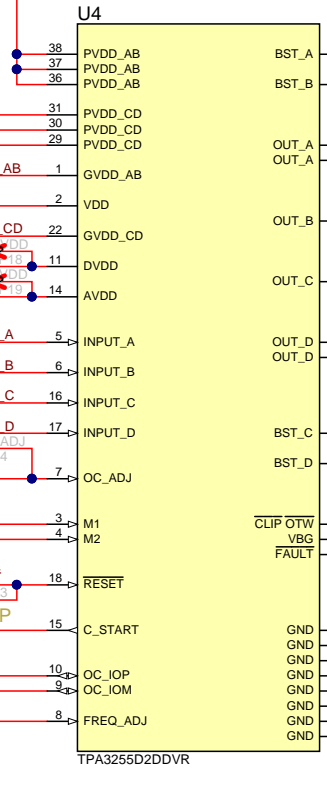
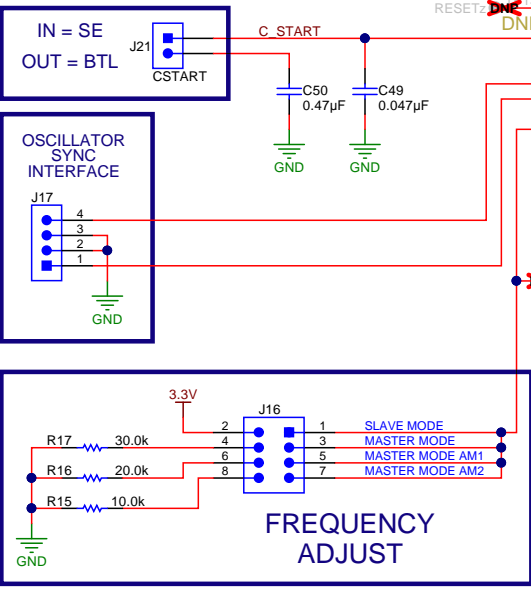
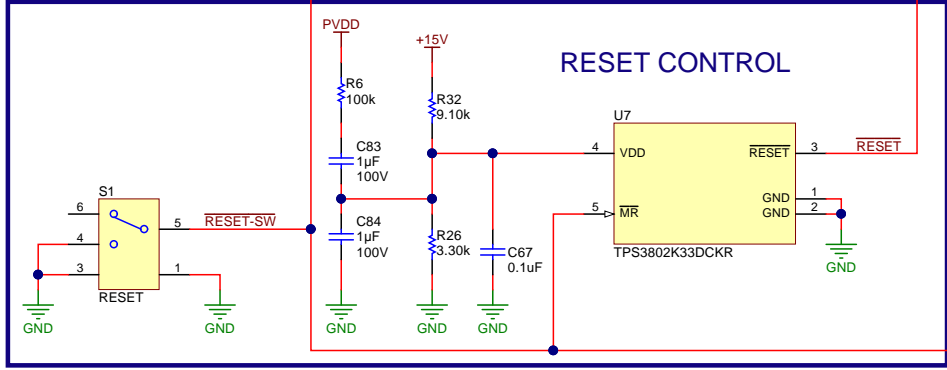


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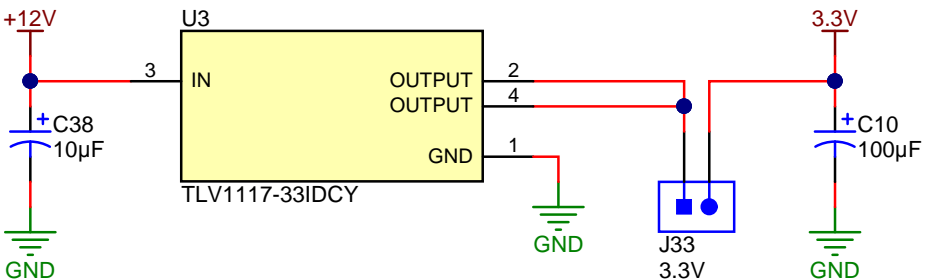
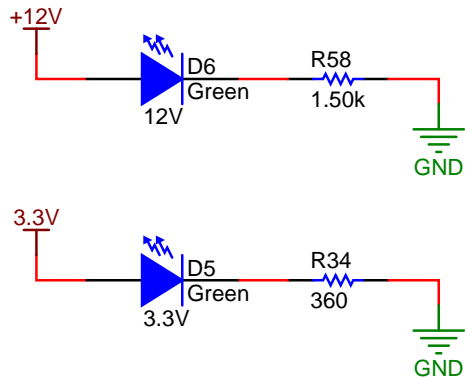
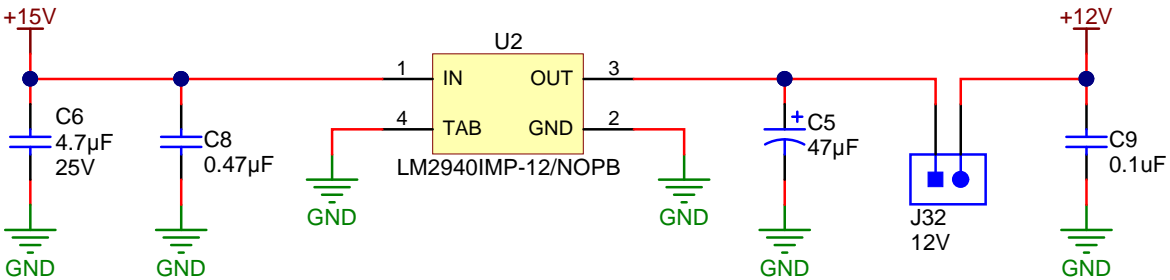
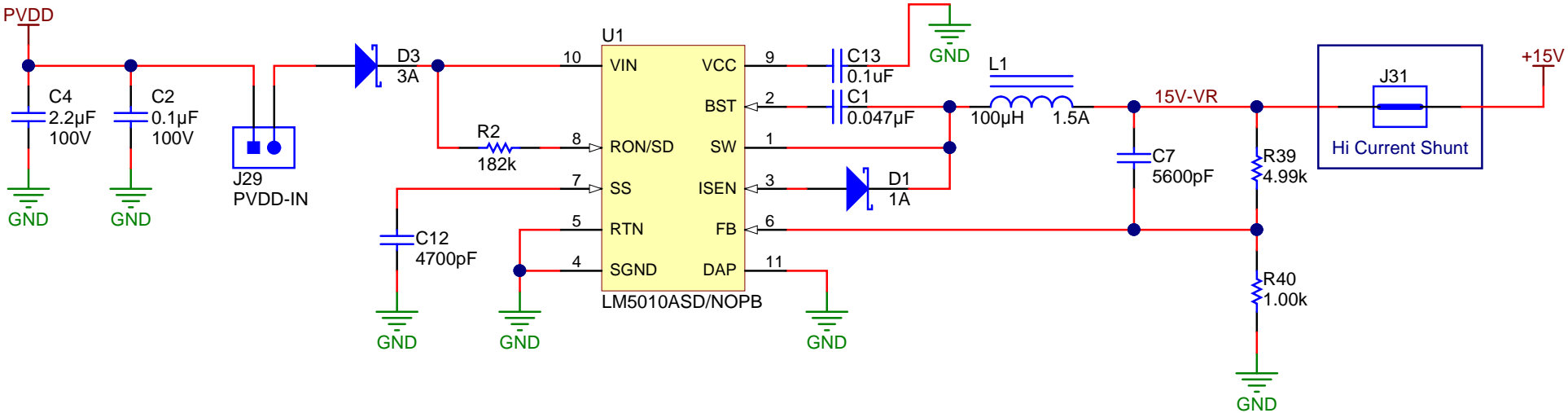
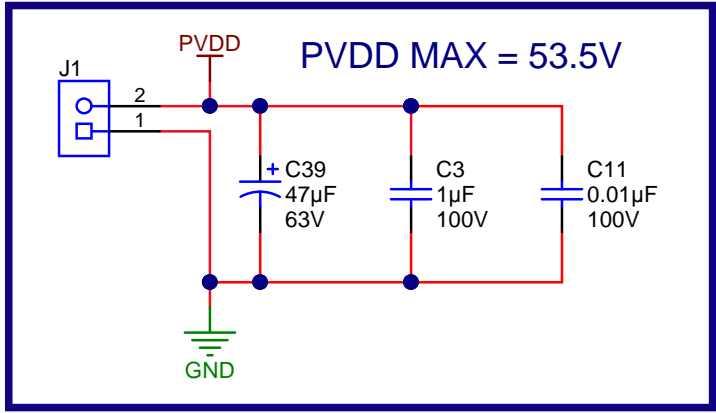
Orderable: TPA3255EVM	Designed for: Public Release	Mod. Date: 6/13/2016
TID #: N/A	Project Title: TPA3255D2DDV Evaluation Module	
Number: AAP053	Rev: A	Sheet Title: Main Schematic
SVN Rev: Version control disabled	Assembly Variant: 001	Sheet: 1 of 4
Drawn By: LDN	File: AAP053A_ANA-MUX.SchDoc	Size: B
Engineer: DAMIAN LEWIS	Contact: http://www.ti.com/support	



MODE PIN SELECTION					
M2	M1	INPUT MODE	OUTPUT	DESCRIPTION	
0	0	2N + 1	2xBTL	STEREO BTL OUTPUT, AD MODE	
0	1	2N/1N + 1	1xBTL + 2xSE	2.1 BTL + SE MODE, AD MODE	
1	0	2N + 1	1xPBTL	PARALLEL BTL OUTPUT, AD MODE	
1	1	1N + 1	4xSE	SINGLE ENDED OUTPUT, AD MODE	



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Number: AAP053

SVN Rev: Version control disabled

Drawn By: LDN

Engineer: DAMIAN LEWIS

Sheet: 3 of 4

Designed for: Public Release

Project Title: TPA3255D2DDV Evaluation Module

Sheet Title: Hardware

Assembly Variant: 001

File: AAP053A_PowerSupplies.SchDoc

Contact: <http://www.ti.com/support>

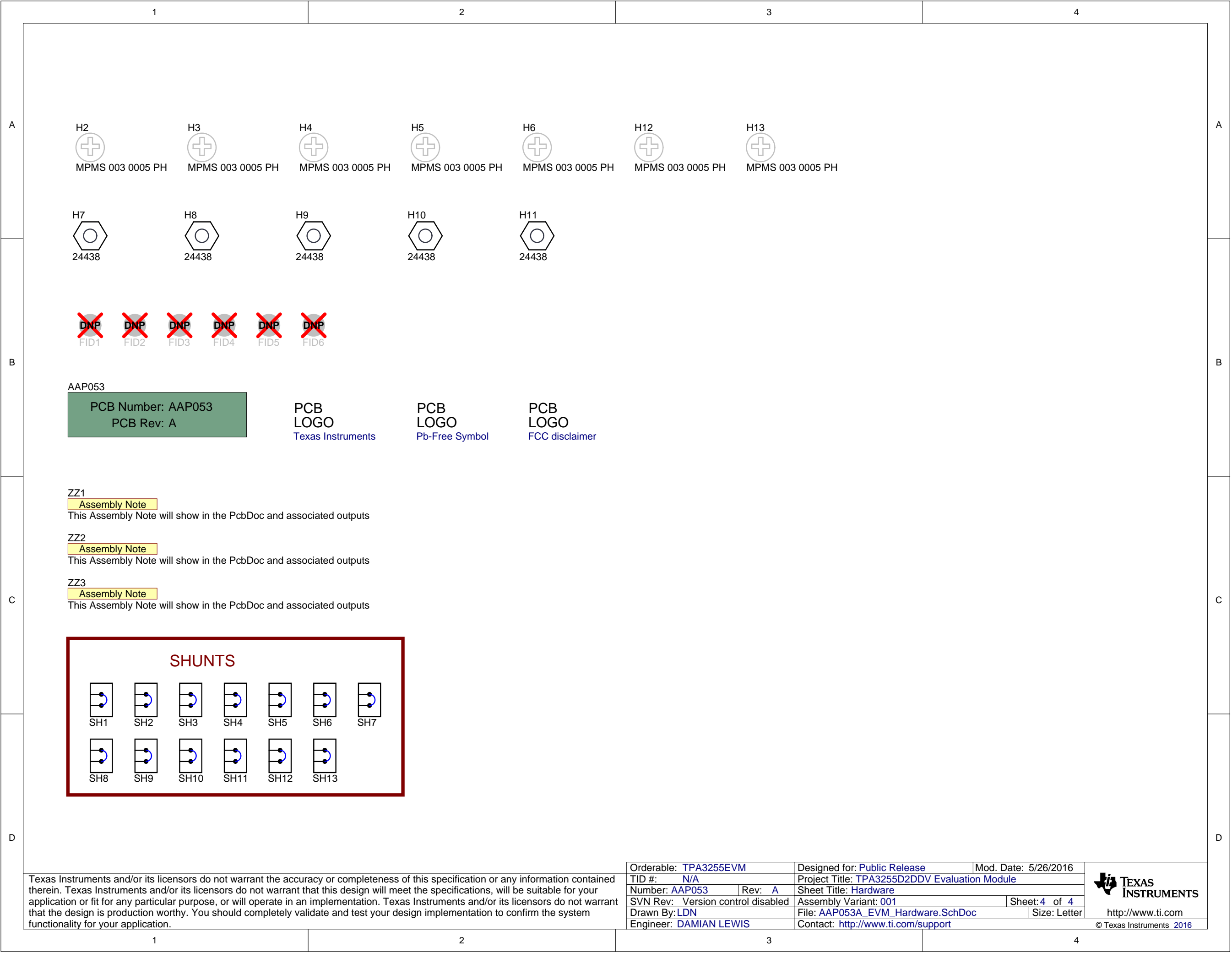
Mod. Date: 6/13/2016

Size: AB

Rev: A



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DNP

FID5



DNP

FID6

AAP053

PCB Number: AAP053

PCB Rev: A

PCB

LOGO

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PCB

LOGO

Pb-Free Symbol

PCB

LOGO

FCC disclaimer

ZZ1

Assembly Note

This Assembly Note will show in the PcbDoc and associated outputs

ZZ2

Assembly Note

This Assembly Note will show in the PcbDoc and associated outputs

ZZ3

Assembly Note

This Assembly Note will show in the PcbDoc and associated outputs

SHUNTS



SH1



SH2



SH3



SH4



SH5



SH6



SH7



SH8



SH9



SH10



SH11



SH12



SH13

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Orderable: [TPA3255EVM](#)

TID #: [N/A](#)

Number: [AAP053](#)

SVN Rev: [Version control disabled](#)

Drawn By: [LDN](#)

Engineer: [DAMIAN LEWIS](#)

Designed for: [Public Release](#)

Project Title: [TPA3255D2DDV Evaluation Module](#)

Sheet Title: [Hardware](#)

Assembly Variant: [001](#)

File: [AAP053A_EVM_Hardware.SchDoc](#)

Contact: [http://www.ti.com/support](#)

Mod. Date: [5/26/2016](#)

Sheet: [4](#) of [4](#)

Size: Letter

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