

AIC3109EVM ADC to DAC Experiment

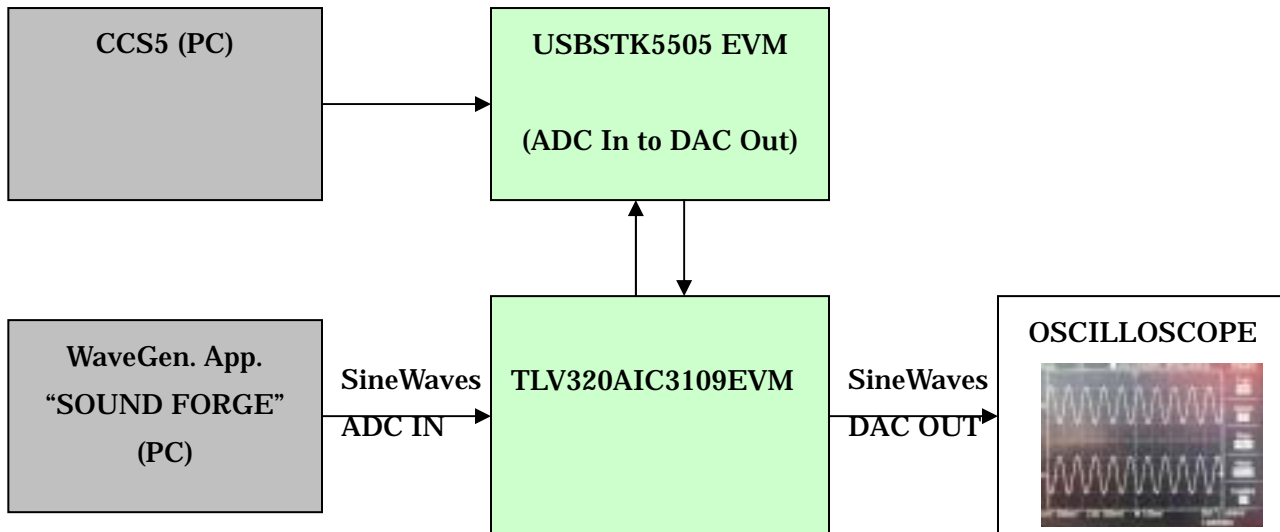
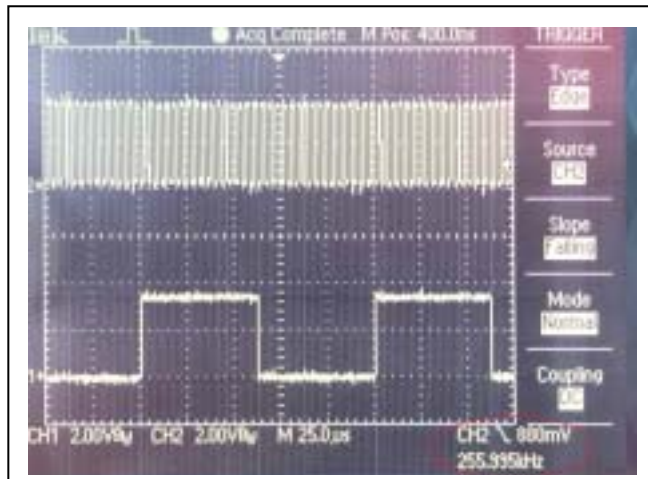
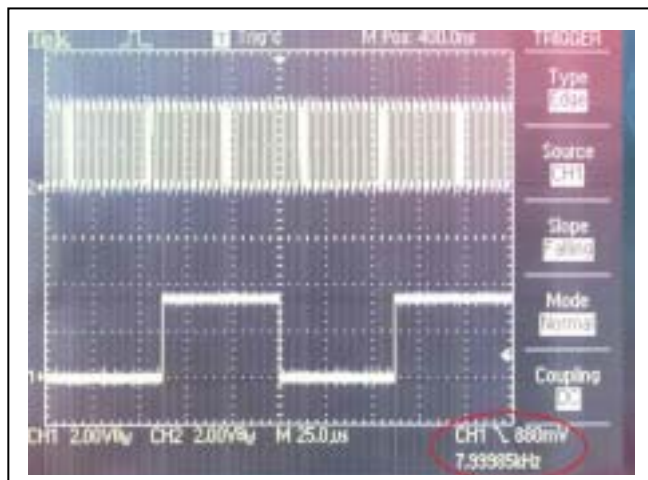


Fig.1 Structure of experiment



Picture.1 BCLK=256kHz (Upper Ch2)



Picture.2 WCLK= 8kHz (Lower Ch1)

