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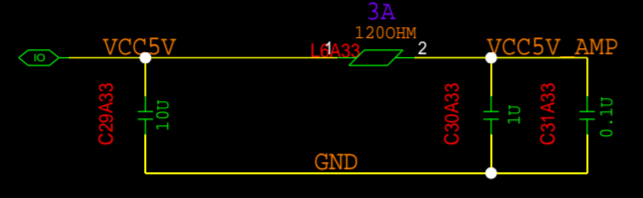
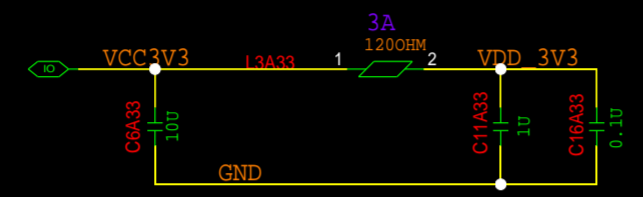
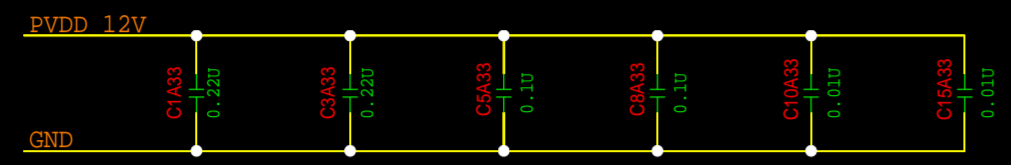
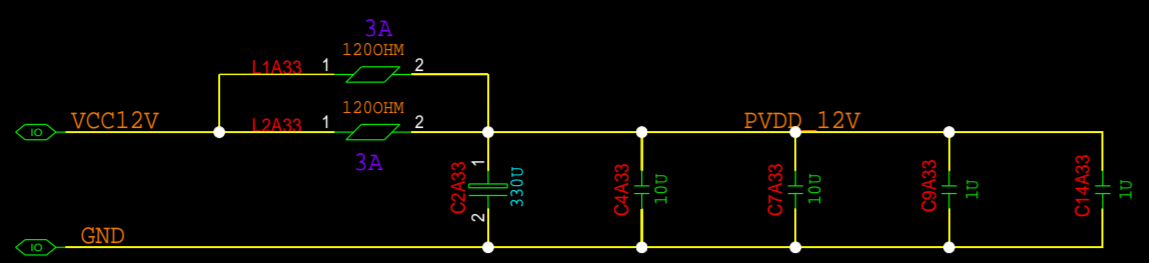


SIZE: A3

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| ZTE CORPORATION | | MODEL: | SHEET OF | |
| DRAWN | | TITLE: | | |
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A
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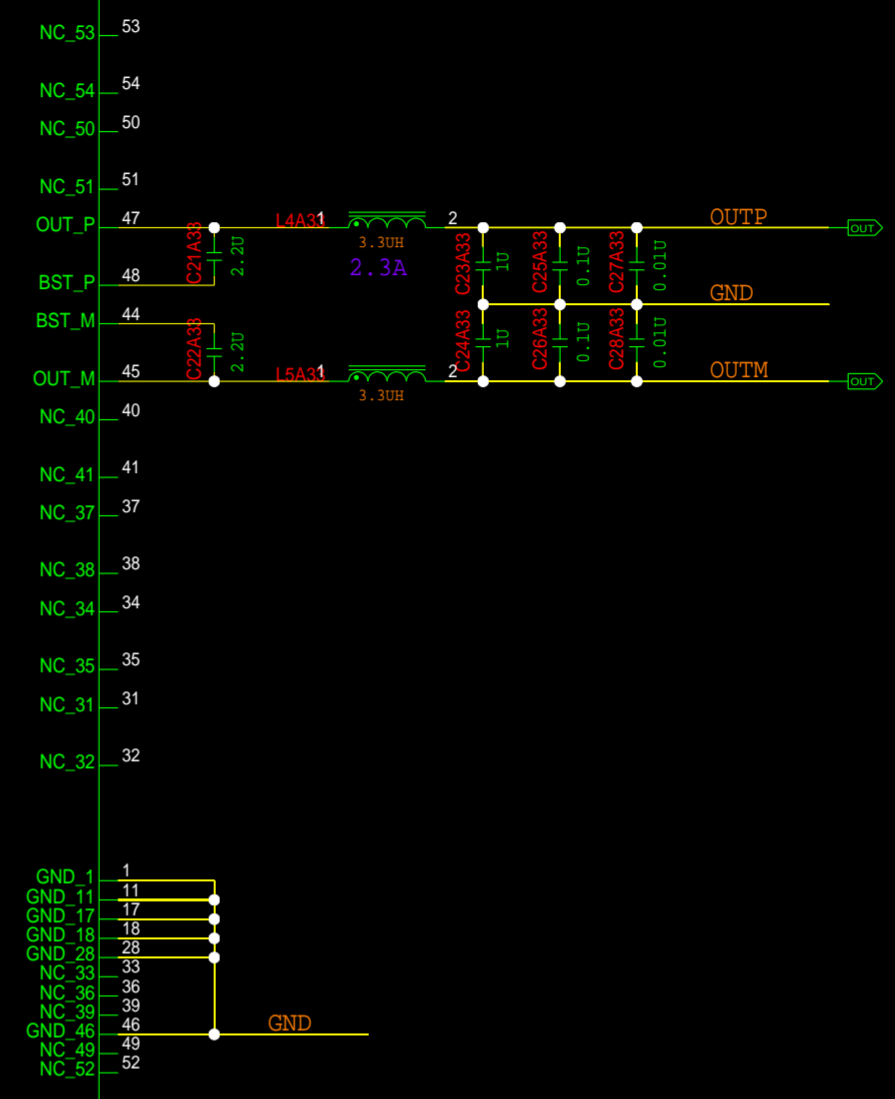
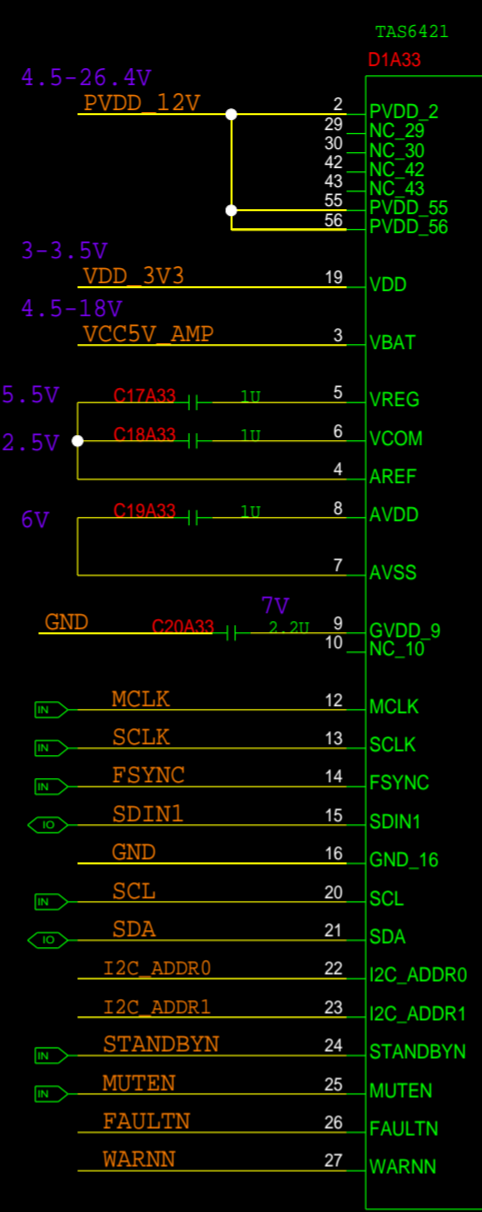
V(HIGH)=3.3V, FOR I2S
I2S NEED 33 OHM SOURCE MATCHING RESISTOR

V(HIGH)=3.3V, FOR I2C, ALREADY PULL-UP IN AD2428

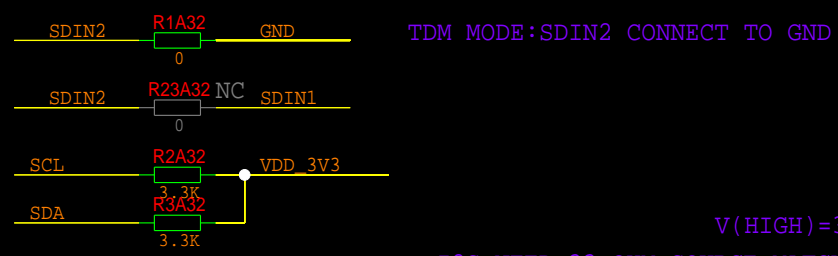
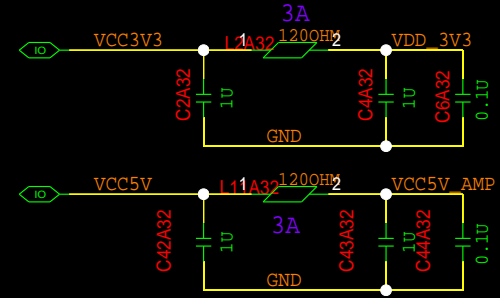
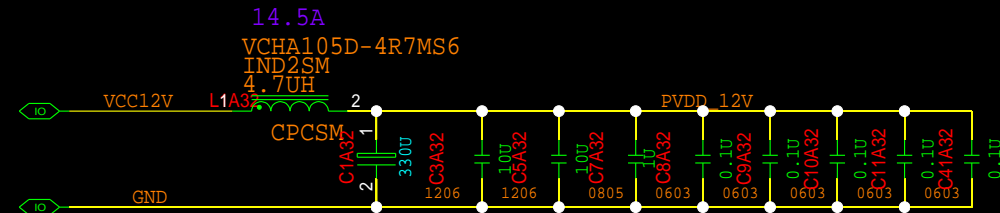
| DESCRIPTION | FC ADDR1 | FC ADDR0 | FC Write | FC Read |
|-------------|----------|----------|----------|---------|
| Device 0 | 0 | 0 | 0x04 | 0x05 |
| Device 1 | 0 | 1 | 0x06 | 0x07 |
| Device 2 | 1 | 0 | 0x08 | 0x09 |
| Device 3 | 1 | 1 | 0x0A | 0x0B |



V(HIGH)=3.3V, 100KOHM INTERNAL PULL DOWN
V(HIGH)=3.3V, 100KOHM INTERNAL PULL DOWN
V(HIGH)=3.3V, 100KOHM INTERNAL PULL UP
V(HIGH)=3.3V, 100KOHM INTERNAL PULL UP



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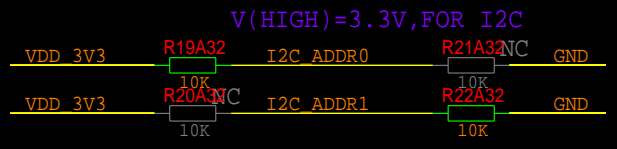


TDM MODE:SDIN2 CONNECT TO GND

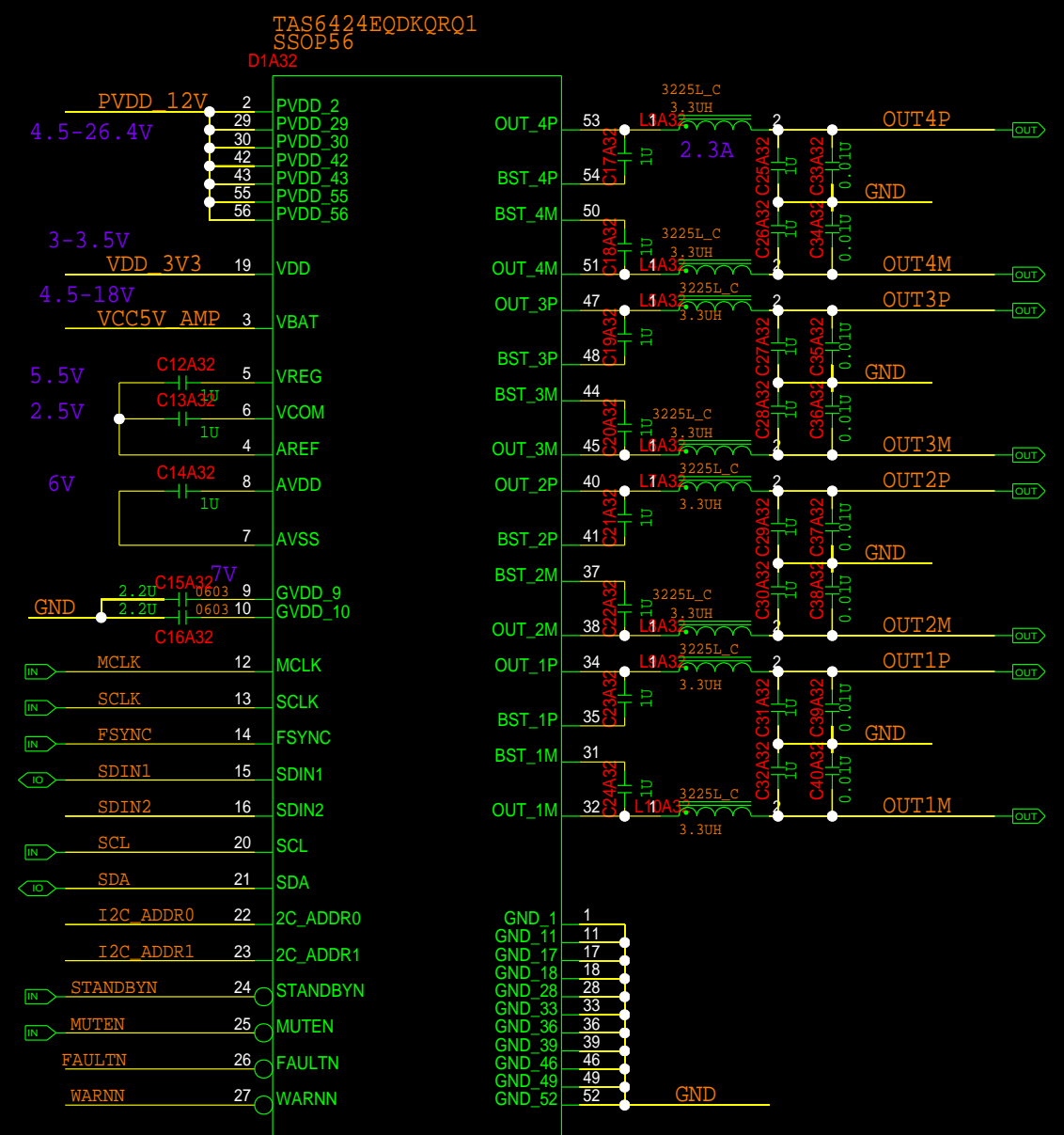
V(HIGH)=3.3V,FOR I2S
I2S NEED 33 OHM SOURCE MATCHING RESISTOR

表 8. I²C Addresses

| DESCRIPTION | I ² C ADDR1 | I ² C ADDR0 | I ² C Write | I ² C Read |
|-------------|------------------------|------------------------|------------------------|-----------------------|
| Device 0 | 0 | 0 | 0xD4 | 0xD5 |
| Device 1 | 0 | 1 | 0xD6 | 0xD7 |
| Device 2 | 1 | 0 | 0xD8 | 0xD9 |
| Device 3 | 1 | 1 | 0xDA | 0xDB |



V(HIGH)=3.3V,100KOHM INTERNAL PULL DOWN
V(HIGH)=3.3V,100KOHM INTERNAL PULL DOWN
V(HIGH)=3.3V,100KOHM INTERNAL PULL UP
V(HIGH)=3.3V,100KOHM INTERNAL PULL UP



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