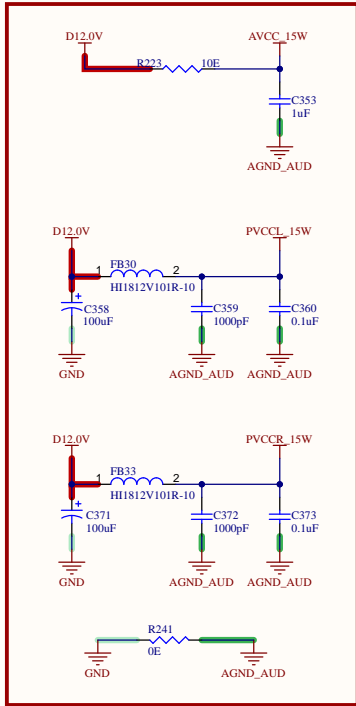


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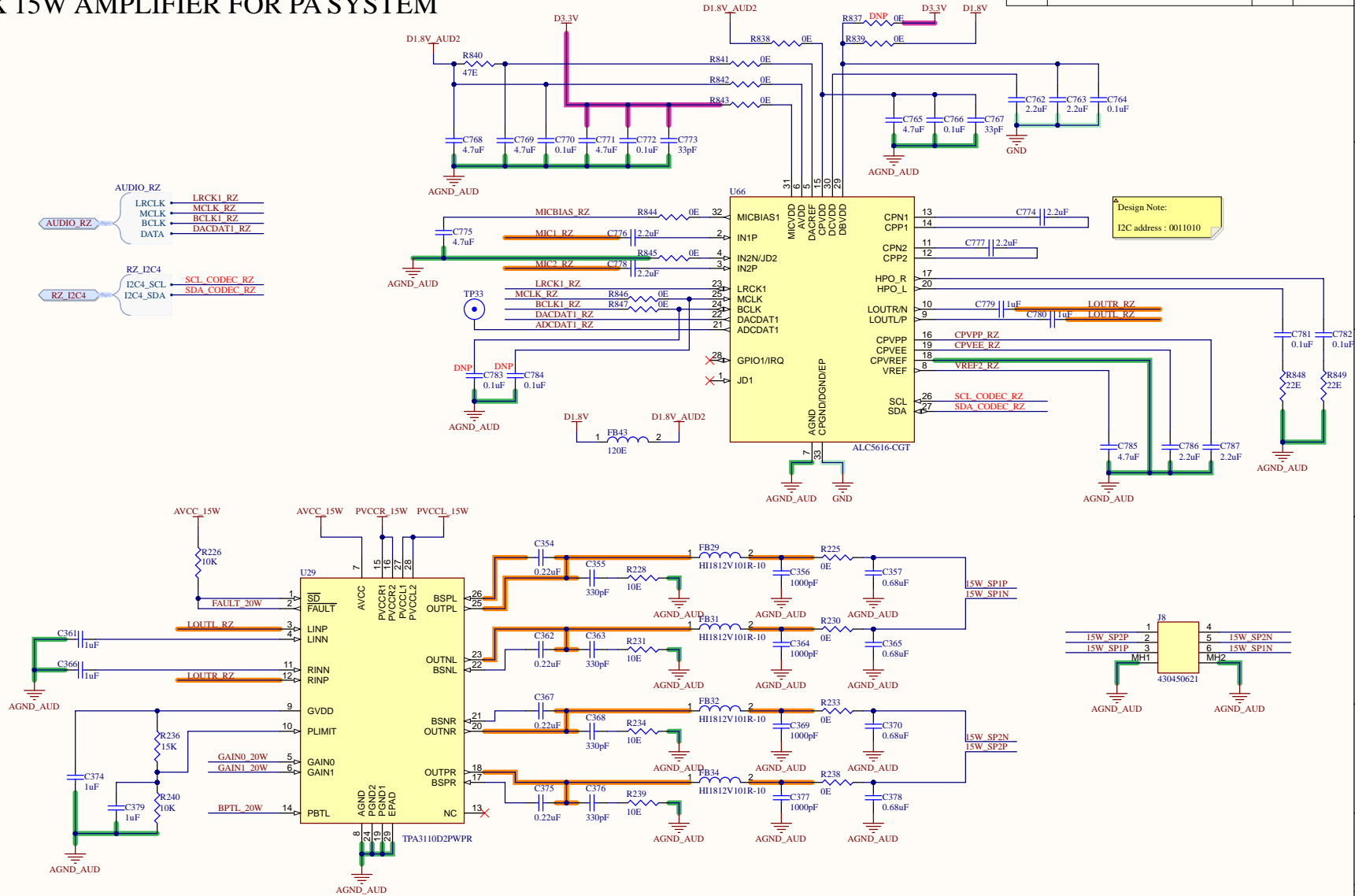
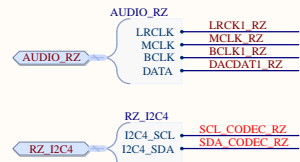
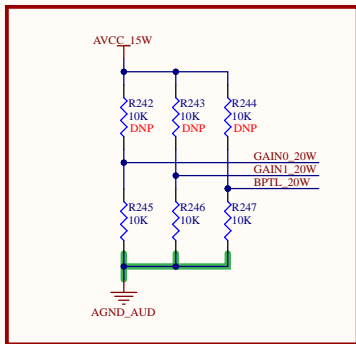
2 X 15W AMPLIFIER FOR PA SYSTEM

REV. NO.	REV. DAT.	REVISION	DESCRIPTION	DATE	APPROVED
40					

POWER SECTION



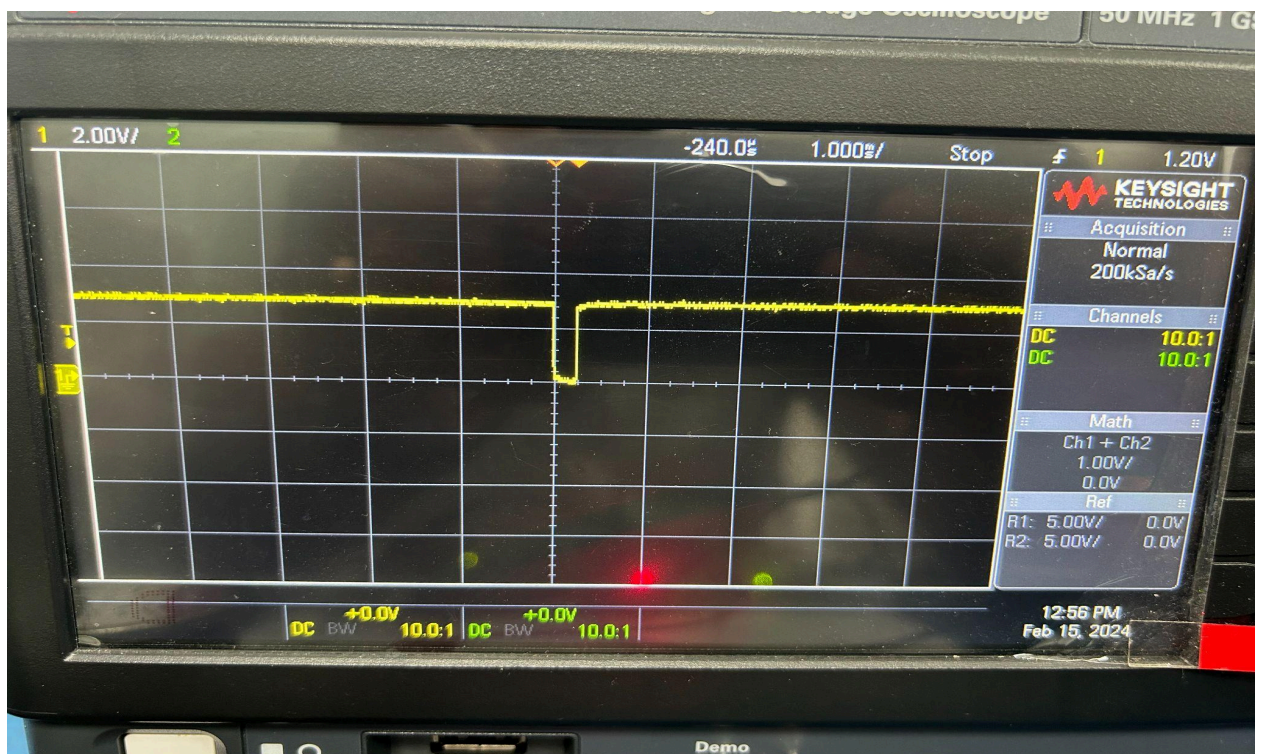
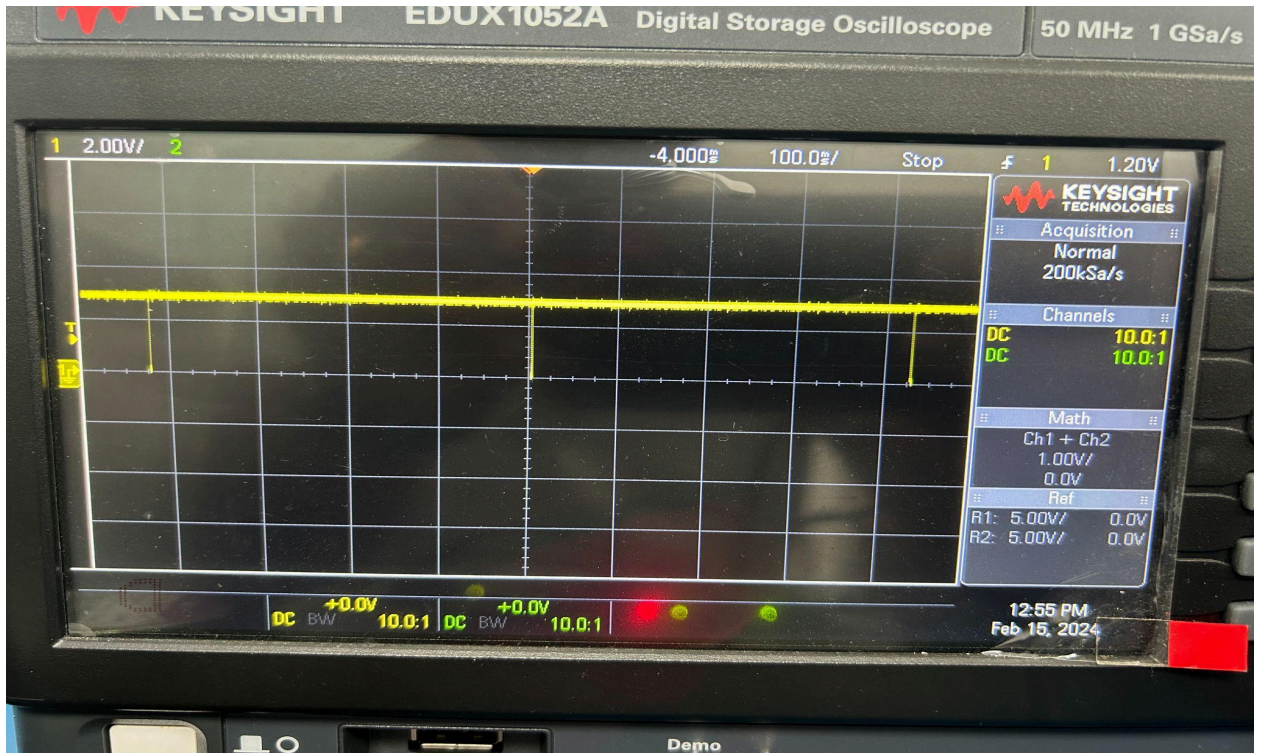
CONFIGURATION



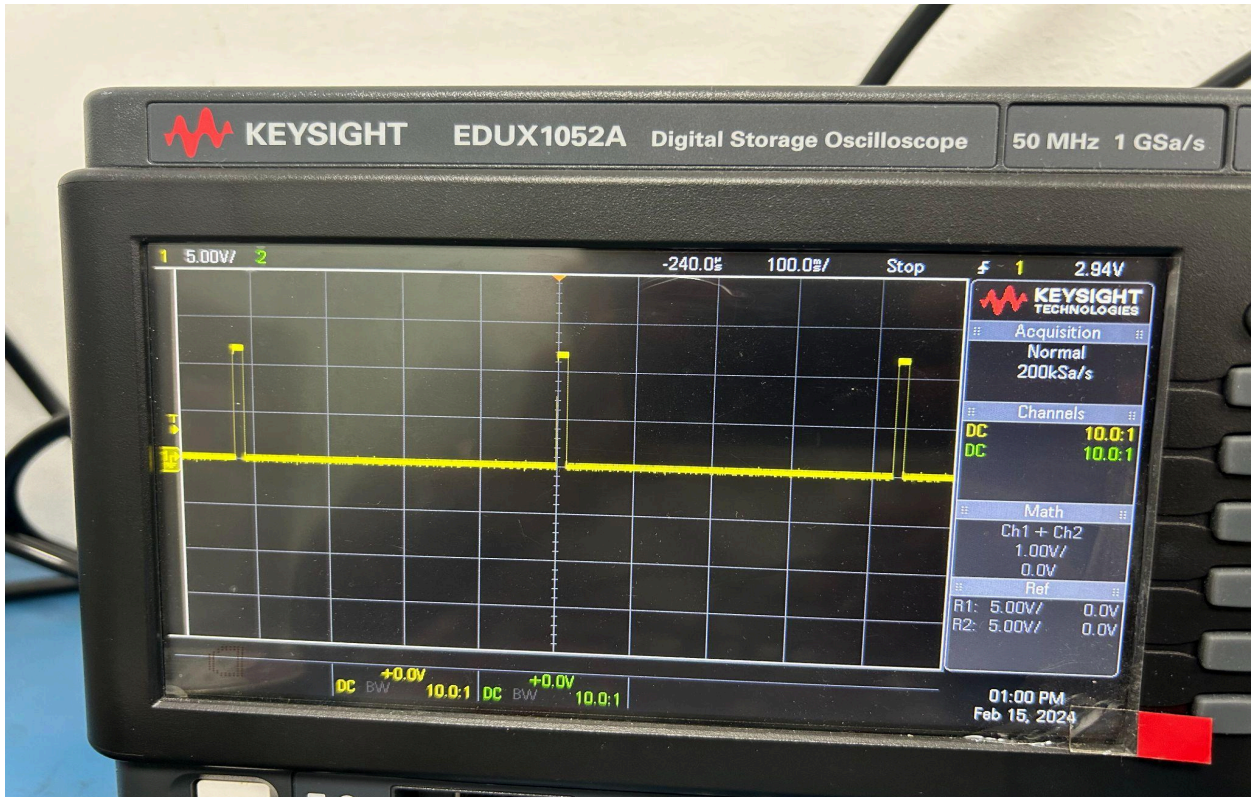
Design Note:
I2C address : 0011010

APPROVALS	DATE	PROJECT	Smart Systems
ENG: -		*	quarters - Cochin
DSN: -			legality Road,
CHK: -			Trivandrum - 682021, India
		PROJECT REVISION:	DOCUMENT REVISION:
		TITLE:	DESIGN ITEM:
		*	
BOM:		REFERENCE DOCUMENTS	
ASSY DWG:	SIZE:	CAGE CODE:	DWG NO.
FAB DWG:	B		
PCBDWG:	SCALE:	FILE NAME:	40 2X15W AMP.SchDoc
		SHEET:	40 OF 54

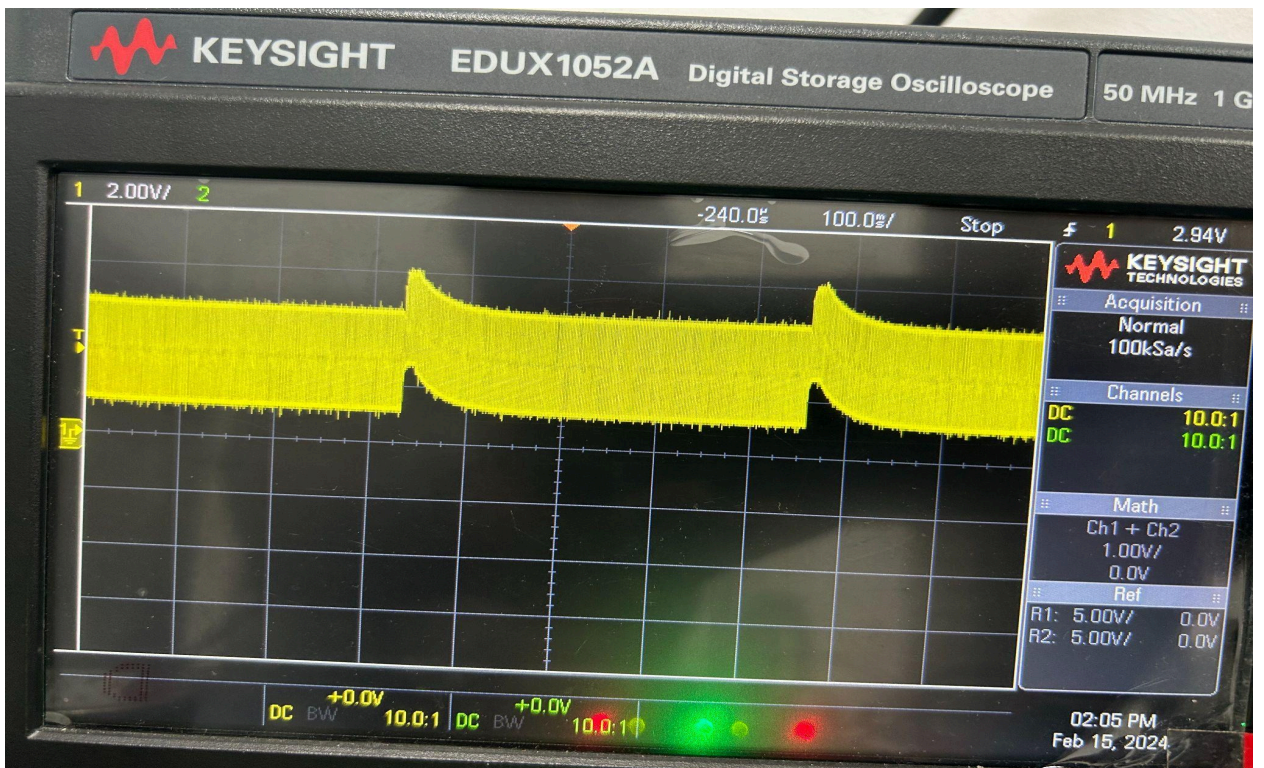
- 1) While probing the LINP and RINP pins without providing any inputs, the pins show 2.5V and then drop to 0V every 420 milliseconds. (Since the SD and FAULT pins are shorted in the circuit, I suspect this behavior is due to the chip restarting because of the DC fault detection.) Is the 2.5V observed the bias voltage?



- 2) In our circuit, we have shorted the SD and FAULT pins. While probing these pins, it shows a pulled-down state for 420 milliseconds and released for 2.5 milliseconds. (I believe this is due to a DC fault.)



- 3) While providing an input to the LINP and RINP, the probed LINP and RINP voltages are below...



- 4) Probing the speaker point without any data given to LINP and RINP, the speaker terminal shows...

