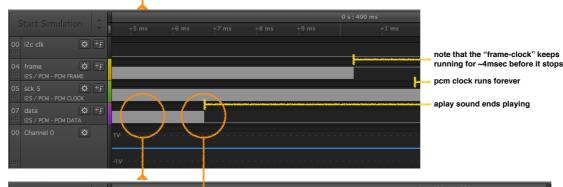


assuming one part in this is a bug in the Sitara / McAsp (I2S) driver...

question though is why the pcm1791 dac outputs this type of noise when both pcm data & frame clock is '0'..!?

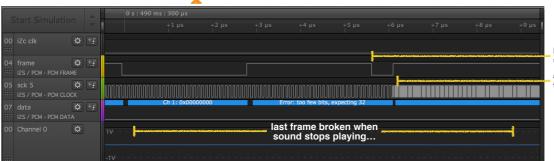
see logic analysis sequence below:

note: this issue happens around 1 in 1000, could be related to cpu-load, 'iw' / temperature.



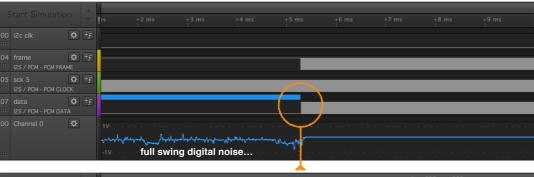
 Start Simulation
 0
 5
 48 µs
 +9 µs
 +1 µs
 +2 µ

 00
 i2c clk
 i
 i
 i
 i
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here the frame-clocck changes to an unexpected much shorter intervall

and suddenly the clock changes even though it has a fixed div setting



during the event of this noise being output from the dac, 'digital attenuation' set via i2c does not work.

next aplay starts, so the frame-clock & bit-clock changes back to expected intervals => noise goes away

