void Pcm1863\_RegsterInit (unsigned char idx)

{

      pError = 0;

      unsigned char ret=0;

      sWrite\_i2c (0, 0);                // switch to page 0;

      if (idx == PCM1863\_I2S\_MASTER\_IDX)

      {

           sWrite\_i2c (REG\_DEV\_CLOCK\_SET, 0x11);//#define REG\_DEV\_CLOCK\_SET        0x20

           sWrite\_i2c (REG\_DEV\_SCK\_DIVIDER, 0x03);//#define REG\_DEV\_SCK\_DIVIDER      0x26

           sWrite\_i2c (REG\_DEV\_BCK\_DIVIDER, 0x3F);//#define REG\_DEV\_BCK\_DIVIDER      0x27

      }

      else

      {

           sWrite\_i2c (REG\_DEV\_CLOCK\_SET, 0x01);

      }

      sWrite\_i2c (REG\_ADC1L\_INPUT, RBIT\_REG\_6\_8\_DIFF\_1);//#define REG\_ADC1L\_INPUT            0x06

      sWrite\_i2c (REG\_ADC1R\_INPUT, RBIT\_REG\_7\_9\_DIFF\_2);//#define REG\_ADC1R\_INPUT            0x07

      sWrite\_i2c (REG\_PGA\_VAL\_CH1\_L, 0);   //#define REG\_PGA\_VAL\_CH1\_L       0x01

      sWrite\_i2c (REG\_PGA\_VAL\_CH1\_R, 0);  //#define REG\_PGA\_VAL\_CH1\_R       0x02

      sWrite\_i2c (REG\_PGA\_VAL\_CH2\_L, 0);   //#define REG\_PGA\_VAL\_CH2\_L       0x03

      sWrite\_i2c (REG\_PGA\_VAL\_CH2\_R, 0);  //#define REG\_PGA\_VAL\_CH2\_R       0x04

      sWrite\_i2c (0x00, 0x00);   // page 0

      sWrite\_i2c (0x70, 0x70);   // is power on mode（0x70）, on sleep mode

      sWrite\_i2c (0x00, 0xfd);    // page 253

      sWrite\_i2c (0x14, 0x10);   // Global bias current trim 75%

      //====end======//

      #if 1

      //set threshold of singal (SLEEP TO ACTIVE) -30dB

      sWrite\_i2c (0x00, 0x01);   // page 1

      sWrite\_i2c (0x02, 0x2d);

      sWrite\_i2c (0x04, 0x00);//-62dB

      sWrite\_i2c (0x05, 0x1a);

      sWrite\_i2c (0x06, 0x07);

      sWrite\_i2c (0x01, 0x01);   // excute to write

      vTaskDelay (100 / portTICK\_RATE\_MS);

      //DelayMs(100);

      sWrite\_i2c (0x00, 0x01);   // page 1

      sWrite\_i2c (0x02, 0x2c);

      sWrite\_i2c (0x04, 0x00);//-62dB

      sWrite\_i2c (0x05, 0x1a);

      sWrite\_i2c (0x06, 0x07);

      sWrite\_i2c (0x01, 0x01);   // excute to write

      vTaskDelay (100 / portTICK\_RATE\_MS);

      //DelayMs(100);

      #endif

      sWrite\_i2c (0x00, 0x00);   // page 0

      sWrite\_i2c (0x60, 0x01);   // enable ENG Interrup.

      sWrite\_i2c (0x34, 0x01);   // set time from 1 ~ 30 mins

      sWrite\_i2c (0x31, 0xFF);

      sWrite\_i2c (0x36, 0x00);   // NO repeat

      sWrite\_i2c (0x00, 0x00);   // page 0

      sWrite\_i2c (0x70, 0x72);// is power on mode, on sleep mode(0X72)

      sWrite\_i2c (PGA\_CONTROL\_MAPPING, 0xF0);//#define PGA\_CONTROL\_MAPPING  0x19

      SwI2Cx\_BusSel (I2C\_BUS\_NUM);

      if (pError)

      {

           UART\_DEBUGF (PCM1863\_DBG, ("PCM1863[%d] init failed\r\n", idx));

      }

      else

      {

           //UART\_DEBUGF (PCM1863\_DBG, ("PCM1863 init finished\r\n"));

      }

      pcmSleepMode[idx] = true;

      return;

}

void ADCInputGainSet (unsigned char inputIndx, unsigned char gainIndx)

{

      unsigned char reg;

      if(gainIndx<PCM1863\_GAININDEX\_MIN||gainIndx>PCM1863\_GAININDEX\_MAX)

           return;

      if (inputIndx < IDX\_PCM1863\_NUM)

      {

           pcmChip\_sel (inputIndx);

           SwI2Cx\_BusSel (PCM1863\_I2C\_BUS\_BASE + (inputIndx / 2));

           //range of gain 0 ~ 24; step 0.5dB  -> 0dB ~ +12dB

           reg = gainIndx;

           //sWrite\_i2c (REG\_PGA\_VAL\_CH1\_L, reg);

           //sWrite\_i2c (REG\_PGA\_VAL\_CH1\_R, reg);

           sWrite\_i2c (DPGA\_VAL\_CH1\_L, 0x40|reg); //#define DPGA\_VAL\_CH1\_L   0x0F

           sWrite\_i2c (DPGA\_VAL\_CH1\_R,  0x40|reg);// #define DPGA\_VAL\_CH1\_R     0x16

           sWrite\_i2c (DPGA\_VAL\_CH2\_L, 0x40|reg); //#define DPGA\_VAL\_CH2\_L   0x17

           sWrite\_i2c (DPGA\_VAL\_CH2\_R,  0x40|reg);// #define DPGA\_VAL\_CH2\_R     0x18

           SwI2Cx\_BusSel (I2C\_BUS\_NUM);

      }