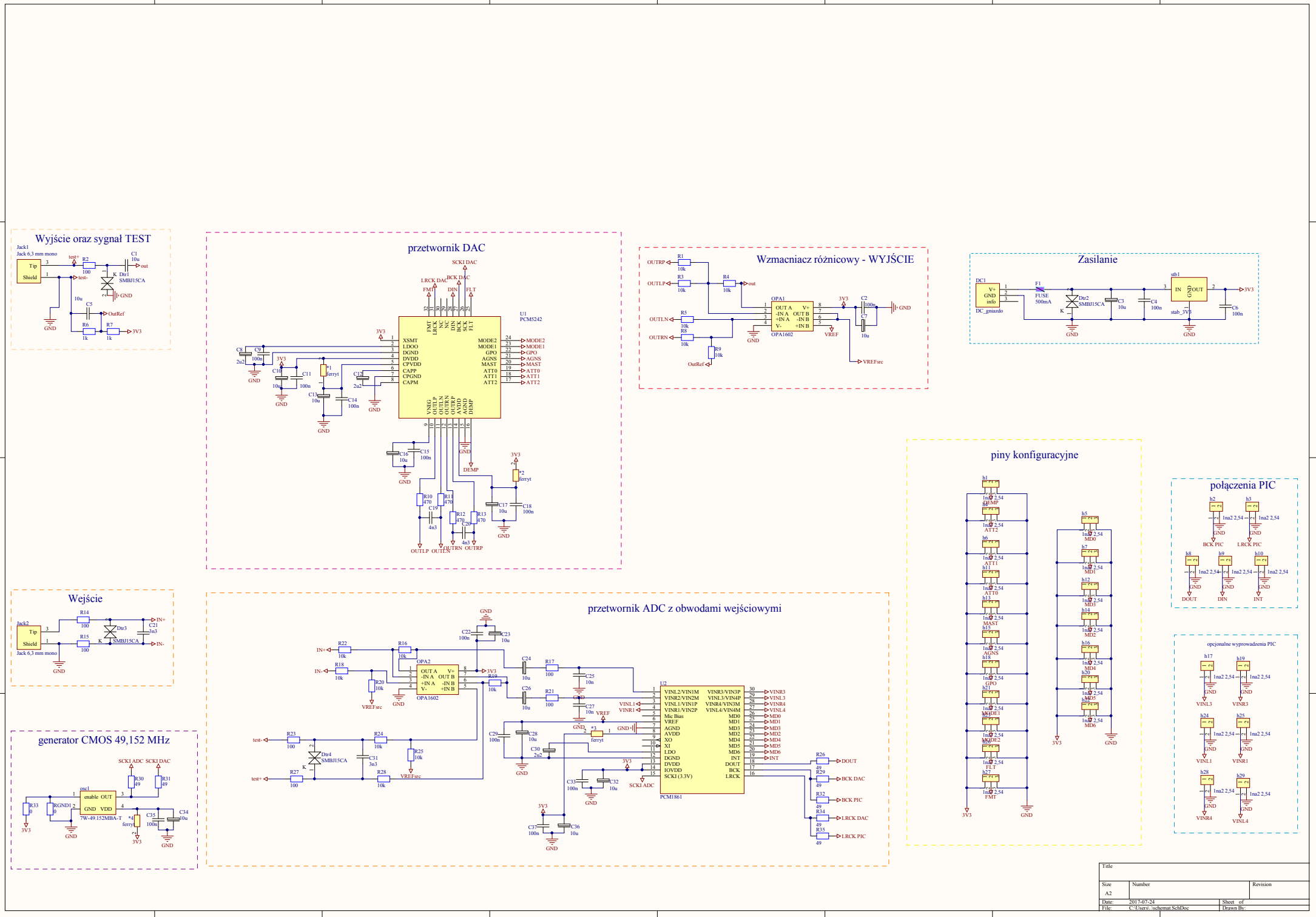
Wyjście oraz sygnał TEST
Jack1
Jack 6,3 mm mono
Tip
Shield
test+
test-
R2
100
C1
10u
out
Dtr1
SMBJ15CA
GND
10u
C5
OutRef
R6
1k
R7
1k
3V3
przetwornik DAC
SCKI DAC
LRCK DAC
BCK DAC
FMT
DIN
FLT
U1
PCM5242
XSMT
LDOO
DGND
DVDD
CPVDD
CAPP
CPGND
CAPM
MODE2
MODE1
GPO
AGNS
MAST
ATT0
ATT1
ATT2
VNEG
OUTLP
OUTLN
OUTRN
OUTRP
AVDD
AGND
DEMP
C8
2u2
C9
100n
C10
10u
C11
100n
C12
2u2
C13
10u
C14
100n
C15
100n
C16
10u
C17
10u
C18
100n
C19
4n3
C20
4n3
R10
470
R11
470
R12
470
R13
470
OUTLP
OUTLN
OUTRN
OUTRP
Wzmacniacz różnicowy - WYJŚCIE
OUTRP
OUTLP
OUTLN
OUTRN
R1
10k
R3
10k
R4
10k
R5
10k
R8
10k
R9
10k
OutRef
OPA1
OPA1602
C2
100n
C7
10u
VREF
VREFsrc
Zasilanie
DC1
V+
GND
info
DC_gniazdo
F1
FUSE
500mA
Dtr2
SMBJ15CA
C3
10u
C4
100n
stb1
IN
OUT
stab_3V3
C6
100n
3V3
Wejście
Jack2
Jack 6,3 mm mono
R14
100
R15
100
Dtr3
SMBJ15CA
C21
3n3
IN+
IN-
generator CMOS 49,152 MHz
SCKI ADC
SCKI DAC
R30
49
R31
49
osc1
enable
OUT
GND
VDD
7W-49.152MBA-T
R33
0
R34
0
C35
100n
C34
10u
ferryt
3V3
GND
przetwornik ADC z obwodami wejściowymi
IN+
IN-
R22
10k
R16
10k
R18
10k
R20
10k
OPA2
OPA1602
VREFsrc
C22
100n
C23
10u
R19
10k
C24
10u
R17
100
C25
10n
C26
10u
R21
100
C27
10n
VREF
test-
test+
R23
100
R27
100
Dtr4
SMBJ15CA
R24
10k
R25
10k
R28
10k
C31
3n3
C29
100n
C28
10u
C30
2u2
U2
PCM1861
VINL2/VIN1M
VINR2/VIN2M
VINL1/VIN1P
VINR1/VIN2P
Mic Bias
VREF
AGND
AVDD
XO
XI
LDO
DGND
DVDD
IOVDD
SCKI (3.3V)
VINR3/VIN3P
VINL3/VIN4P
VINR4/VIN3M
VINL4/VIN4M
MD0
MD1
MD3
MD2
MD4
MD5
MD6
INT
DOUT
BCK
LRCK
VINL1
VINR1
VINR3
VINL3
VINR4
VINL4
MD0
MD1
MD3
MD2
MD4
MD5
MD6
INT
R26
49
R29
49
R32
49
R34
49
R35
49
DOUT
BCK DAC
BCK PIC
LRCK DAC
LRCK PIC
C33
100n
C32
10u
C37
100n
C36
10u
SCKI ADC
piny konfiguracyjne
h1
h2
h3
h4
h5
h6
h7
h8
h9
h10
h11
h12
h13
h14
h15
h16
h17
h18
h19
h20
h21
h22
h23
h24
h25
h26
h27
h28
h29
ATT2
ATT1
ATT0
MAST
AGNS
GPO
MDE1
MDE2
FLT
FMT
MD0
MD1
MD3
MD2
MD4
MD5
MD6
połączenia PIC
BCK PIC
LRCK PIC
DOUT
DIN
INT
opcjonalne wyprowadzenia PIC
VINL3
VINR3
VINL1
VINR1
VINR4
VINL4
Title
Size
A2
Number
Revision
Date:
2017-07-24
Sheet of
File:
C:\Users\...\schemat.SchDoc
Drawn By:

Board S