

Layer 1 Top

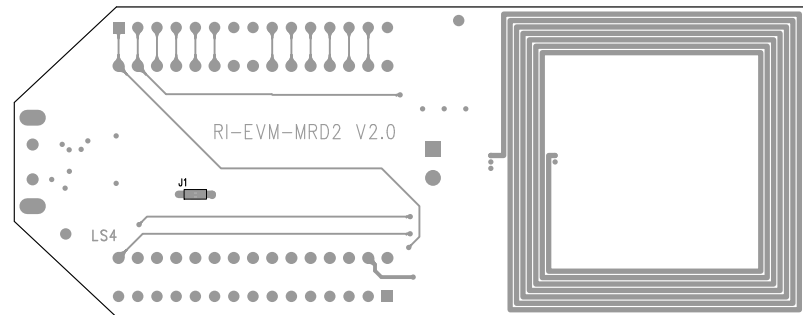
Bestueckungsplan(Assembly) Top

Texas Instruments Freising
 RI-EVM-MRD2 V2.0

File: RI-EVM-MRD2-V2-0
 Th.Huber 08-NOV-2011

RI-EVM-MRD2-V2.0
08-NOV-2011
Tr.Huber

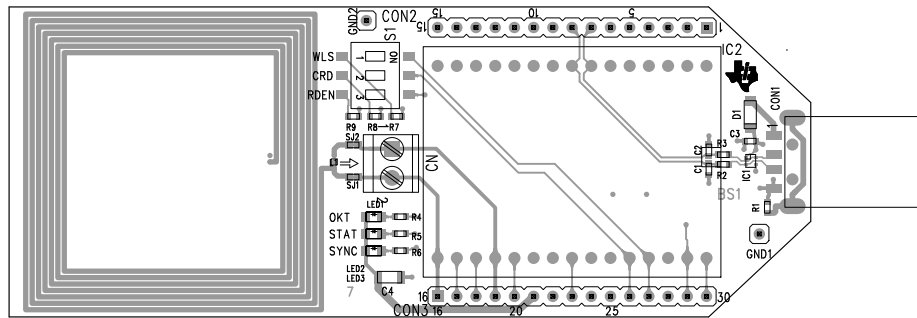
RI-EVM-MRD2 V2.0
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Bestueckungsplan(Assembly) Bottom

Layer 4 Bottom

! Clear silkscreen/legend, to have no ink on solderpad !



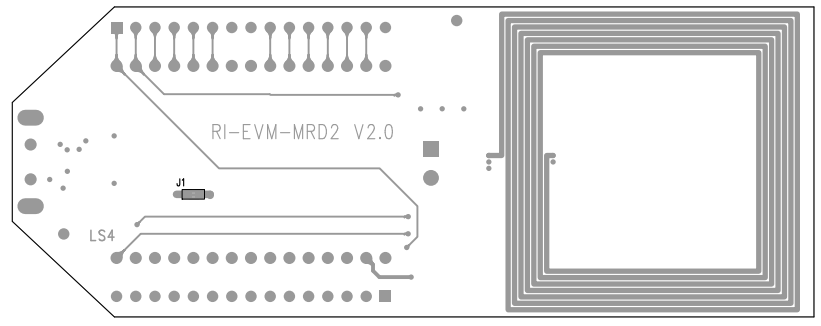
Layer 1 Top

Bestueckungsdruck(Legend) Top

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! Clear silkscreen/legend, to have no ink on solderpad !

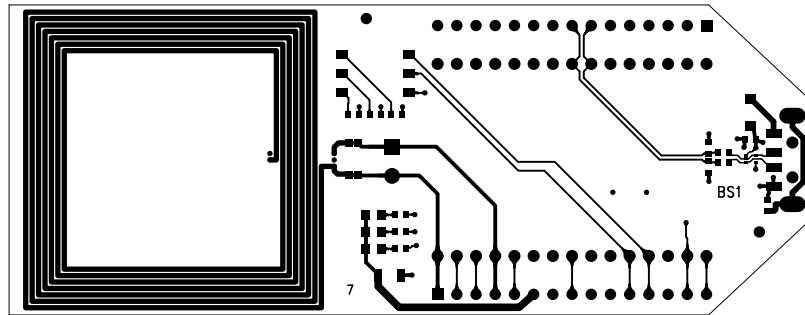


Bestueckungsdruck(Legend) Bottom

Layer 4 Bottom

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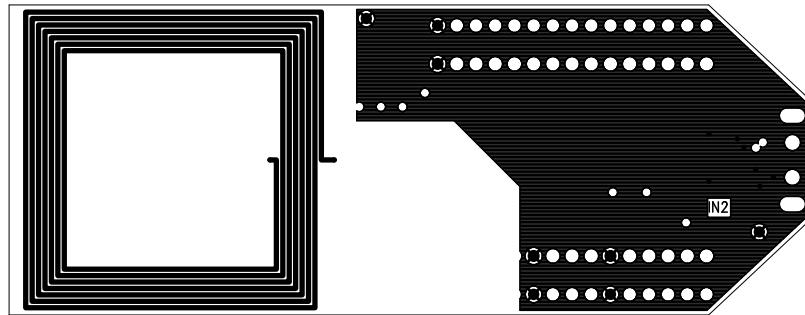
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Layer 1 Top

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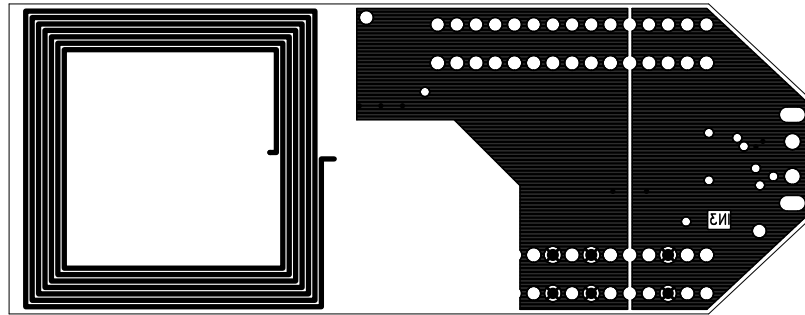
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Inner Layer 2

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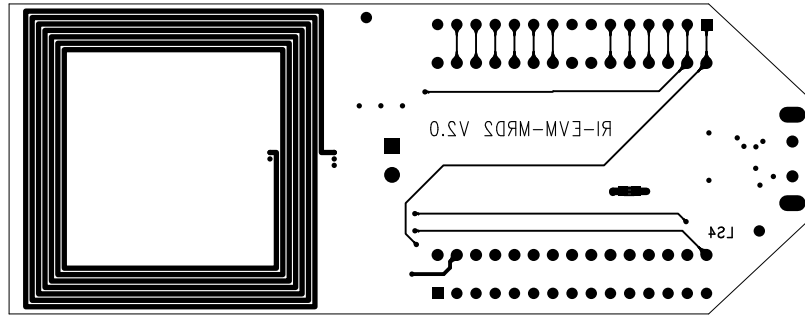
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3. Inner Layer

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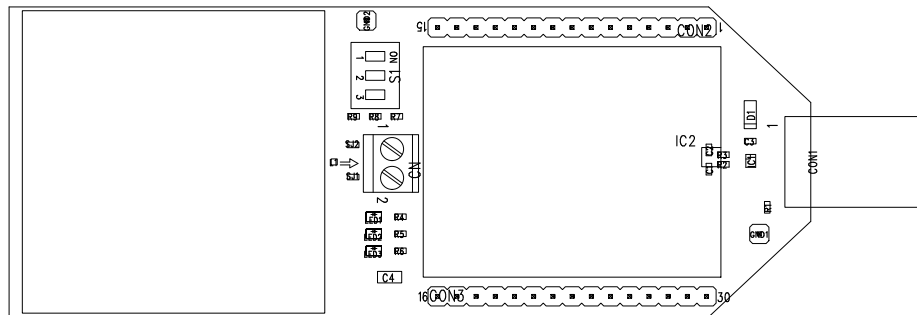
File: RI-EVM-MRD2-V2-0
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Layer 4 Bottom

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RI-EVM-MRD2 V2.0

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Bestueckungsplan(Assembly) Top

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 RI-EVM-MRD2 V2.0

File: RI-EVM-MRD2-V2-0
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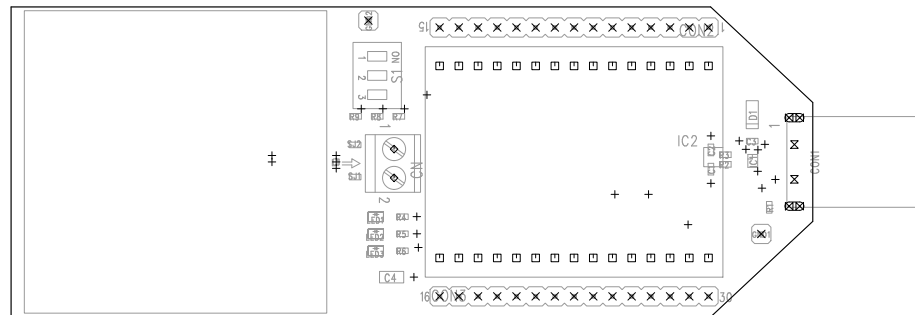
RI-EVM-MRD3-V3-0
Tr.Huber 08-NOV-2011
Date: RI-EVM-MRD3-V3-0

RI-EVM-MRD3 V3.0
Texas Instruments Freising



Bestueckungsplan(Assembly) Bottom

SIZE	QTY	SYM	PLATED	TOL
0.3001	25	+	YES	Via
0.9	32	X	YES	+0.1/-0.05
1	30	□	YES	+0.1/-0.05
1.1	2	◇	YES	+0.1/-0.05
1.1 x 2.5	2	⊠	YES	+0.1/-0.05slot
1.2	2	⊗	YES	+0.1/-0.05

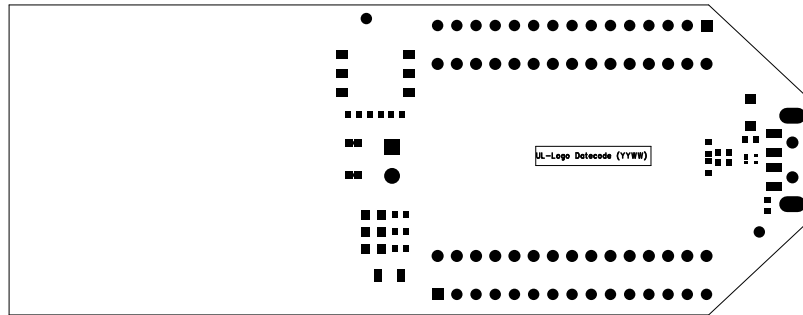


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RI-EVM-MRD2 V2.0

File: RI-EVM-MRD2-V2-0
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Bestueckungsplan(Assembly) Top

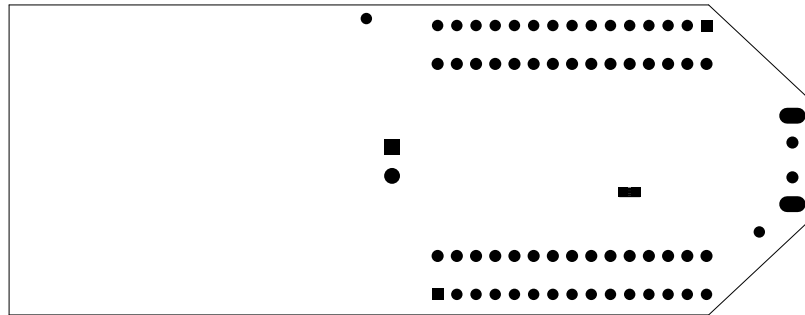
Bohrplan 106.02 x 41 x 1.6 mm FR4 35/35/35/35um



Soldermask Top

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Zolbetwerk Bottom

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