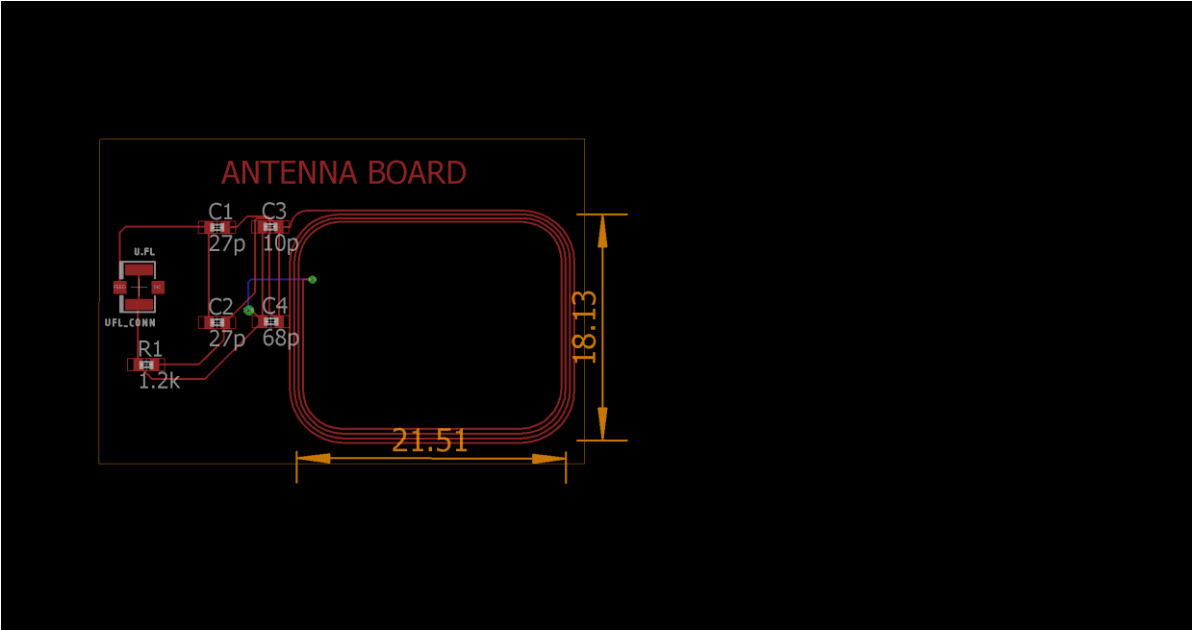


PCB LAYOUT



SCHEMATIC

