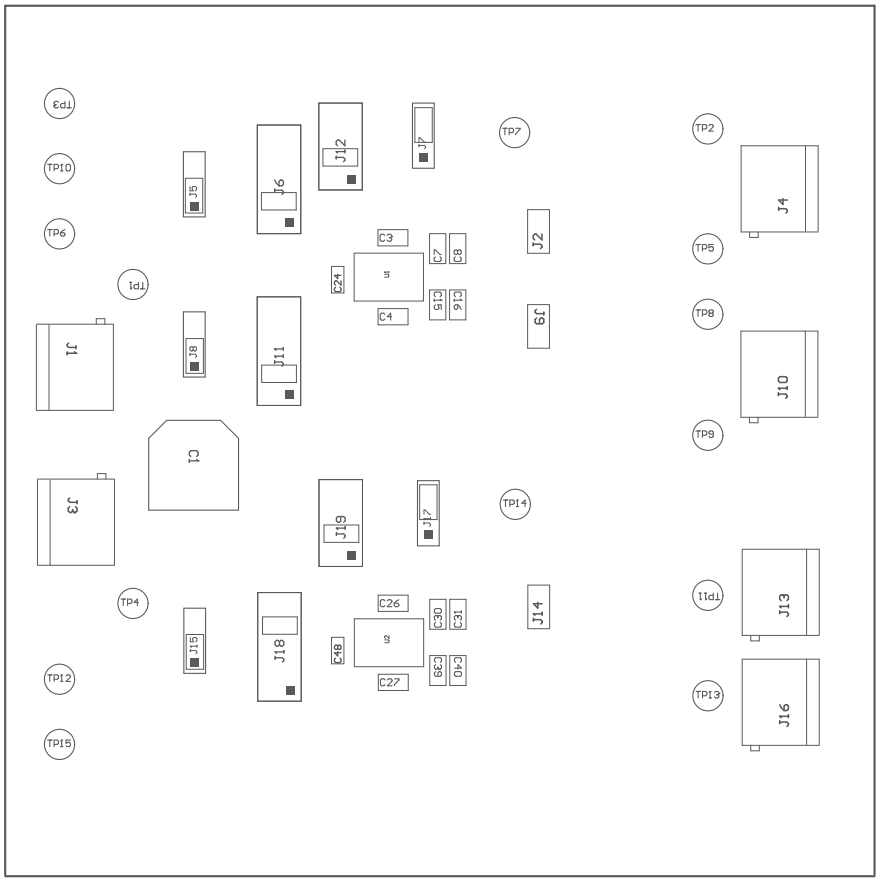
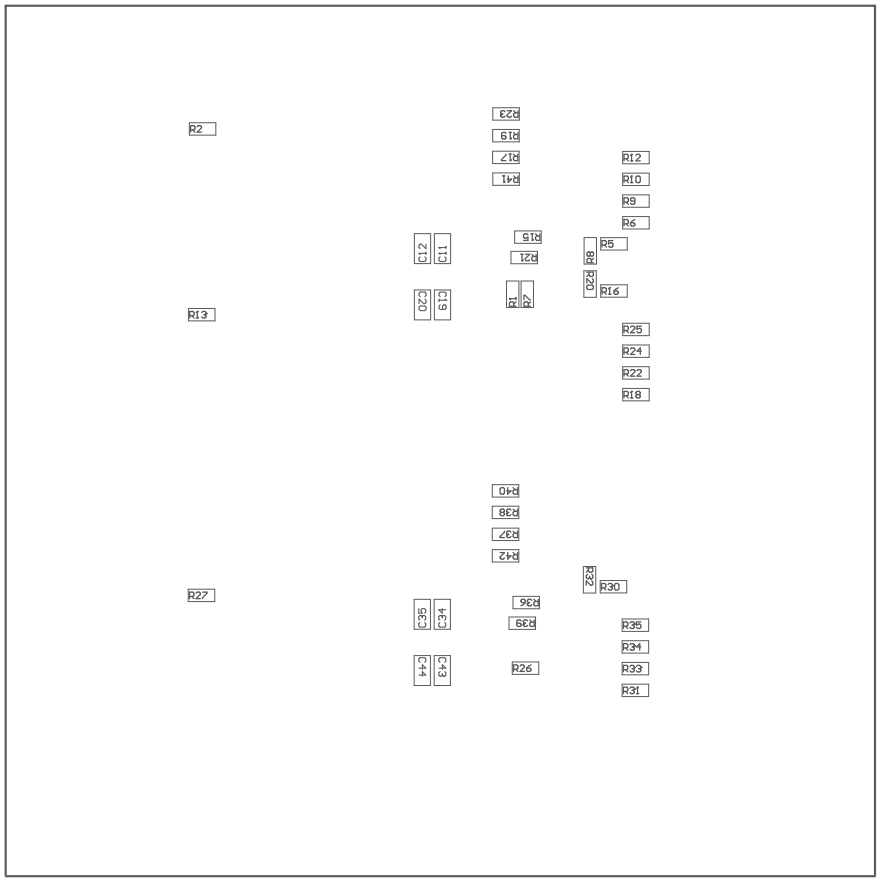
ZZ1 ■ Install label in silkscreened box after final wash. Text shall be 8 pt font. Text shall be per the Label Table in the PDF schematic.
ZZ2 ■ These assemblies are ESD sensitive, ESD precautions shall be observed.
ZZ3 ■ These assemblies must be clean and free from flux and all contaminants. Use of no clean flux is not acceptable.
ZZ4 ■ These assemblies must comply with workmanship standards IPC-A-610 Class 2, unless otherwise specified.
ZZ5 ■ Place SH-J5 on J5 pins 1 and 2
ZZ6 ■ Place SH-J6 on J6 pins 3 and 4
ZZ7 ■ Place SH-J7 on J7 pins 2 and 3
ZZ8 ■ Place SH-J8 on J8 pins 1 and 2
ZZ9 ■ Place SH-J11 on J11 pins 3 and 4
ZZ10 ■ Place SH-J12 on J12 pins 3 and 4
ZZ11 ■ Place SH-J15 on J15 pins 1 and 2
ZZ12 ■ Place SH-J17 on J17 pins 2 and 3
ZZ13 ■ Place SH-J18 on J18 pins 7 and 8
ZZ14 ■ Place SH-J19 on J19 pins 3 and 4



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001

PCB VIEWED FROM TOP SIDE	BOARD #: BSR105	REV: A	SVN REV: Not In VersionControl
	TID #: N/A		
PLOT NAME = Top Assembly Drawing	GENERATED : 8/17/2020 5:47:40 PM		TEXAS INSTRUMENTS



COMPONENTS MARKED 'DNP' SHOULD NOT BE POPULATED.
ASSEMBLY VARIANT: 001

PCB VIEWED FROM BOTTOM SIDE	BOARD #: BSR105	REV: A	SVN REV: Not In VersionControl
	TID #: N/A		
PLOT NAME = Bottom Assembly Drawing	GENERATED : 8/17/2020 5:47:41 PM		TEXAS INSTRUMENTS