Dear Neeraj,

Thank you very much for the file you provided last time, which helped me a lot. I still hope to get your guidance on the following questions.

I read the manual of the ADC12DJ5200RF and found the following description:



I'm not sure whether my understanding is correct. Does it mean that the sampling point can be adjusted by controlling the delay, and the delay accuracy can reach 19fs? I would like to know how to realize sampling point (phase) delay control by operating on the ADC GUI software? Besides, how long can the maximum delay reach?

In addition, the manual also mentions that the full-scale input voltage is 0.8V VPP (peak to peak), and the screenshot is as follows:



But I tested in the JMODE30 mode at a sampling rate of 10.4GS/s. When I input the 260MHz RF signal of about 1.31V VPP, the sampling result is close to the full range. Are there several different voltage range options? What are they respectively? How can I operate to select different ranges?

Thank you again for your hard work.

Best Wishes！