

DAC38RF83 Phase Noise Performance

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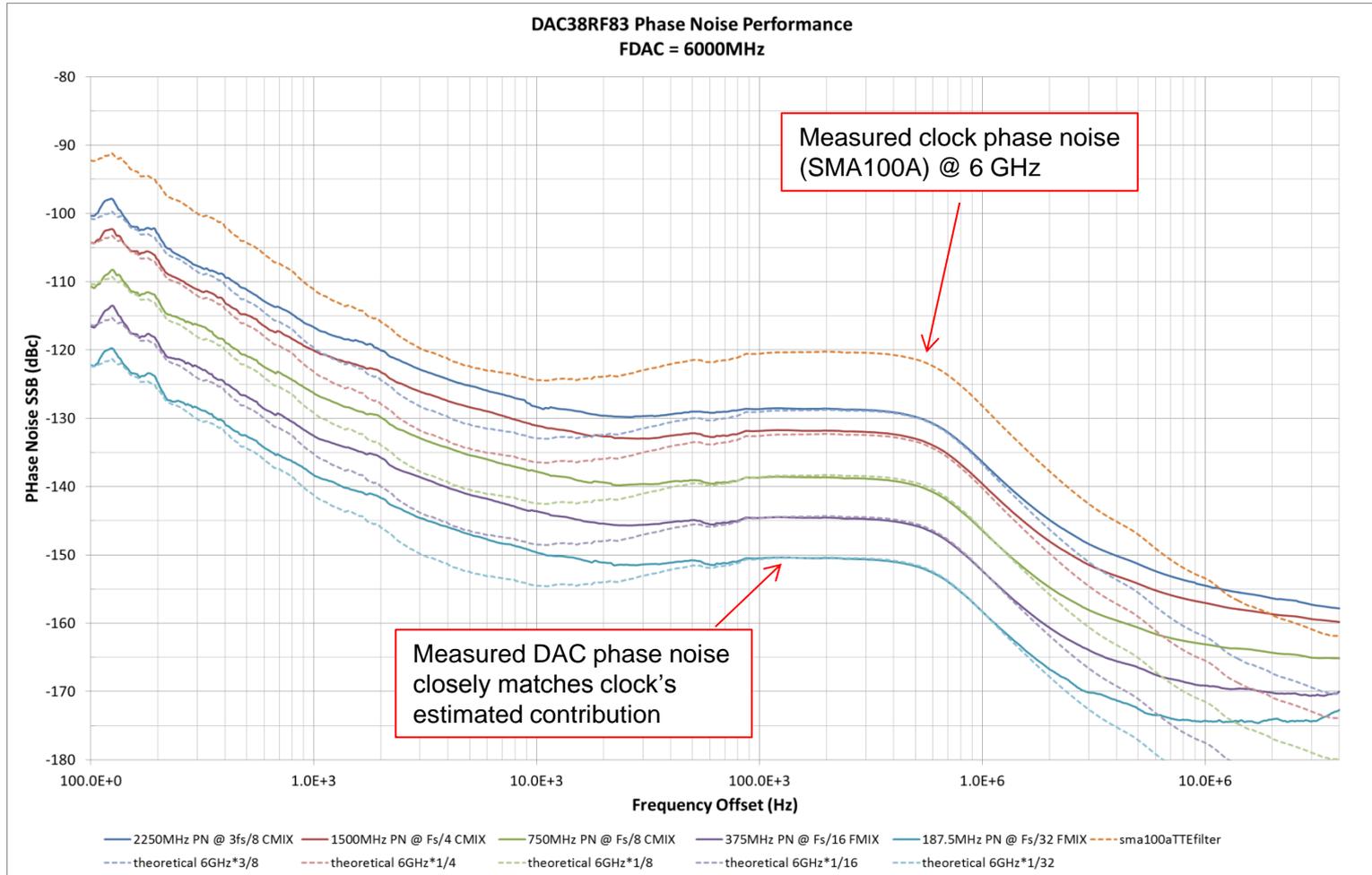
Kang Hsia

Test Setup

- **FDAC = 6000MHz**
- **CMIX and FMIX Enabled with Constant Full-Scale Input**
- **E5052A Phase Noise Analyzer with DAC38RF83 EVM Rev B**
- **R&S SMA100A As DAC Clock Source with TTE K5693-6G-1.5p-A 1.5% BW BPF (10dBm power after filter)**
- **CLKVDD provided at 1.05V with HP 6216A Analog Power Supply**

Measured DAC PN vs. Ideal DAC PN Estimate

CLK Source = SMA100A with TTE 6GHz BPF



Estimated DAC PN Performance (DACs Phase Noise Contribution)

