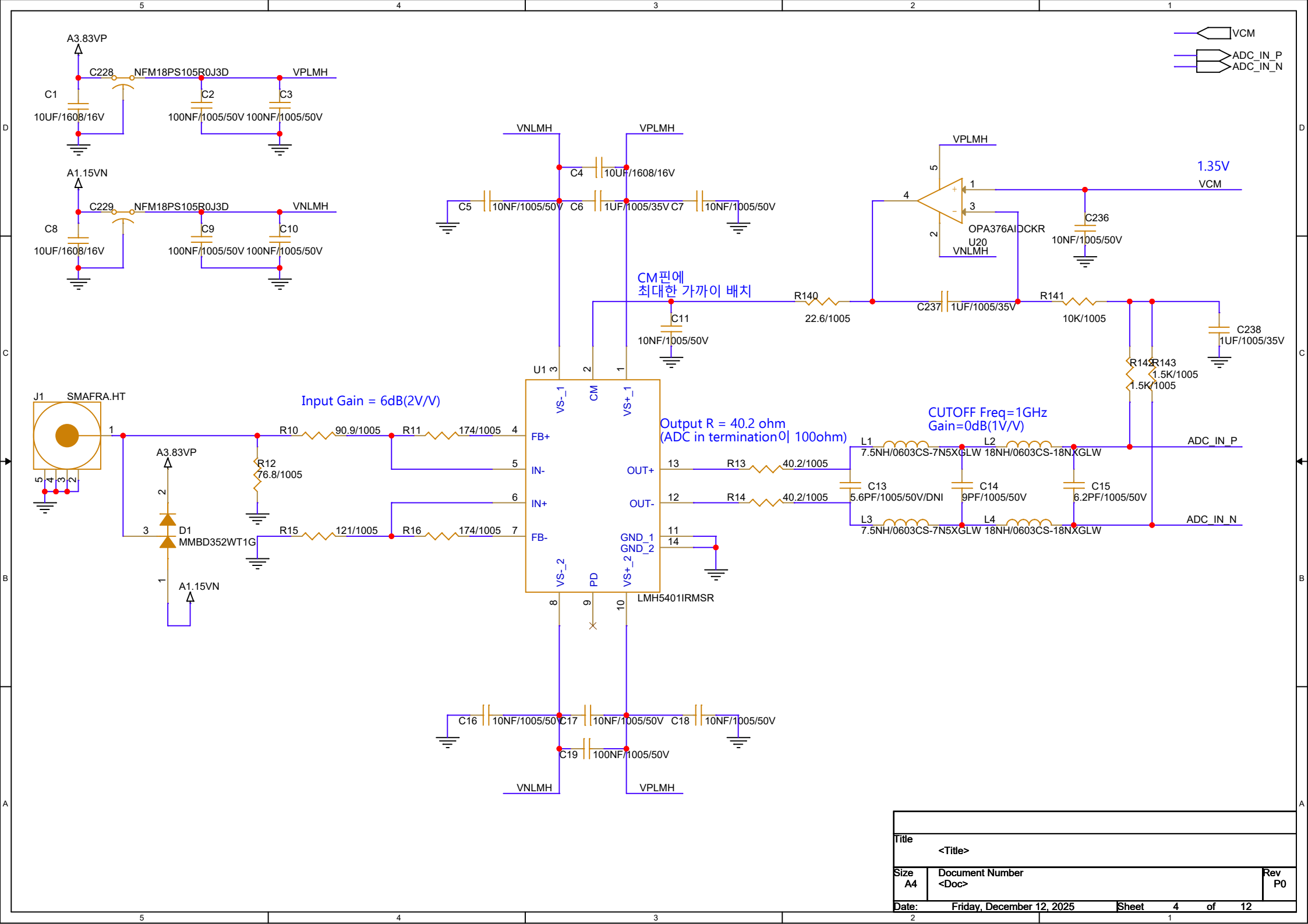


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Date:	Thursday, December 11, 2025	Sheet 3 of 12



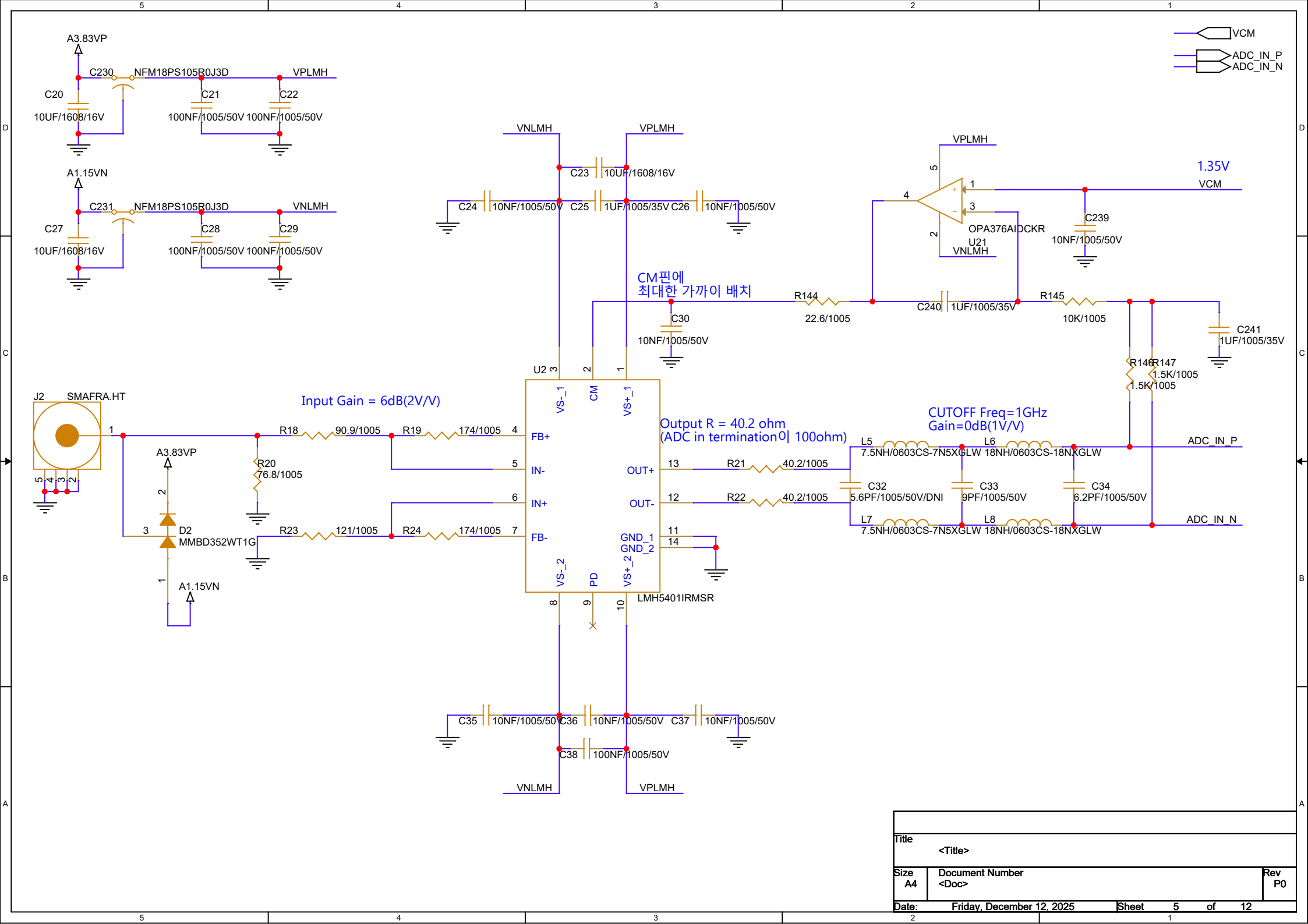
Input Gain = 6dB(2V/V)

Output R = 40.2 ohm
(ADC in termination) | 100ohm

CUTOFF Freq=1GHz
Gain=0dB(1V/V)

CM핀에
최대한 가까이 배치

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Date:	Friday, December 12, 2025	Sheet	4 of 12



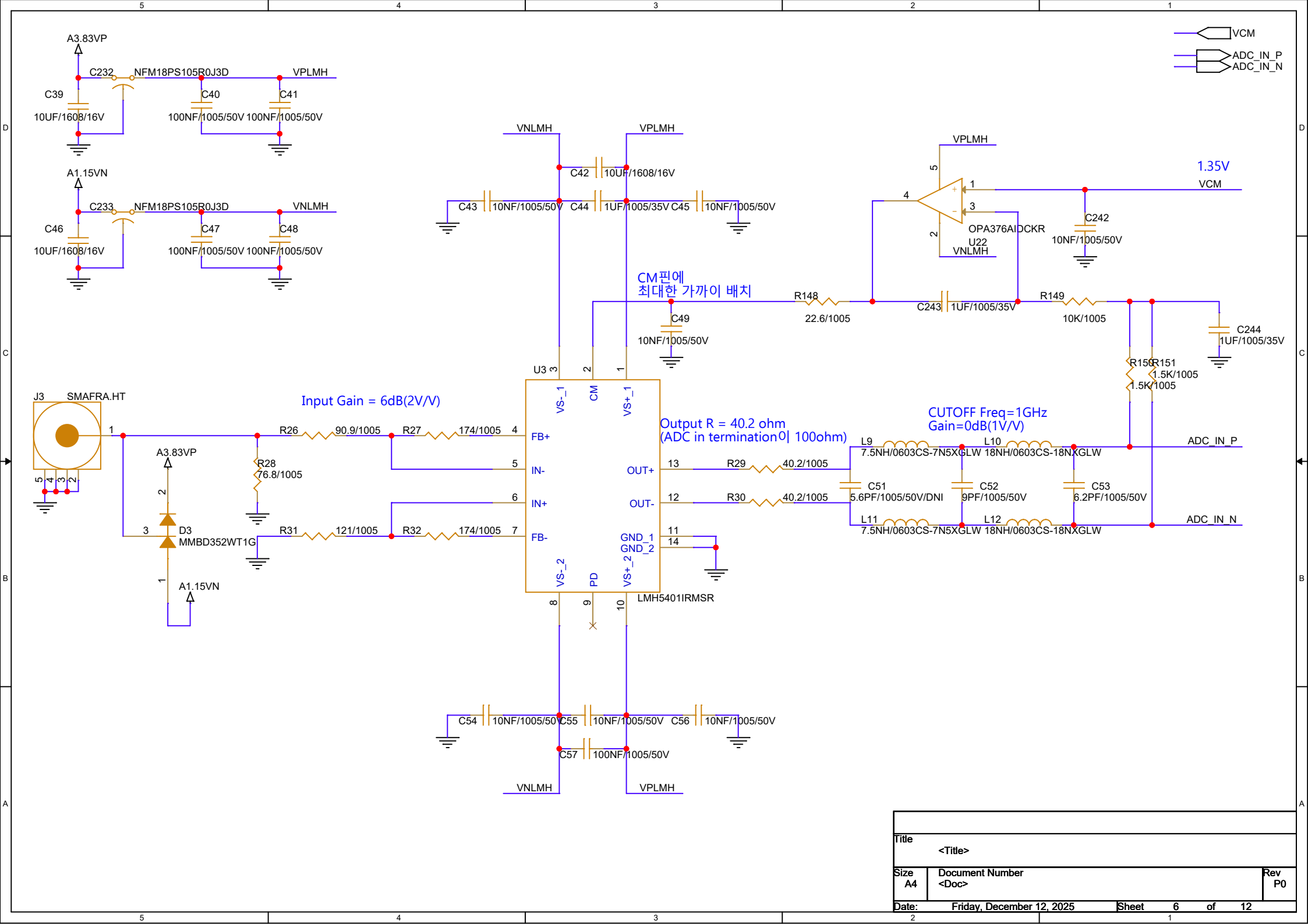
CM핀에
최대한 가까이 배치

Input Gain = 6dB(2V/V)

Output R = 40.2 ohm
(ADC in termination) 100ohm

CUTOFF Freq=1GHz
Gain=0dB(1V/V)

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A4	<Doc>	P0	
Date:	Friday, December 12, 2025	Sheet	5 of 12



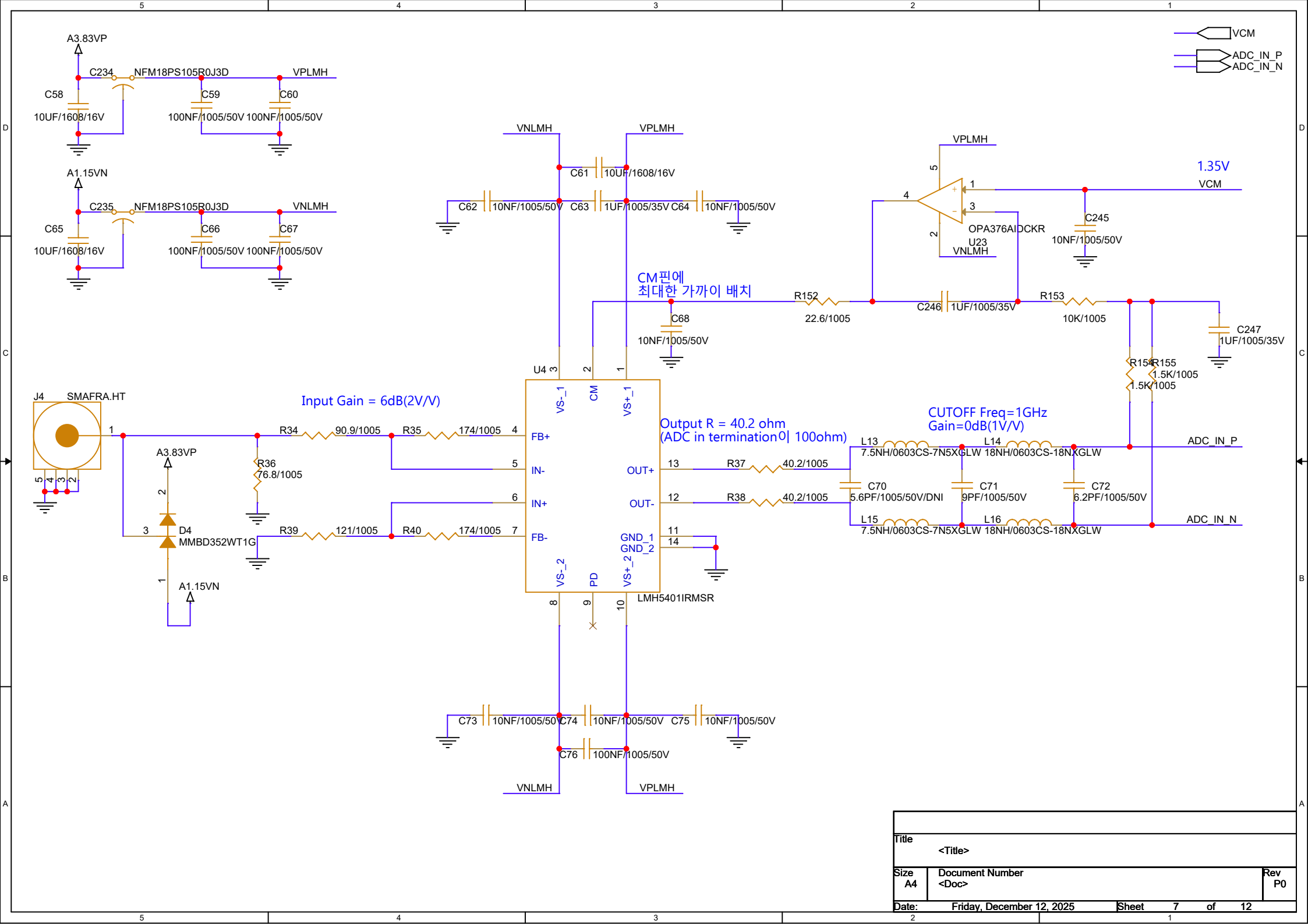
Input Gain = 6dB(2V/V)

Output R = 40.2 ohm
(ADC in termination 100ohm)

CUTOFF Freq=1GHz
Gain=0dB(1V/V)

CM핀에
최대한 가까이 배치

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Date:	Friday, December 12, 2025	Sheet	6 of 12



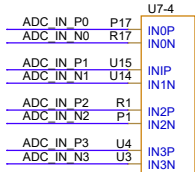
Input Gain = 6dB(2V/V)

Output R = 40.2 ohm
(ADC in termination @ 100ohm)

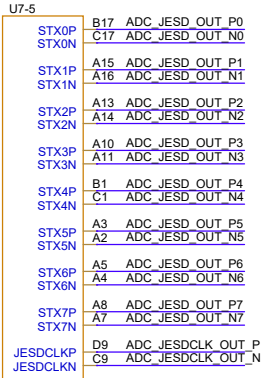
CUTOFF Freq=1GHz
Gain=0dB(1V/V)

CM핀에
최대한 가까이 배치

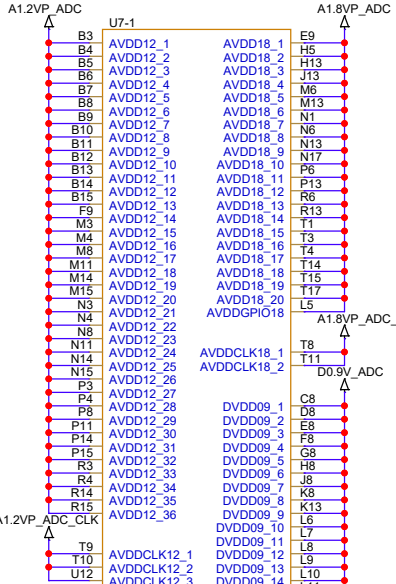
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Date:	Friday, December 12, 2025	Sheet	7 of 12
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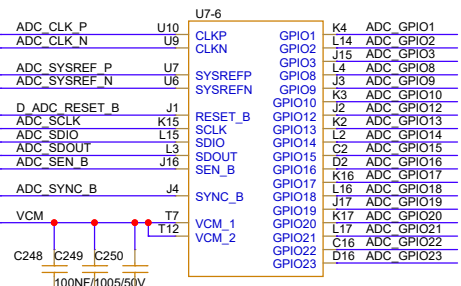
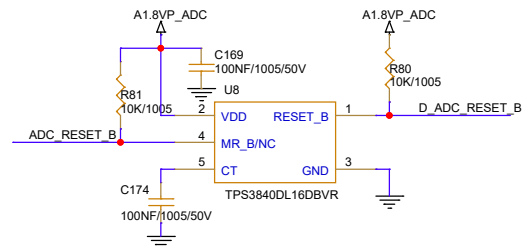
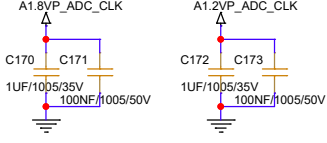
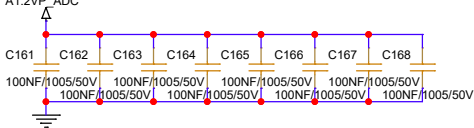
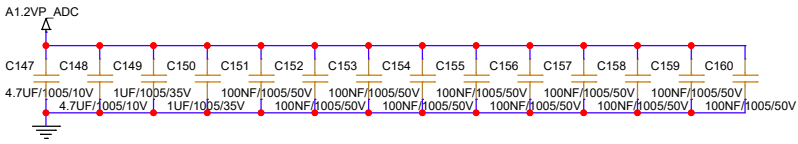
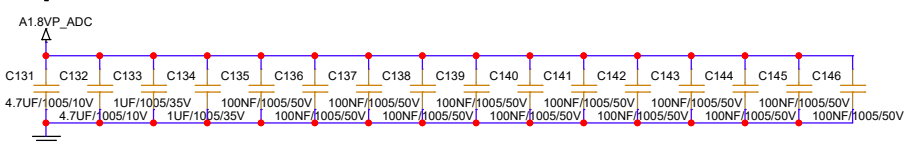
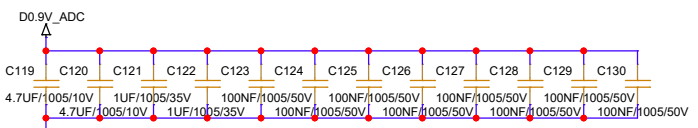
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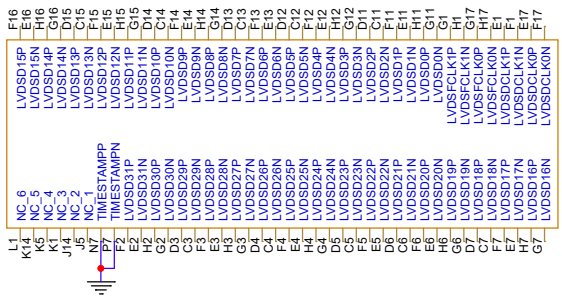
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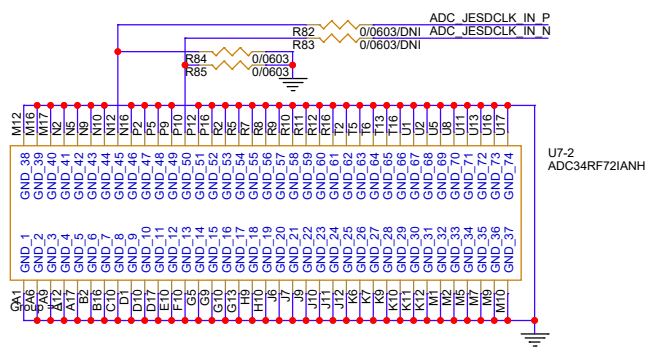
ADC34RF72IANH
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ADC34RF72IANH
Group = 1



ADC34RF72IANH
Group = 1



ADC34RF72IANH
Group = 1

