



Title		
Size	Number	Revision
A4		
Date:	11.17.2023	Sheet of
File:	G:\Master\..DAC.SchDoc	Drawn By:

- Internal pull-down (SCLK/SDIO/SDO/TXENABLE/SLEEP) and internal pull-up (SDENB/RESETB).  
 - SDIO Bi-directional in 3-pin mode and input only in 4-pin mode.

- Use (33/34) and (35/36) for 12-bit DAC3164.  
 - Use additionally (37/38) and (39/40) for 14-bit DAC3174.

- Gain setting option:  
 For full-scale output current (20mA), use 960Ohm resistor.

- Output level matching:  
 Common-mode DAC (according to Analog VDD 3.3V) and common-mode modulator (1.7V)

- Resetb: To reinitialize all registers after every power cycle (25ns active-low).  
 - Check "7.5.1 Power-Up Sequence" for programming.