

```
\\AFE77xx
\\START: Doing AFE Config
```

```
\\AFE77xx
\\Library Version: 2.19
```

```
\\ STEP: rstDevice/step0
```

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\\AFE77xx
\\EXTERNAL-ACTION: Toggle Hardware Reset
```

```
\\AFE77xx
\\START: Resetting the Device
```

```
//AFE77xx
SPIWrite 0000,30,0,7 //global_soft_reset=0x0; Address(0x0[7:7],)
SPIWrite 0000,b0,0,7 //global_soft_reset=0x1; Address(0x0[7:7],)
SPIWrite 0000,30,0,7 //global_soft_reset=0x0; Address(0x0[7:7],)
SPIWrite 0000,30,0,7 //global_4pin=0x1; Address(0x0[4:4],)
SPIWrite 0000,30,0,7 //global_ascend=0x1; Address(0x0[5:5],)
SPIReadCheck 0003,0,7,0a
```

```
\\Read chip_type=0xa; Address(0x3[7:0],)
```

```
SPIReadCheck 0004,0,7,77
SPIReadCheck 0005,0,7,00
```

```
\\Read chip_id=0x77; Address(0x4[7:0],0x5[7:0],)
```

```
SPIReadCheck 0006,0,7,11
```

```
\\Read chip_ver=0x11; Address(0x6[7:0],)
```

```
\\Read vendor_id=0x451; Address(0x7[7:0],0x8[7:0],)
```

```
\\END: Done resetting the Device
```

```
\\The list of parameters. In the arrays of 2 elements, each value corresponds to one 2T2R1F.
// jesdProtocol: 0; fbNco: [1843.2, 2139.84]; serdesManualCTLEEn: False;
usePllExternalLoClock: [False, False]; enableTxIqmcLolHostUpdateMode: False;
// txDsaCalibMode: 0; enableReliabilityDetector: True; bitFileType: 0; jesdRxLaneMux: [4, 2,
0, 1, 5, 3, 6, 7]; LMFShdTxFb: ['14810', '14810'];
// X: 61.44; fbDsaPerTx: [0, 0, 0, 0]; halfRateModeRx: 0; lowIfNcoTx: [0.0, 0.0];
serdesTxLanePolarity: [False, False, False, False, False, False, False, False];
// txIqmcExternalDelayValue: [0, 0, 0, 0]; txDsaGainStepDelay: 5; dedicatedLaneMode: [1, 1];
chipType: 16; txIqmcCalibMode: 0;
// chipVersion: 17; intPinsParams: [{'JESD': True, 'SPI': False, 'SRTXA': True, 'SRTXB': True,
'SRTXC': True, 'SRTXD': True, 'PLL4': True, 'PLL0': True, 'PLL1': True, 'PLL2': True, 'PLL3':
True}, {'JESD': False, 'SPI': True, 'SRTXA': False, 'SRTXB': False, 'SRTXC': False, 'SRTXD':
False, 'PLL4': False, 'PLL0': False, 'PLL1': False, 'PLL2': False, 'PLL3': False}]; LMFShdFb:
['12810', '12810']; enableTxIqmcLolPowerUpCorr: False; defaultRxDsa: [0, 0, 0, 0];
// enableRxIqmcLolPowerUpCorr: False; defaultTxDsa: [13, 13, 13, 13]; jesdLoopbackEn: 0;
ddcFactorRx: [48, 48]; syncLoopBack: True;
// jesdTxFBCDSyncMux: 2; enableTxJesdLinks: [1, 1]; pllMuxModes: 3; enableTxIqmcDelayChar:
False; halfRateModeTx: 0;
// lowIfNcoRx: [0.0, 0.0]; fbDsaPerTxEn: False; jesdRxRbd: [15, 15]; FRef: 491.52;
lowIfNcoFb: [0.0, 0.0];
// simulationMode: False; rxDsaPacketFilePath: ; setLoForCoherence: True; gpioMapping:
{'D14': 'u'tdd_2t_en_cd', 'D15': 'u'fb_nco_sw', 'D10': 'u'rx_ext_lnbypass', 'D11':
'u'rx_ext_lnbypass', 'E17': 'u'alarm_2', 'E16': 'u'global_pdn', 'U18': 'u'tx_fb_loop_1', 'T13':
'u'tdd_1f_en_ab', 'C13': 'u'tdd_2r_en_cd', 'T18': 'u'tx_fb_loop_2', 'U14': 'u'tdd_2t_en_ab', 'U17':
'u'tx_fb_loop_3', 'U16': 'u'spi_b1_clk', 'C17': 'u'alarm_1', 'A5': 'u'dac_sync_n_cd_0', 'V5':
```

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u'adc_sync_n_ab_0', 'C5': u'adc_sync_n_cd_0', 'V18': u'tx_fb_loop_0', 'V13': u'tdd_2r_en_ab',
'V10': u'rx_ext_lnbypass', 'V11': u'rx_ext_lnbypass', 'V16': u'intbipi_spib1_sdi', 'V14':
u'spib1_cs_n', 'V15': u'spib1_sdo', 'Y5': u'dac_sync_n_ab_0'}}; Fs: 2949.12;
// pllLo: [1843.2, 2949.12, 2139.84, 1747.44, 1950.0]; agcRegConfigParams:
[{'coarseIndexBits': 4, 'dgcMode': 3, 'windowLenSa': 128, 'thresholdSd': -10.0, 'stepSizeSa':
1, 'thresholdBa': -1.0, 'thresholdLowSd': -10, 'numHitsSa': 8, 'stepSizePa': 1, 'numHitsSd': 8,
'thresholdLowSa': -3.0, 'thresholdPd': -18.0, 'coarseStep': 3, 'enablePd': False, 'enablePa':
False, 'windowLenBa': 32, 'enableEl': False, 'stepSizePd': 1, 'windowLenBd': 32768,
'thresholdSa': -3.0, 'fdsaOffset': 0, 'regFreeze': 0, 'thresholdPa': -9.0, 'windowLenPd':
32768, 'floatingPointMode': 0, 'windowLenPa': 6, 'enableBd': False, 'dgcEnable': 1,
'stepSizeBa': 3, 'enableSd': True, 'enableBa': False, 'gainControl': 2, 'pinFreezeEn': 0,
'freeze': 0, 'floatingPointFormat': 0, 'numHitsBd': 8, 'enableIa': 1, 'stepSizeSd': 1,
'thresholdLowBd': -12.0, 'modePd': 0, 'thresholdLowBa': -1.0, 'gainMargin': 2, 'modePa': 0,
'enableSa': True, 'numHitsBa': 8, 'thresholdBd': -12.0, 'lnbypassGain': 16, 'windowLenSd':
131072, 'phmOvrEn': 0, 'stepSizeBd': 3}, {'coarseIndexBits': 4, 'dgcMode': 3, 'windowLenSa':
128, 'thresholdSd': -10.0, 'stepSizeSa': 1, 'thresholdBa': -1.0, 'thresholdLowSd': -10,
'numHitsSa': 8, 'stepSizePa': 1, 'numHitsSd': 8, 'thresholdLowSa': -3.0, 'thresholdPd': -18.0,
'coarseStep': 3, 'enablePd': False, 'enablePa': False, 'windowLenBa': 32, 'enableEl': False,
'stepSizePd': 1, 'windowLenBd': 32768, 'thresholdSa': -3.0, 'fdsaOffset': 0, 'regFreeze': 0,
'thresholdPa': -9.0, 'windowLenPd': 32768, 'floatingPointMode': 0, 'windowLenPa': 6,
'enableBd': False, 'dgcEnable': 1, 'stepSizeBa': 3, 'enableSd': True, 'enableBa': False,
'gainControl': 2, 'pinFreezeEn': 0, 'freeze': 0, 'floatingPointFormat': 0, 'numHitsBd': 8,
'enableIa': 1, 'stepSizeSd': 1, 'thresholdLowBd': -12.0, 'modePd': 0, 'thresholdLowBa': -1.0,
'gainMargin': 2, 'modePa': 0, 'enableSa': True, 'numHitsBa': 8, 'thresholdBd': -12.0,
'lnbypassGain': 16, 'windowLenSd': 131072, 'phmOvrEn': 0, 'stepSizeBd': 3},
{'coarseIndexBits': 4, 'dgcMode': 3, 'windowLenSa': 128, 'thresholdSd': -10.0, 'stepSizeSa': 1,
'thresholdBa': -1.0, 'thresholdLowSd': -10, 'numHitsSa': 8, 'stepSizePa': 1, 'numHitsSd': 8,
'thresholdLowSa': -3.0, 'thresholdPd': -18.0, 'coarseStep': 3, 'enablePd': False, 'enablePa':
False, 'windowLenBa': 32, 'enableEl': False, 'stepSizePd': 1, 'windowLenBd': 32768,
'thresholdSa': -3.0, 'fdsaOffset': 0, 'regFreeze': 0, 'thresholdPa': -9.0, 'windowLenPd':
32768, 'floatingPointMode': 0, 'windowLenPa': 6, 'enableBd': False, 'dgcEnable': 1,
'stepSizeBa': 3, 'enableSd': True, 'enableBa': False, 'gainControl': 2, 'pinFreezeEn': 0,
'freeze': 0, 'floatingPointFormat': 0, 'numHitsBd': 8, 'enableIa': 1, 'stepSizeSd': 1,
'thresholdLowBd': -12.0, 'modePd': 0, 'thresholdLowBa': -1.0, 'gainMargin': 2, 'modePa': 0,
'enableSa': True, 'numHitsBa': 8, 'thresholdBd': -12.0, 'lnbypassGain': 16, 'windowLenSd':
131072, 'phmOvrEn': 0, 'stepSizeBd': 3}, {'coarseIndexBits': 4, 'dgcMode': 3, 'windowLenSa':
128, 'thresholdSd': -10.0, 'stepSizeSa': 1, 'thresholdBa': -1.0, 'thresholdLowSd': -10,
'numHitsSa': 8, 'stepSizePa': 1, 'numHitsSd': 8, 'thresholdLowSa': -3.0, 'thresholdPd': -18.0,
'coarseStep': 3, 'enablePd': False, 'enablePa': False, 'windowLenBa': 32, 'enableEl': False,
'stepSizePd': 1, 'windowLenBd': 32768, 'thresholdSa': -3.0, 'fdsaOffset': 0, 'regFreeze': 0,
'thresholdPa': -9.0, 'windowLenPd': 32768, 'floatingPointMode': 0, 'windowLenPa': 6,
'enableBd': False, 'dgcEnable': 1, 'stepSizeBa': 3, 'enableSd': True, 'enableBa': False,
'gainControl': 2, 'pinFreezeEn': 0, 'freeze': 0, 'floatingPointFormat': 0, 'numHitsBd': 8,
'enableIa': 1, 'stepSizeSd': 1, 'thresholdLowBd': -12.0, 'modePd': 0, 'thresholdLowBa': -1.0,
'gainMargin': 2, 'modePa': 0, 'enableSa': True, 'numHitsBa': 8, 'thresholdBd': -12.0,
'lnbypassGain': 16, 'windowLenSd': 131072, 'phmOvrEn': 0, 'stepSizeBd': 3}}];
txDsaPacketFilePath: ; srConfigParams: [{'AttnStepSize': 38, 'alarmMask': 4032, 'enable':
True, 'mode': 0, 'threshold': 30, 'AmplUpdateCycles': 6, 'GainStepSize': 38}, {'AttnStepSize':
38, 'alarmMask': 4032, 'enable': True, 'mode': 0, 'threshold': 30, 'AmplUpdateCycles': 6,
'GainStepSize': 38}, {'AttnStepSize': 38, 'alarmMask': 4032, 'enable': True, 'mode': 0,
'threshold': 30, 'AmplUpdateCycles': 6, 'GainStepSize': 38}, {'AttnStepSize': 38, 'alarmMask':
4032, 'enable': True, 'mode': 0, 'threshold': 30, 'AmplUpdateCycles': 6, 'GainStepSize': 38}];
ducFactorTx: [24, 24];
// serdesFirmware: True; fbNcoBand1: [2139.84, 2139.84]; jesdABLvdsSync: 0;
serdesRxDsaFactoryCal: [False, False, False, False, False, False, False, False]; useMacros:
True;
// rxDsaCalibMode: 0; serdesManualCTLE: [6, 6, 6, 6, 6, 6, 6, 6]; sampDropByFb: [1, 1];
enableTxDsaFactoryCal: False; halfRateModeFb: 0;
// jesdScr: [False, False]; releaseTxMuxControlToPins: False; latteLibVersion: 2.19;
jesdTxFBABSyncMux: 2; jesdRxABSyncMux: 0;
// customerConfig: True; jesdTxRxABSyncMux: 0; serdesTxPostCursor: [0, 0, 0, 0, 0, 0, 0, 0];
enableRxFbJesdLinks: [1, 0]; chipId: 119;
// defaultFbDsa: [1, 1]; serdesTxMainCursor: [3, 0, 0, 0, 0, 0, 0, 3];
executeLinkUpSequenceSeparately: True; sampDropByRx: [1, 1]; enableRxDsaFactoryCal: False;
// ddcFactorFb: [24, 24]; jesdK: [32, 32]; enableTxIqmcLolTrackingCorr: True;
jesdRxCDSyncMux: 2; useSpiSysref: False;
// sysrefFreqDiv: 768; jesdTxRxCDSyncMux: 0; enableRxIqmcLolTrackingCorr: True;
txIqmcFullBandEstimation: False; enableFbCd: False;
// setTxLoFbNcoFreqForTxCalib: True; enableTxSigGen: False; mode2t2r: True; systemMode: [1,

```

```
1]; jesdTxLaneMux: [1, 3, 5, 4, 0, 2, 6, 7];
// txDsaUpdateMode: 0; LMFSHdRx: ['181610', '181610']; serdesTxPreCursor: [0, 0, 0, 0, 0, 0,
0, 0]; gpioConfigMode: 3; jesdCDLvdsSync: 0;
//
```

\\Doing LMK config

\\Doing LMK config

\\ STEP: wakeUp/step0

\\START: Waking up device

```
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1;(Meaning: ); Address(0x15[7:7],)
SPIWrite 0600,03,0,7 //misc_spi_global_pdn_ctrl=0x1; Address(0x600[0:0],)
SPIWrite 0600,01,0,7 //misc_spi_global_pdn_sig=0x0; Address(0x600[1:1],)
```

\\END: Done waking up device

\\START: Loading Efuse Chain

```
SPIWrite 07f0,00,0,7 //spi_ovr_sys_autoload_chain=0x0; Address(0x7f0[5:2],)
SPIWrite 0804,00,0,7 //spi_ovr_sys_autoload_chain=0x0; Address(0x804[5:2],)
SPIWrite 07f0,3c,0,7 //spi_ovr_sys_autoload_chain=0xf; Address(0x7f0[5:2],)
SPIWrite 0804,3c,0,7 //spi_ovr_sys_autoload_chain=0xf; Address(0x804[5:2],)
SPIWrite 07f0,3c,0,7 //spi_sys_rstn_ctrl=0x0; Address(0x7f0[1:0],)
SPIWrite 07f0,3d,0,7 //spi_sys_rstn_ctrl=0x1; Address(0x7f0[1:0],)
SPIWrite 07f0,3f,0,7 //spi_sys_rstn_ctrl=0x3; Address(0x7f0[1:0],)
SPIWrite 07f0,3c,0,7 //spi_sys_rstn_ctrl=0x0; Address(0x7f0[1:0],)
SPIWrite 0804,3c,0,7 //spi_sys_rstn_ctrl=0x0; Address(0x804[1:0],)
SPIWrite 0804,3d,0,7 //spi_sys_rstn_ctrl=0x1; Address(0x804[1:0],)
SPIWrite 0804,3f,0,7 //spi_sys_rstn_ctrl=0x3; Address(0x804[1:0],)
SPIWrite 0804,3c,0,7 //spi_sys_rstn_ctrl=0x0; Address(0x804[1:0],)
SPIWrite 07f0,00,0,7 //spi_ovr_sys_autoload_chain=0x0; Address(0x7f0[5:2],)
SPIWrite 0804,00,0,7 //spi_ovr_sys_autoload_chain=0x0; Address(0x804[5:2],)
```

WAIT 0.01

\\END: Done loading Efuse Chain

\\START: Checking Efuse Chain

```
\\Read obs_func_spi_chain_autoload_done=0xf; Address(0x138[3:0],)
```

```
\\Read obs_func_spi_chain_autoload_error=0x0; Address(0x138[7:4],)
```

\\END: Done checking Efuse Chain

\\START: Enabling/Disabling Efuse Clock

```
SPIWrite 0135,01,0,7 //spi_ovr_sys_clk_gate=0x1; Address(0x134[8:8],)
SPIWrite 0131,01,0,7 //spi_ovr_sys_clk_gate=0x1; Address(0x130[8:8],)
```

\\END: Done enabling/Disabling Efuse Clock

```
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0;(Meaning: ); Address(0x15[7:7],)
\\ STEP: rstMCU/step0
```

\\START: Resetting Top MC

```
SPIWrite 0013,10,0,7    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0140,01,0,7
```

```
\\END: Done resetting Top MC
```

```
\\START: Resetting Top MC
```

```
SPIWrite 0140,00,0,7
```

```
\\END: Done resetting Top MC
```

```
WAIT 0.1
```

```
SPIWrite 0013,00,0,7    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
\\ STEP: rstMCU/step1
SPIWrite 0012,10,4,4    //PAGE: sys_calib_macro_memory=0x1;(Meaning: );    Address(0x12[4:4],)
SPIWrite 0040,00,0,7
SPIWrite 0020,00,0,7
SPIWrite 0021,00,0,7
SPIWrite 0022,00,0,7
SPIWrite 0023,00,0,7
SPIWrite 0024,75,0,7
SPIWrite 0025,8a,0,7
SPIWrite 0026,02,0,7
SPIWrite 0027,00,0,7
SPIWrite 0028,b5,0,7
SPIWrite 0029,a4,0,7
SPIWrite 002a,6d,0,7
SPIWrite 002b,ea,0,7
SPIWrite 002c,7d,0,7
SPIWrite 002d,19,0,7
SPIWrite 002e,ce,0,7
SPIWrite 002f,ca,0,7
SPIWrite 0030,10,0,7
SPIWrite 0031,00,0,7
SPIWrite 0032,99,0,7
SPIWrite 0033,10,0,7
SPIWrite 0034,0a,0,7
SPIWrite 0035,01,0,7
SPIWrite 0036,12,0,7
SPIWrite 0037,00,0,7
SPIWrite 0038,11,0,7
SPIWrite 0039,00,0,7
SPIWrite 003a,9e,0,7
SPIWrite 003b,11,0,7
SPIWrite 003c,05,0,7
SPIWrite 003d,06,0,7
SPIWrite 003e,12,0,7
SPIWrite 003f,00,0,7
SPIWrite 0040,00,0,7
SPIWrite 0041,00,0,7
SPIWrite 0042,00,0,7
SPIWrite 0043,05,0,7
SPIWrite 0044,09,0,7
SPIWrite 0045,08,0,7
SPIWrite 0046,17,0,7
SPIWrite 0047,00,0,7
SPIWrite 0048,00,0,7
SPIWrite 0049,00,0,7
SPIWrite 004a,00,0,7
SPIWrite 004b,00,0,7
SPIWrite 004c,00,0,7
SPIWrite 004d,00,0,7
SPIWrite 004e,00,0,7
SPIWrite 004f,00,0,7
SPIWrite 0050,ad,0,7
SPIWrite 0051,f1,0,7
```

SPIWrite 0052,08,0,7
SPIWrite 0053,0d,0,7
SPIWrite 0054,00,0,7
SPIWrite 0055,21,0,7
SPIWrite 0056,7f,0,7
SPIWrite 0057,4a,0,7
SPIWrite 0058,00,0,7
SPIWrite 0059,91,0,7
SPIWrite 005a,50,0,7
SPIWrite 005b,7d,0,7
SPIWrite 005c,00,0,7
SPIWrite 005d,90,0,7
SPIWrite 005e,00,0,7
SPIWrite 005f,98,0,7
SPIWrite 0060,c0,0,7
SPIWrite 0061,b1,0,7
SPIWrite 0062,10,0,7
SPIWrite 0063,46,0,7
SPIWrite 0064,81,0,7
SPIWrite 0065,75,0,7
SPIWrite 0066,00,0,7
SPIWrite 0067,bf,0,7
SPIWrite 0068,0a,0,7
SPIWrite 0069,46,0,7
SPIWrite 006a,01,0,7
SPIWrite 006b,92,0,7
SPIWrite 006c,82,0,7
SPIWrite 006d,7d,0,7
SPIWrite 006e,01,0,7
SPIWrite 006f,92,0,7
SPIWrite 0070,01,0,7
SPIWrite 0071,9a,0,7
SPIWrite 0072,01,0,7
SPIWrite 0073,2a,0,7
SPIWrite 0074,0d,0,7
SPIWrite 0075,d0,0,7
SPIWrite 0076,01,0,7
SPIWrite 0077,23,0,7
SPIWrite 0078,20,0,7
SPIWrite 0079,22,0,7
SPIWrite 007a,c0,0,7
SPIWrite 007b,f2,0,7
SPIWrite 007c,00,0,7
SPIWrite 007d,02,0,7
SPIWrite 007e,52,0,7
SPIWrite 007f,1e,0,7
SPIWrite 0080,fd,0,7
SPIWrite 0081,d1,0,7
SPIWrite 0082,00,0,7
SPIWrite 0083,bf,0,7
SPIWrite 0084,00,0,7
SPIWrite 0085,bf,0,7
SPIWrite 0086,c3,0,7
SPIWrite 0087,75,0,7
SPIWrite 0088,82,0,7
SPIWrite 0089,7d,0,7
SPIWrite 008a,01,0,7
SPIWrite 008b,92,0,7
SPIWrite 008c,01,0,7
SPIWrite 008d,9a,0,7
SPIWrite 008e,01,0,7
SPIWrite 008f,2a,0,7
SPIWrite 0090,f2,0,7
SPIWrite 0091,d1,0,7
SPIWrite 0092,c1,0,7
SPIWrite 0093,75,0,7
SPIWrite 0094,02,0,7
SPIWrite 0095,b0,0,7
SPIWrite 0096,70,0,7

SPIWrite 0097,47,0,7
SPIWrite 0098,2d,0,7
SPIWrite 0099,e9,0,7
SPIWrite 009a,f0,0,7
SPIWrite 009b,47,0,7
SPIWrite 009c,6d,0,7
SPIWrite 009d,4c,0,7
SPIWrite 009e,21,0,7
SPIWrite 009f,79,0,7
SPIWrite 00a0,60,0,7
SPIWrite 00a1,79,0,7
SPIWrite 00a2,08,0,7
SPIWrite 00a3,43,0,7
SPIWrite 00a4,65,0,7
SPIWrite 00a5,d0,0,7
SPIWrite 00a6,dc,0,7
SPIWrite 00a7,48,0,7
SPIWrite 00a8,df,0,7
SPIWrite 00a9,f8,0,7
SPIWrite 00aa,68,0,7
SPIWrite 00ab,a3,0,7
SPIWrite 00ac,22,0,7
SPIWrite 00ad,88,0,7
SPIWrite 00ae,00,0,7
SPIWrite 00af,88,0,7
SPIWrite 00b0,da,0,7
SPIWrite 00b1,f8,0,7
SPIWrite 00b2,08,0,7
SPIWrite 00b3,14,0,7
SPIWrite 00b4,50,0,7
SPIWrite 00b5,43,0,7
SPIWrite 00b6,c0,0,7
SPIWrite 00b7,10,0,7
SPIWrite 00b8,88,0,7
SPIWrite 00b9,47,0,7
SPIWrite 00ba,20,0,7
SPIWrite 00bb,79,0,7
SPIWrite 00bc,58,0,7
SPIWrite 00bd,b3,0,7
SPIWrite 00be,4f,0,7
SPIWrite 00bf,f0,0,7
SPIWrite 00c0,78,0,7
SPIWrite 00c1,47,0,7
SPIWrite 00c2,01,0,7
SPIWrite 00c3,20,0,7
SPIWrite 00c4,4f,0,7
SPIWrite 00c5,f0,0,7
SPIWrite 00c6,2f,0,7
SPIWrite 00c7,46,0,7
SPIWrite 00c8,97,0,7
SPIWrite 00c9,f8,0,7
SPIWrite 00ca,39,0,7
SPIWrite 00cb,91,0,7
SPIWrite 00cc,87,0,7
SPIWrite 00cd,f8,0,7
SPIWrite 00ce,39,0,7
SPIWrite 00cf,01,0,7
SPIWrite 00d0,d6,0,7
SPIWrite 00d1,f8,0,7
SPIWrite 00d2,00,0,7
SPIWrite 00d3,81,0,7
SPIWrite 00d4,d6,0,7
SPIWrite 00d5,f8,0,7
SPIWrite 00d6,00,0,7
SPIWrite 00d7,11,0,7
SPIWrite 00d8,41,0,7
SPIWrite 00d9,f0,0,7
SPIWrite 00da,80,0,7
SPIWrite 00db,71,0,7

SPIWrite 00dc,c6,0,7
SPIWrite 00dd,f8,0,7
SPIWrite 00de,00,0,7
SPIWrite 00df,11,0,7
SPIWrite 00e0,00,0,7
SPIWrite 00e1,25,0,7
SPIWrite 00e2,86,0,7
SPIWrite 00e3,f8,0,7
SPIWrite 00e4,02,0,7
SPIWrite 00e5,51,0,7
SPIWrite 00e6,e3,0,7
SPIWrite 00e7,7a,0,7
SPIWrite 00e8,86,0,7
SPIWrite 00e9,f8,0,7
SPIWrite 00ea,01,0,7
SPIWrite 00eb,31,0,7
SPIWrite 00ec,a2,0,7
SPIWrite 00ed,7a,0,7
SPIWrite 00ee,86,0,7
SPIWrite 00ef,f8,0,7
SPIWrite 00f0,00,0,7
SPIWrite 00f1,21,0,7
SPIWrite 00f2,86,0,7
SPIWrite 00f3,f8,0,7
SPIWrite 00f4,02,0,7
SPIWrite 00f5,01,0,7
SPIWrite 00f6,da,0,7
SPIWrite 00f7,f8,0,7
SPIWrite 00f8,08,0,7
SPIWrite 00f9,14,0,7
SPIWrite 00fa,88,0,7
SPIWrite 00fb,47,0,7
SPIWrite 00fc,86,0,7
SPIWrite 00fd,f8,0,7
SPIWrite 00fe,02,0,7
SPIWrite 00ff,51,0,7
SPIWrite 0100,c8,0,7
SPIWrite 0101,f3,0,7
SPIWrite 0102,00,0,7
SPIWrite 0103,61,0,7
SPIWrite 0104,87,0,7
SPIWrite 0105,f8,0,7
SPIWrite 0106,39,0,7
SPIWrite 0107,91,0,7
SPIWrite 0108,d6,0,7
SPIWrite 0109,f8,0,7
SPIWrite 010a,00,0,7
SPIWrite 010b,01,0,7
SPIWrite 010c,25,0,7
SPIWrite 010d,71,0,7
SPIWrite 010e,61,0,7
SPIWrite 010f,f3,0,7
SPIWrite 0110,18,0,7
SPIWrite 0111,60,0,7
SPIWrite 0112,c6,0,7
SPIWrite 0113,f8,0,7
SPIWrite 0114,00,0,7
SPIWrite 0115,01,0,7
SPIWrite 0116,60,0,7
SPIWrite 0117,79,0,7
SPIWrite 0118,58,0,7
SPIWrite 0119,b3,0,7
SPIWrite 011a,4f,0,7
SPIWrite 011b,f0,0,7
SPIWrite 011c,78,0,7
SPIWrite 011d,47,0,7
SPIWrite 011e,01,0,7
SPIWrite 011f,20,0,7
SPIWrite 0120,4f,0,7

SPIWrite 0121,f0,0,7
SPIWrite 0122,2f,0,7
SPIWrite 0123,46,0,7
SPIWrite 0124,97,0,7
SPIWrite 0125,f8,0,7
SPIWrite 0126,49,0,7
SPIWrite 0127,91,0,7
SPIWrite 0128,87,0,7
SPIWrite 0129,f8,0,7
SPIWrite 012a,49,0,7
SPIWrite 012b,01,0,7
SPIWrite 012c,d6,0,7
SPIWrite 012d,f8,0,7
SPIWrite 012e,20,0,7
SPIWrite 012f,81,0,7
SPIWrite 0130,d6,0,7
SPIWrite 0131,f8,0,7
SPIWrite 0132,20,0,7
SPIWrite 0133,11,0,7
SPIWrite 0134,41,0,7
SPIWrite 0135,f0,0,7
SPIWrite 0136,80,0,7
SPIWrite 0137,71,0,7
SPIWrite 0138,c6,0,7
SPIWrite 0139,f8,0,7
SPIWrite 013a,20,0,7
SPIWrite 013b,11,0,7
SPIWrite 013c,00,0,7
SPIWrite 013d,25,0,7
SPIWrite 013e,86,0,7
SPIWrite 013f,f8,0,7
SPIWrite 0140,22,0,7
SPIWrite 0141,51,0,7
SPIWrite 0142,63,0,7
SPIWrite 0143,7b,0,7
SPIWrite 0144,86,0,7
SPIWrite 0145,f8,0,7
SPIWrite 0146,21,0,7
SPIWrite 0147,31,0,7
SPIWrite 0148,22,0,7
SPIWrite 0149,7b,0,7
SPIWrite 014a,86,0,7
SPIWrite 014b,f8,0,7
SPIWrite 014c,20,0,7
SPIWrite 014d,21,0,7
SPIWrite 014e,86,0,7
SPIWrite 014f,f8,0,7
SPIWrite 0150,22,0,7
SPIWrite 0151,01,0,7
SPIWrite 0152,da,0,7
SPIWrite 0153,f8,0,7
SPIWrite 0154,08,0,7
SPIWrite 0155,14,0,7
SPIWrite 0156,65,0,7
SPIWrite 0157,71,0,7
SPIWrite 0158,88,0,7
SPIWrite 0159,47,0,7
SPIWrite 015a,86,0,7
SPIWrite 015b,f8,0,7
SPIWrite 015c,22,0,7
SPIWrite 015d,51,0,7
SPIWrite 015e,87,0,7
SPIWrite 015f,f8,0,7
SPIWrite 0160,49,0,7
SPIWrite 0161,91,0,7
SPIWrite 0162,d6,0,7
SPIWrite 0163,f8,0,7
SPIWrite 0164,20,0,7
SPIWrite 0165,01,0,7

SPIWrite 0166,c8,0,7
SPIWrite 0167,f3,0,7
SPIWrite 0168,00,0,7
SPIWrite 0169,61,0,7
SPIWrite 016a,61,0,7
SPIWrite 016b,f3,0,7
SPIWrite 016c,18,0,7
SPIWrite 016d,60,0,7
SPIWrite 016e,c6,0,7
SPIWrite 016f,f8,0,7
SPIWrite 0170,20,0,7
SPIWrite 0171,01,0,7
SPIWrite 0172,bd,0,7
SPIWrite 0173,e8,0,7
SPIWrite 0174,f0,0,7
SPIWrite 0175,87,0,7
SPIWrite 0176,2d,0,7
SPIWrite 0177,e9,0,7
SPIWrite 0178,f0,0,7
SPIWrite 0179,47,0,7
SPIWrite 017a,36,0,7
SPIWrite 017b,4c,0,7
SPIWrite 017c,a1,0,7
SPIWrite 017d,78,0,7
SPIWrite 017e,e0,0,7
SPIWrite 017f,78,0,7
SPIWrite 0180,08,0,7
SPIWrite 0181,43,0,7
SPIWrite 0182,65,0,7
SPIWrite 0183,d0,0,7
SPIWrite 0184,a4,0,7
SPIWrite 0185,48,0,7
SPIWrite 0186,df,0,7
SPIWrite 0187,f8,0,7
SPIWrite 0188,8c,0,7
SPIWrite 0189,a2,0,7
SPIWrite 018a,22,0,7
SPIWrite 018b,88,0,7
SPIWrite 018c,00,0,7
SPIWrite 018d,88,0,7
SPIWrite 018e,da,0,7
SPIWrite 018f,f8,0,7
SPIWrite 0190,08,0,7
SPIWrite 0191,14,0,7
SPIWrite 0192,50,0,7
SPIWrite 0193,43,0,7
SPIWrite 0194,c0,0,7
SPIWrite 0195,10,0,7
SPIWrite 0196,88,0,7
SPIWrite 0197,47,0,7
SPIWrite 0198,a0,0,7
SPIWrite 0199,78,0,7
SPIWrite 019a,58,0,7
SPIWrite 019b,b3,0,7
SPIWrite 019c,4f,0,7
SPIWrite 019d,f0,0,7
SPIWrite 019e,f0,0,7
SPIWrite 019f,47,0,7
SPIWrite 01a0,01,0,7
SPIWrite 01a1,20,0,7
SPIWrite 01a2,4f,0,7
SPIWrite 01a3,f0,0,7
SPIWrite 01a4,2e,0,7
SPIWrite 01a5,46,0,7
SPIWrite 01a6,97,0,7
SPIWrite 01a7,f8,0,7
SPIWrite 01a8,39,0,7
SPIWrite 01a9,91,0,7
SPIWrite 01aa,87,0,7

SPIWrite 01ab,f8,0,7
SPIWrite 01ac,39,0,7
SPIWrite 01ad,01,0,7
SPIWrite 01ae,d6,0,7
SPIWrite 01af,f8,0,7
SPIWrite 01b0,00,0,7
SPIWrite 01b1,81,0,7
SPIWrite 01b2,d6,0,7
SPIWrite 01b3,f8,0,7
SPIWrite 01b4,00,0,7
SPIWrite 01b5,11,0,7
SPIWrite 01b6,41,0,7
SPIWrite 01b7,f0,0,7
SPIWrite 01b8,80,0,7
SPIWrite 01b9,71,0,7
SPIWrite 01ba,c6,0,7
SPIWrite 01bb,f8,0,7
SPIWrite 01bc,00,0,7
SPIWrite 01bd,11,0,7
SPIWrite 01be,00,0,7
SPIWrite 01bf,25,0,7
SPIWrite 01c0,86,0,7
SPIWrite 01c1,f8,0,7
SPIWrite 01c2,02,0,7
SPIWrite 01c3,51,0,7
SPIWrite 01c4,e3,0,7
SPIWrite 01c5,79,0,7
SPIWrite 01c6,86,0,7
SPIWrite 01c7,f8,0,7
SPIWrite 01c8,01,0,7
SPIWrite 01c9,31,0,7
SPIWrite 01ca,a2,0,7
SPIWrite 01cb,79,0,7
SPIWrite 01cc,86,0,7
SPIWrite 01cd,f8,0,7
SPIWrite 01ce,00,0,7
SPIWrite 01cf,21,0,7
SPIWrite 01d0,86,0,7
SPIWrite 01d1,f8,0,7
SPIWrite 01d2,02,0,7
SPIWrite 01d3,01,0,7
SPIWrite 01d4,da,0,7
SPIWrite 01d5,f8,0,7
SPIWrite 01d6,08,0,7
SPIWrite 01d7,14,0,7
SPIWrite 01d8,88,0,7
SPIWrite 01d9,47,0,7
SPIWrite 01da,86,0,7
SPIWrite 01db,f8,0,7
SPIWrite 01dc,02,0,7
SPIWrite 01dd,51,0,7
SPIWrite 01de,c8,0,7
SPIWrite 01df,f3,0,7
SPIWrite 01e0,00,0,7
SPIWrite 01e1,61,0,7
SPIWrite 01e2,87,0,7
SPIWrite 01e3,f8,0,7
SPIWrite 01e4,39,0,7
SPIWrite 01e5,91,0,7
SPIWrite 01e6,d6,0,7
SPIWrite 01e7,f8,0,7
SPIWrite 01e8,00,0,7
SPIWrite 01e9,01,0,7
SPIWrite 01ea,a5,0,7
SPIWrite 01eb,70,0,7
SPIWrite 01ec,61,0,7
SPIWrite 01ed,f3,0,7
SPIWrite 01ee,18,0,7
SPIWrite 01ef,60,0,7

SPIWrite 01f0,c6,0,7
SPIWrite 01f1,f8,0,7
SPIWrite 01f2,00,0,7
SPIWrite 01f3,01,0,7
SPIWrite 01f4,e0,0,7
SPIWrite 01f5,78,0,7
SPIWrite 01f6,58,0,7
SPIWrite 01f7,b3,0,7
SPIWrite 01f8,4f,0,7
SPIWrite 01f9,f0,0,7
SPIWrite 01fa,f0,0,7
SPIWrite 01fb,47,0,7
SPIWrite 01fc,01,0,7
SPIWrite 01fd,20,0,7
SPIWrite 01fe,4f,0,7
SPIWrite 01ff,f0,0,7
SPIWrite 0200,2e,0,7
SPIWrite 0201,46,0,7
SPIWrite 0202,97,0,7
SPIWrite 0203,f8,0,7
SPIWrite 0204,49,0,7
SPIWrite 0205,91,0,7
SPIWrite 0206,87,0,7
SPIWrite 0207,f8,0,7
SPIWrite 0208,49,0,7
SPIWrite 0209,01,0,7
SPIWrite 020a,d6,0,7
SPIWrite 020b,f8,0,7
SPIWrite 020c,20,0,7
SPIWrite 020d,81,0,7
SPIWrite 020e,d6,0,7
SPIWrite 020f,f8,0,7
SPIWrite 0210,20,0,7
SPIWrite 0211,11,0,7
SPIWrite 0212,41,0,7
SPIWrite 0213,f0,0,7
SPIWrite 0214,80,0,7
SPIWrite 0215,71,0,7
SPIWrite 0216,c6,0,7
SPIWrite 0217,f8,0,7
SPIWrite 0218,20,0,7
SPIWrite 0219,11,0,7
SPIWrite 021a,00,0,7
SPIWrite 021b,25,0,7
SPIWrite 021c,86,0,7
SPIWrite 021d,f8,0,7
SPIWrite 021e,22,0,7
SPIWrite 021f,51,0,7
SPIWrite 0220,63,0,7
SPIWrite 0221,7a,0,7
SPIWrite 0222,86,0,7
SPIWrite 0223,f8,0,7
SPIWrite 0224,21,0,7
SPIWrite 0225,31,0,7
SPIWrite 0226,22,0,7
SPIWrite 0227,7a,0,7
SPIWrite 0228,86,0,7
SPIWrite 0229,f8,0,7
SPIWrite 022a,20,0,7
SPIWrite 022b,21,0,7
SPIWrite 022c,86,0,7
SPIWrite 022d,f8,0,7
SPIWrite 022e,22,0,7
SPIWrite 022f,01,0,7
SPIWrite 0230,da,0,7
SPIWrite 0231,f8,0,7
SPIWrite 0232,08,0,7
SPIWrite 0233,14,0,7
SPIWrite 0234,88,0,7

SPIWrite 0235,47,0,7
SPIWrite 0236,86,0,7
SPIWrite 0237,f8,0,7
SPIWrite 0238,22,0,7
SPIWrite 0239,51,0,7
SPIWrite 023a,c8,0,7
SPIWrite 023b,f3,0,7
SPIWrite 023c,00,0,7
SPIWrite 023d,61,0,7
SPIWrite 023e,87,0,7
SPIWrite 023f,f8,0,7
SPIWrite 0240,49,0,7
SPIWrite 0241,91,0,7
SPIWrite 0242,d6,0,7
SPIWrite 0243,f8,0,7
SPIWrite 0244,20,0,7
SPIWrite 0245,01,0,7
SPIWrite 0246,e5,0,7
SPIWrite 0247,70,0,7
SPIWrite 0248,61,0,7
SPIWrite 0249,f3,0,7
SPIWrite 024a,18,0,7
SPIWrite 024b,60,0,7
SPIWrite 024c,c6,0,7
SPIWrite 024d,f8,0,7
SPIWrite 024e,20,0,7
SPIWrite 024f,01,0,7
SPIWrite 0250,bd,0,7
SPIWrite 0251,e8,0,7
SPIWrite 0252,f0,0,7
SPIWrite 0253,87,0,7
SPIWrite 0254,00,0,7
SPIWrite 0255,00,0,7
SPIWrite 0256,01,0,7
SPIWrite 0257,20,0,7
SPIWrite 0258,2d,0,7
SPIWrite 0259,e9,0,7
SPIWrite 025a,f0,0,7
SPIWrite 025b,4f,0,7
SPIWrite 025c,6e,0,7
SPIWrite 025d,4b,0,7
SPIWrite 025e,9a,0,7
SPIWrite 025f,4c,0,7
SPIWrite 0260,ad,0,7
SPIWrite 0261,f1,0,7
SPIWrite 0262,24,0,7
SPIWrite 0263,0d,0,7
SPIWrite 0264,df,0,7
SPIWrite 0265,f8,0,7
SPIWrite 0266,ac,0,7
SPIWrite 0267,81,0,7
SPIWrite 0268,04,0,7
SPIWrite 0269,90,0,7
SPIWrite 026a,1b,0,7
SPIWrite 026b,88,0,7
SPIWrite 026c,05,0,7
SPIWrite 026d,91,0,7
SPIWrite 026e,24,0,7
SPIWrite 026f,78,0,7
SPIWrite 0270,06,0,7
SPIWrite 0271,92,0,7
SPIWrite 0272,40,0,7
SPIWrite 0273,08,0,7
SPIWrite 0274,03,0,7
SPIWrite 0275,fb,0,7
SPIWrite 0276,04,0,7
SPIWrite 0277,f3,0,7
SPIWrite 0278,4f,0,7
SPIWrite 0279,ea,0,7

SPIWrite 027a,e3,0,7
SPIWrite 027b,01,0,7
SPIWrite 027c,07,0,7
SPIWrite 027d,91,0,7
SPIWrite 027e,c0,0,7
SPIWrite 027f,f0,0,7
SPIWrite 0280,6f,0,7
SPIWrite 0281,81,0,7
SPIWrite 0282,d1,0,7
SPIWrite 0283,49,0,7
SPIWrite 0284,d2,0,7
SPIWrite 0285,4c,0,7
SPIWrite 0286,0f,0,7
SPIWrite 0287,46,0,7
SPIWrite 0288,20,0,7
SPIWrite 0289,3c,0,7
SPIWrite 028a,26,0,7
SPIWrite 028b,78,0,7
SPIWrite 028c,65,0,7
SPIWrite 028d,78,0,7
SPIWrite 028e,38,0,7
SPIWrite 028f,78,0,7
SPIWrite 0290,4f,0,7
SPIWrite 0291,f0,0,7
SPIWrite 0292,01,0,7
SPIWrite 0293,09,0,7
SPIWrite 0294,01,0,7
SPIWrite 0295,90,0,7
SPIWrite 0296,01,0,7
SPIWrite 0297,28,0,7
SPIWrite 0298,81,0,7
SPIWrite 0299,f8,0,7
SPIWrite 029a,00,0,7
SPIWrite 029b,90,0,7
SPIWrite 029c,05,0,7
SPIWrite 029d,d0,0,7
SPIWrite 029e,cb,0,7
SPIWrite 029f,48,0,7
SPIWrite 02a0,00,0,7
SPIWrite 02a1,68,0,7
SPIWrite 02a2,49,0,7
SPIWrite 02a3,46,0,7
SPIWrite 02a4,02,0,7
SPIWrite 02a5,46,0,7
SPIWrite 02a6,2e,0,7
SPIWrite 02a7,20,0,7
SPIWrite 02a8,90,0,7
SPIWrite 02a9,47,0,7
SPIWrite 02aa,20,0,7
SPIWrite 02ab,68,0,7
SPIWrite 02ac,21,0,7
SPIWrite 02ad,68,0,7
SPIWrite 02ae,41,0,7
SPIWrite 02af,f0,0,7
SPIWrite 02b0,80,0,7
SPIWrite 02b1,71,0,7
SPIWrite 02b2,21,0,7
SPIWrite 02b3,60,0,7
SPIWrite 02b4,c0,0,7
SPIWrite 02b5,f3,0,7
SPIWrite 02b6,00,0,7
SPIWrite 02b7,60,0,7
SPIWrite 02b8,03,0,7
SPIWrite 02b9,90,0,7
SPIWrite 02ba,00,0,7
SPIWrite 02bb,27,0,7
SPIWrite 02bc,a7,0,7
SPIWrite 02bd,70,0,7
SPIWrite 02be,06,0,7

SPIWrite 02bf,9b,0,7
SPIWrite 02c0,84,0,7
SPIWrite 02c1,f8,0,7
SPIWrite 02c2,9d,0,7
SPIWrite 02c3,98,0,7
SPIWrite 02c4,9d,0,7
SPIWrite 02c5,42,0,7
SPIWrite 02c6,84,0,7
SPIWrite 02c7,f8,0,7
SPIWrite 02c8,a3,0,7
SPIWrite 02c9,98,0,7
SPIWrite 02ca,b4,0,7
SPIWrite 02cb,bf,0,7
SPIWrite 02cc,cb,0,7
SPIWrite 02cd,46,0,7
SPIWrite 02ce,4f,0,7
SPIWrite 02cf,f0,0,7
SPIWrite 02d0,ff,0,7
SPIWrite 02d1,3b,0,7
SPIWrite 02d2,05,0,7
SPIWrite 02d3,98,0,7
SPIWrite 02d4,ba,0,7
SPIWrite 02d5,46,0,7
SPIWrite 02d6,b0,0,7
SPIWrite 02d7,42,0,7
SPIWrite 02d8,06,0,7
SPIWrite 02d9,dc,0,7
SPIWrite 02da,30,0,7
SPIWrite 02db,1a,0,7
SPIWrite 02dc,4f,0,7
SPIWrite 02dd,f0,0,7
SPIWrite 02de,ff,0,7
SPIWrite 02df,31,0,7
SPIWrite 02e0,02,0,7
SPIWrite 02e1,28,0,7
SPIWrite 02e2,00,0,7
SPIWrite 02e3,91,0,7
SPIWrite 02e4,05,0,7
SPIWrite 02e5,da,0,7
SPIWrite 02e6,05,0,7
SPIWrite 02e7,e0,0,7
SPIWrite 02e8,80,0,7
SPIWrite 02e9,1b,0,7
SPIWrite 02ea,cd,0,7
SPIWrite 02eb,f8,0,7
SPIWrite 02ec,00,0,7
SPIWrite 02ed,90,0,7
SPIWrite 02ee,02,0,7
SPIWrite 02ef,28,0,7
SPIWrite 02f0,00,0,7
SPIWrite 02f1,db,0,7
SPIWrite 02f2,ca,0,7
SPIWrite 02f3,46,0,7
SPIWrite 02f4,bb,0,7
SPIWrite 02f5,f1,0,7
SPIWrite 02f6,01,0,7
SPIWrite 02f7,0f,0,7
SPIWrite 02f8,13,0,7
SPIWrite 02f9,bf,0,7
SPIWrite 02fa,ba,0,7
SPIWrite 02fb,f1,0,7
SPIWrite 02fc,01,0,7
SPIWrite 02fd,0f,0,7
SPIWrite 02fe,18,0,7
SPIWrite 02ff,1c,0,7
SPIWrite 0300,02,0,7
SPIWrite 0301,90,0,7
SPIWrite 0302,02,0,7
SPIWrite 0303,95,0,7

SPIWrite 0304,12,0,7
SPIWrite 0305,e1,0,7
SPIWrite 0306,0b,0,7
SPIWrite 0307,eb,0,7
SPIWrite 0308,05,0,7
SPIWrite 0309,00,0,7
SPIWrite 030a,c5,0,7
SPIWrite 030b,b2,0,7
SPIWrite 030c,65,0,7
SPIWrite 030d,70,0,7
SPIWrite 030e,84,0,7
SPIWrite 030f,f8,0,7
SPIWrite 0310,02,0,7
SPIWrite 0311,90,0,7
SPIWrite 0312,d8,0,7
SPIWrite 0313,f8,0,7
SPIWrite 0314,08,0,7
SPIWrite 0315,14,0,7
SPIWrite 0316,48,0,7
SPIWrite 0317,46,0,7
SPIWrite 0318,88,0,7
SPIWrite 0319,47,0,7
SPIWrite 031a,a7,0,7
SPIWrite 031b,70,0,7
SPIWrite 031c,07,0,7
SPIWrite 031d,98,0,7
SPIWrite 031e,d8,0,7
SPIWrite 031f,f8,0,7
SPIWrite 0320,08,0,7
SPIWrite 0321,14,0,7
SPIWrite 0322,88,0,7
SPIWrite 0323,47,0,7
SPIWrite 0324,ff,0,7
SPIWrite 0325,f7,0,7
SPIWrite 0326,94,0,7
SPIWrite 0327,fe,0,7
SPIWrite 0328,28,0,7
SPIWrite 0329,e2,0,7
SPIWrite 032a,0b,0,7
SPIWrite 032b,eb,0,7
SPIWrite 032c,05,0,7
SPIWrite 032d,00,0,7
SPIWrite 032e,c5,0,7
SPIWrite 032f,b2,0,7
SPIWrite 0330,65,0,7
SPIWrite 0331,70,0,7
SPIWrite 0332,84,0,7
SPIWrite 0333,f8,0,7
SPIWrite 0334,02,0,7
SPIWrite 0335,90,0,7
SPIWrite 0336,d8,0,7
SPIWrite 0337,f8,0,7
SPIWrite 0338,08,0,7
SPIWrite 0339,14,0,7
SPIWrite 033a,48,0,7
SPIWrite 033b,46,0,7
SPIWrite 033c,88,0,7
SPIWrite 033d,47,0,7
SPIWrite 033e,a7,0,7
SPIWrite 033f,70,0,7
SPIWrite 0340,07,0,7
SPIWrite 0341,98,0,7
SPIWrite 0342,d8,0,7
SPIWrite 0343,f8,0,7
SPIWrite 0344,08,0,7
SPIWrite 0345,14,0,7
SPIWrite 0346,88,0,7
SPIWrite 0347,47,0,7
SPIWrite 0348,ff,0,7

SPIWrite 0349,f7,0,7
SPIWrite 034a,82,0,7
SPIWrite 034b,fe,0,7
SPIWrite 034c,02,0,7
SPIWrite 034d,98,0,7
SPIWrite 034e,a8,0,7
SPIWrite 034f,42,0,7
SPIWrite 0350,02,0,7
SPIWrite 0351,d0,0,7
SPIWrite 0352,ba,0,7
SPIWrite 0353,f1,0,7
SPIWrite 0354,00,0,7
SPIWrite 0355,0f,0,7
SPIWrite 0356,e8,0,7
SPIWrite 0357,d0,0,7
SPIWrite 0358,00,0,7
SPIWrite 0359,98,0,7
SPIWrite 035a,ce,0,7
SPIWrite 035b,4b,0,7
SPIWrite 035c,80,0,7
SPIWrite 035d,19,0,7
SPIWrite 035e,c6,0,7
SPIWrite 035f,b2,0,7
SPIWrite 0360,c3,0,7
SPIWrite 0361,eb,0,7
SPIWrite 0362,c6,0,7
SPIWrite 0363,00,0,7
SPIWrite 0364,01,0,7
SPIWrite 0365,68,0,7
SPIWrite 0366,40,0,7
SPIWrite 0367,68,0,7
SPIWrite 0368,c4,0,7
SPIWrite 0369,f8,0,7
SPIWrite 036a,98,0,7
SPIWrite 036b,0a,0,7
SPIWrite 036c,84,0,7
SPIWrite 036d,f8,0,7
SPIWrite 036e,9c,0,7
SPIWrite 036f,1a,0,7
SPIWrite 0370,84,0,7
SPIWrite 0371,f8,0,7
SPIWrite 0372,a2,0,7
SPIWrite 0373,7a,0,7
SPIWrite 0374,84,0,7
SPIWrite 0375,f8,0,7
SPIWrite 0376,a2,0,7
SPIWrite 0377,9a,0,7
SPIWrite 0378,07,0,7
SPIWrite 0379,98,0,7
SPIWrite 037a,84,0,7
SPIWrite 037b,f8,0,7
SPIWrite 037c,a2,0,7
SPIWrite 037d,7a,0,7
SPIWrite 037e,d8,0,7
SPIWrite 037f,f8,0,7
SPIWrite 0380,08,0,7
SPIWrite 0381,14,0,7
SPIWrite 0382,88,0,7
SPIWrite 0383,47,0,7
SPIWrite 0384,ff,0,7
SPIWrite 0385,f7,0,7
SPIWrite 0386,64,0,7
SPIWrite 0387,fe,0,7
SPIWrite 0388,ba,0,7
SPIWrite 0389,46,0,7
SPIWrite 038a,f3,0,7
SPIWrite 038b,e1,0,7
SPIWrite 038c,0b,0,7
SPIWrite 038d,eb,0,7

SPIWrite 038e,05,0,7
SPIWrite 038f,00,0,7
SPIWrite 0390,c5,0,7
SPIWrite 0391,b2,0,7
SPIWrite 0392,65,0,7
SPIWrite 0393,70,0,7
SPIWrite 0394,84,0,7
SPIWrite 0395,f8,0,7
SPIWrite 0396,02,0,7
SPIWrite 0397,90,0,7
SPIWrite 0398,d8,0,7
SPIWrite 0399,f8,0,7
SPIWrite 039a,08,0,7
SPIWrite 039b,14,0,7
SPIWrite 039c,48,0,7
SPIWrite 039d,46,0,7
SPIWrite 039e,88,0,7
SPIWrite 039f,47,0,7
SPIWrite 03a0,a7,0,7
SPIWrite 03a1,70,0,7
SPIWrite 03a2,07,0,7
SPIWrite 03a3,98,0,7
SPIWrite 03a4,d8,0,7
SPIWrite 03a5,f8,0,7
SPIWrite 03a6,08,0,7
SPIWrite 03a7,14,0,7
SPIWrite 03a8,88,0,7
SPIWrite 03a9,47,0,7
SPIWrite 03aa,ff,0,7
SPIWrite 03ab,f7,0,7
SPIWrite 03ac,51,0,7
SPIWrite 03ad,fe,0,7
SPIWrite 03ae,87,0,7
SPIWrite 03af,e1,0,7
SPIWrite 03b0,0b,0,7
SPIWrite 03b1,eb,0,7
SPIWrite 03b2,05,0,7
SPIWrite 03b3,00,0,7
SPIWrite 03b4,c5,0,7
SPIWrite 03b5,b2,0,7
SPIWrite 03b6,65,0,7
SPIWrite 03b7,70,0,7
SPIWrite 03b8,84,0,7
SPIWrite 03b9,f8,0,7
SPIWrite 03ba,02,0,7
SPIWrite 03bb,90,0,7
SPIWrite 03bc,d8,0,7
SPIWrite 03bd,f8,0,7
SPIWrite 03be,08,0,7
SPIWrite 03bf,14,0,7
SPIWrite 03c0,48,0,7
SPIWrite 03c1,46,0,7
SPIWrite 03c2,88,0,7
SPIWrite 03c3,47,0,7
SPIWrite 03c4,a7,0,7
SPIWrite 03c5,70,0,7
SPIWrite 03c6,07,0,7
SPIWrite 03c7,98,0,7
SPIWrite 03c8,d8,0,7
SPIWrite 03c9,f8,0,7
SPIWrite 03ca,08,0,7
SPIWrite 03cb,14,0,7
SPIWrite 03cc,88,0,7
SPIWrite 03cd,47,0,7
SPIWrite 03ce,ff,0,7
SPIWrite 03cf,f7,0,7
SPIWrite 03d0,3f,0,7
SPIWrite 03d1,fe,0,7
SPIWrite 03d2,02,0,7

SPIWrite 03d3,98,0,7
SPIWrite 03d4,a8,0,7
SPIWrite 03d5,42,0,7
SPIWrite 03d6,02,0,7
SPIWrite 03d7,d0,0,7
SPIWrite 03d8,ba,0,7
SPIWrite 03d9,f1,0,7
SPIWrite 03da,00,0,7
SPIWrite 03db,0f,0,7
SPIWrite 03dc,e8,0,7
SPIWrite 03dd,d0,0,7
SPIWrite 03de,00,0,7
SPIWrite 03df,98,0,7
SPIWrite 03e0,ad,0,7
SPIWrite 03e1,4b,0,7
SPIWrite 03e2,80,0,7
SPIWrite 03e3,19,0,7
SPIWrite 03e4,c6,0,7
SPIWrite 03e5,b2,0,7
SPIWrite 03e6,c3,0,7
SPIWrite 03e7,eb,0,7
SPIWrite 03e8,c6,0,7
SPIWrite 03e9,00,0,7
SPIWrite 03ea,01,0,7
SPIWrite 03eb,68,0,7
SPIWrite 03ec,40,0,7
SPIWrite 03ed,68,0,7
SPIWrite 03ee,c4,0,7
SPIWrite 03ef,f8,0,7
SPIWrite 03f0,98,0,7
SPIWrite 03f1,08,0,7
SPIWrite 03f2,84,0,7
SPIWrite 03f3,f8,0,7
SPIWrite 03f4,9c,0,7
SPIWrite 03f5,18,0,7
SPIWrite 03f6,84,0,7
SPIWrite 03f7,f8,0,7
SPIWrite 03f8,a2,0,7
SPIWrite 03f9,78,0,7
SPIWrite 03fa,84,0,7
SPIWrite 03fb,f8,0,7
SPIWrite 03fc,a2,0,7
SPIWrite 03fd,98,0,7
SPIWrite 03fe,07,0,7
SPIWrite 03ff,98,0,7
SPIWrite 0400,84,0,7
SPIWrite 0401,f8,0,7
SPIWrite 0402,a2,0,7
SPIWrite 0403,78,0,7
SPIWrite 0404,d8,0,7
SPIWrite 0405,f8,0,7
SPIWrite 0406,08,0,7
SPIWrite 0407,14,0,7
SPIWrite 0408,88,0,7
SPIWrite 0409,47,0,7
SPIWrite 040a,ff,0,7
SPIWrite 040b,f7,0,7
SPIWrite 040c,21,0,7
SPIWrite 040d,fe,0,7
SPIWrite 040e,ba,0,7
SPIWrite 040f,46,0,7
SPIWrite 0410,52,0,7
SPIWrite 0411,e1,0,7
SPIWrite 0412,c0,0,7
SPIWrite 0413,46,0,7
SPIWrite 0414,40,0,7
SPIWrite 0415,52,0,7
SPIWrite 0416,00,0,7
SPIWrite 0417,20,0,7

SPIWrite 0418,d8,0,7
SPIWrite 0419,33,0,7
SPIWrite 041a,00,0,7
SPIWrite 041b,20,0,7
SPIWrite 041c,0b,0,7
SPIWrite 041d,eb,0,7
SPIWrite 041e,05,0,7
SPIWrite 041f,00,0,7
SPIWrite 0420,c5,0,7
SPIWrite 0421,b2,0,7
SPIWrite 0422,65,0,7
SPIWrite 0423,70,0,7
SPIWrite 0424,84,0,7
SPIWrite 0425,f8,0,7
SPIWrite 0426,02,0,7
SPIWrite 0427,90,0,7
SPIWrite 0428,d8,0,7
SPIWrite 0429,f8,0,7
SPIWrite 042a,08,0,7
SPIWrite 042b,14,0,7
SPIWrite 042c,48,0,7
SPIWrite 042d,46,0,7
SPIWrite 042e,88,0,7
SPIWrite 042f,47,0,7
SPIWrite 0430,a7,0,7
SPIWrite 0431,70,0,7
SPIWrite 0432,07,0,7
SPIWrite 0433,98,0,7
SPIWrite 0434,d8,0,7
SPIWrite 0435,f8,0,7
SPIWrite 0436,08,0,7
SPIWrite 0437,14,0,7
SPIWrite 0438,88,0,7
SPIWrite 0439,47,0,7
SPIWrite 043a,ff,0,7
SPIWrite 043b,f7,0,7
SPIWrite 043c,09,0,7
SPIWrite 043d,fe,0,7
SPIWrite 043e,db,0,7
SPIWrite 043f,e0,0,7
SPIWrite 0440,0b,0,7
SPIWrite 0441,eb,0,7
SPIWrite 0442,05,0,7
SPIWrite 0443,00,0,7
SPIWrite 0444,c5,0,7
SPIWrite 0445,b2,0,7
SPIWrite 0446,65,0,7
SPIWrite 0447,70,0,7
SPIWrite 0448,84,0,7
SPIWrite 0449,f8,0,7
SPIWrite 044a,02,0,7
SPIWrite 044b,90,0,7
SPIWrite 044c,d8,0,7
SPIWrite 044d,f8,0,7
SPIWrite 044e,08,0,7
SPIWrite 044f,14,0,7
SPIWrite 0450,48,0,7
SPIWrite 0451,46,0,7
SPIWrite 0452,88,0,7
SPIWrite 0453,47,0,7
SPIWrite 0454,a7,0,7
SPIWrite 0455,70,0,7
SPIWrite 0456,07,0,7
SPIWrite 0457,98,0,7
SPIWrite 0458,d8,0,7
SPIWrite 0459,f8,0,7
SPIWrite 045a,08,0,7
SPIWrite 045b,14,0,7
SPIWrite 045c,88,0,7

SPIWrite 045d,47,0,7
SPIWrite 045e,ff,0,7
SPIWrite 045f,f7,0,7
SPIWrite 0460,f7,0,7
SPIWrite 0461,fd,0,7
SPIWrite 0462,02,0,7
SPIWrite 0463,98,0,7
SPIWrite 0464,a8,0,7
SPIWrite 0465,42,0,7
SPIWrite 0466,02,0,7
SPIWrite 0467,d0,0,7
SPIWrite 0468,ba,0,7
SPIWrite 0469,f1,0,7
SPIWrite 046a,00,0,7
SPIWrite 046b,0f,0,7
SPIWrite 046c,e8,0,7
SPIWrite 046d,d0,0,7
SPIWrite 046e,00,0,7
SPIWrite 046f,98,0,7
SPIWrite 0470,d0,0,7
SPIWrite 0471,4b,0,7
SPIWrite 0472,80,0,7
SPIWrite 0473,19,0,7
SPIWrite 0474,c6,0,7
SPIWrite 0475,b2,0,7
SPIWrite 0476,c3,0,7
SPIWrite 0477,eb,0,7
SPIWrite 0478,c6,0,7
SPIWrite 0479,00,0,7
SPIWrite 047a,01,0,7
SPIWrite 047b,68,0,7
SPIWrite 047c,40,0,7
SPIWrite 047d,68,0,7
SPIWrite 047e,c4,0,7
SPIWrite 047f,f8,0,7
SPIWrite 0480,98,0,7
SPIWrite 0481,0a,0,7
SPIWrite 0482,84,0,7
SPIWrite 0483,f8,0,7
SPIWrite 0484,9c,0,7
SPIWrite 0485,1a,0,7
SPIWrite 0486,84,0,7
SPIWrite 0487,f8,0,7
SPIWrite 0488,a2,0,7
SPIWrite 0489,7a,0,7
SPIWrite 048a,84,0,7
SPIWrite 048b,f8,0,7
SPIWrite 048c,a2,0,7
SPIWrite 048d,9a,0,7
SPIWrite 048e,07,0,7
SPIWrite 048f,98,0,7
SPIWrite 0490,84,0,7
SPIWrite 0491,f8,0,7
SPIWrite 0492,a2,0,7
SPIWrite 0493,7a,0,7
SPIWrite 0494,d8,0,7
SPIWrite 0495,f8,0,7
SPIWrite 0496,08,0,7
SPIWrite 0497,14,0,7
SPIWrite 0498,88,0,7
SPIWrite 0499,47,0,7
SPIWrite 049a,ff,0,7
SPIWrite 049b,f7,0,7
SPIWrite 049c,d9,0,7
SPIWrite 049d,fd,0,7
SPIWrite 049e,ba,0,7
SPIWrite 049f,46,0,7
SPIWrite 04a0,a6,0,7
SPIWrite 04a1,e0,0,7

SPIWrite 04a2,0b,0,7
SPIWrite 04a3,eb,0,7
SPIWrite 04a4,05,0,7
SPIWrite 04a5,00,0,7
SPIWrite 04a6,c5,0,7
SPIWrite 04a7,b2,0,7
SPIWrite 04a8,65,0,7
SPIWrite 04a9,70,0,7
SPIWrite 04aa,84,0,7
SPIWrite 04ab,f8,0,7
SPIWrite 04ac,02,0,7
SPIWrite 04ad,90,0,7
SPIWrite 04ae,d8,0,7
SPIWrite 04af,f8,0,7
SPIWrite 04b0,08,0,7
SPIWrite 04b1,14,0,7
SPIWrite 04b2,48,0,7
SPIWrite 04b3,46,0,7
SPIWrite 04b4,88,0,7
SPIWrite 04b5,47,0,7
SPIWrite 04b6,a7,0,7
SPIWrite 04b7,70,0,7
SPIWrite 04b8,07,0,7
SPIWrite 04b9,98,0,7
SPIWrite 04ba,d8,0,7
SPIWrite 04bb,f8,0,7
SPIWrite 04bc,08,0,7
SPIWrite 04bd,14,0,7
SPIWrite 04be,88,0,7
SPIWrite 04bf,47,0,7
SPIWrite 04c0,ff,0,7
SPIWrite 04c1,f7,0,7
SPIWrite 04c2,c6,0,7
SPIWrite 04c3,fd,0,7
SPIWrite 04c4,35,0,7
SPIWrite 04c5,e0,0,7
SPIWrite 04c6,c0,0,7
SPIWrite 04c7,46,0,7
SPIWrite 04c8,14,0,7
SPIWrite 04c9,00,0,7
SPIWrite 04ca,01,0,7
SPIWrite 04cb,20,0,7
SPIWrite 04cc,0b,0,7
SPIWrite 04cd,eb,0,7
SPIWrite 04ce,05,0,7
SPIWrite 04cf,00,0,7
SPIWrite 04d0,c5,0,7
SPIWrite 04d1,b2,0,7
SPIWrite 04d2,65,0,7
SPIWrite 04d3,70,0,7
SPIWrite 04d4,84,0,7
SPIWrite 04d5,f8,0,7
SPIWrite 04d6,02,0,7
SPIWrite 04d7,90,0,7
SPIWrite 04d8,d8,0,7
SPIWrite 04d9,f8,0,7
SPIWrite 04da,08,0,7
SPIWrite 04db,14,0,7
SPIWrite 04dc,48,0,7
SPIWrite 04dd,46,0,7
SPIWrite 04de,88,0,7
SPIWrite 04df,47,0,7
SPIWrite 04e0,a7,0,7
SPIWrite 04e1,70,0,7
SPIWrite 04e2,07,0,7
SPIWrite 04e3,98,0,7
SPIWrite 04e4,d8,0,7
SPIWrite 04e5,f8,0,7
SPIWrite 04e6,08,0,7

SPIWrite 04e7,14,0,7
SPIWrite 04e8,88,0,7
SPIWrite 04e9,47,0,7
SPIWrite 04ea,ff,0,7
SPIWrite 04eb,f7,0,7
SPIWrite 04ec,b1,0,7
SPIWrite 04ed,fd,0,7
SPIWrite 04ee,02,0,7
SPIWrite 04ef,98,0,7
SPIWrite 04f0,a8,0,7
SPIWrite 04f1,42,0,7
SPIWrite 04f2,02,0,7
SPIWrite 04f3,d0,0,7
SPIWrite 04f4,ba,0,7
SPIWrite 04f5,f1,0,7
SPIWrite 04f6,00,0,7
SPIWrite 04f7,0f,0,7
SPIWrite 04f8,e8,0,7
SPIWrite 04f9,d0,0,7
SPIWrite 04fa,00,0,7
SPIWrite 04fb,98,0,7
SPIWrite 04fc,ae,0,7
SPIWrite 04fd,4b,0,7
SPIWrite 04fe,80,0,7
SPIWrite 04ff,19,0,7
SPIWrite 0500,c6,0,7
SPIWrite 0501,b2,0,7
SPIWrite 0502,c3,0,7
SPIWrite 0503,eb,0,7
SPIWrite 0504,c6,0,7
SPIWrite 0505,00,0,7
SPIWrite 0506,01,0,7
SPIWrite 0507,68,0,7
SPIWrite 0508,40,0,7
SPIWrite 0509,68,0,7
SPIWrite 050a,c4,0,7
SPIWrite 050b,f8,0,7
SPIWrite 050c,98,0,7
SPIWrite 050d,08,0,7
SPIWrite 050e,84,0,7
SPIWrite 050f,f8,0,7
SPIWrite 0510,9c,0,7
SPIWrite 0511,18,0,7
SPIWrite 0512,84,0,7
SPIWrite 0513,f8,0,7
SPIWrite 0514,a2,0,7
SPIWrite 0515,78,0,7
SPIWrite 0516,84,0,7
SPIWrite 0517,f8,0,7
SPIWrite 0518,a2,0,7
SPIWrite 0519,98,0,7
SPIWrite 051a,07,0,7
SPIWrite 051b,98,0,7
SPIWrite 051c,84,0,7
SPIWrite 051d,f8,0,7
SPIWrite 051e,a2,0,7
SPIWrite 051f,78,0,7
SPIWrite 0520,d8,0,7
SPIWrite 0521,f8,0,7
SPIWrite 0522,08,0,7
SPIWrite 0523,14,0,7
SPIWrite 0524,88,0,7
SPIWrite 0525,47,0,7
SPIWrite 0526,ff,0,7
SPIWrite 0527,f7,0,7
SPIWrite 0528,93,0,7
SPIWrite 0529,fd,0,7
SPIWrite 052a,ba,0,7
SPIWrite 052b,46,0,7

SPIWrite 052c,05,0,7
SPIWrite 052d,98,0,7
SPIWrite 052e,b0,0,7
SPIWrite 052f,42,0,7
SPIWrite 0530,dd,0,7
SPIWrite 0531,d1,0,7
SPIWrite 0532,06,0,7
SPIWrite 0533,98,0,7
SPIWrite 0534,85,0,7
SPIWrite 0535,42,0,7
SPIWrite 0536,b4,0,7
SPIWrite 0537,d1,0,7
SPIWrite 0538,84,0,7
SPIWrite 0539,f8,0,7
SPIWrite 053a,9d,0,7
SPIWrite 053b,78,0,7
SPIWrite 053c,84,0,7
SPIWrite 053d,f8,0,7
SPIWrite 053e,a3,0,7
SPIWrite 053f,78,0,7
SPIWrite 0540,26,0,7
SPIWrite 0541,70,0,7
SPIWrite 0542,84,0,7
SPIWrite 0543,f8,0,7
SPIWrite 0544,02,0,7
SPIWrite 0545,90,0,7
SPIWrite 0546,d8,0,7
SPIWrite 0547,f8,0,7
SPIWrite 0548,08,0,7
SPIWrite 0549,14,0,7
SPIWrite 054a,48,0,7
SPIWrite 054b,46,0,7
SPIWrite 054c,88,0,7
SPIWrite 054d,47,0,7
SPIWrite 054e,1e,0,7
SPIWrite 054f,4d,0,7
SPIWrite 0550,a7,0,7
SPIWrite 0551,70,0,7
SPIWrite 0552,03,0,7
SPIWrite 0553,99,0,7
SPIWrite 0554,20,0,7
SPIWrite 0555,68,0,7
SPIWrite 0556,61,0,7
SPIWrite 0557,f3,0,7
SPIWrite 0558,18,0,7
SPIWrite 0559,60,0,7
SPIWrite 055a,20,0,7
SPIWrite 055b,60,0,7
SPIWrite 055c,01,0,7
SPIWrite 055d,9e,0,7
SPIWrite 055e,2e,0,7
SPIWrite 055f,70,0,7
SPIWrite 0560,04,0,7
SPIWrite 0561,98,0,7
SPIWrite 0562,80,0,7
SPIWrite 0563,08,0,7
SPIWrite 0564,60,0,7
SPIWrite 0565,d3,0,7
SPIWrite 0566,1a,0,7
SPIWrite 0567,4c,0,7
SPIWrite 0568,94,0,7
SPIWrite 0569,48,0,7
SPIWrite 056a,26,0,7
SPIWrite 056b,78,0,7
SPIWrite 056c,4f,0,7
SPIWrite 056d,f0,0,7
SPIWrite 056e,01,0,7
SPIWrite 056f,09,0,7
SPIWrite 0570,00,0,7

SPIWrite 0571,27,0,7
SPIWrite 0572,65,0,7
SPIWrite 0573,78,0,7
SPIWrite 0574,00,0,7
SPIWrite 0575,78,0,7
SPIWrite 0576,91,0,7
SPIWrite 0577,49,0,7
SPIWrite 0578,01,0,7
SPIWrite 0579,90,0,7
SPIWrite 057a,01,0,7
SPIWrite 057b,28,0,7
SPIWrite 057c,81,0,7
SPIWrite 057d,f8,0,7
SPIWrite 057e,00,0,7
SPIWrite 057f,90,0,7
SPIWrite 0580,04,0,7
SPIWrite 0581,d0,0,7
SPIWrite 0582,d8,0,7
SPIWrite 0583,f8,0,7
SPIWrite 0584,1c,0,7
SPIWrite 0585,23,0,7
SPIWrite 0586,2e,0,7
SPIWrite 0587,20,0,7
SPIWrite 0588,49,0,7
SPIWrite 0589,46,0,7
SPIWrite 058a,90,0,7
SPIWrite 058b,47,0,7
SPIWrite 058c,21,0,7
SPIWrite 058d,68,0,7
SPIWrite 058e,20,0,7
SPIWrite 058f,68,0,7
SPIWrite 0590,c1,0,7
SPIWrite 0591,f3,0,7
SPIWrite 0592,00,0,7
SPIWrite 0593,63,0,7
SPIWrite 0594,03,0,7
SPIWrite 0595,93,0,7
SPIWrite 0596,40,0,7
SPIWrite 0597,f0,0,7
SPIWrite 0598,80,0,7
SPIWrite 0599,70,0,7
SPIWrite 059a,20,0,7
SPIWrite 059b,60,0,7
SPIWrite 059c,a7,0,7
SPIWrite 059d,70,0,7
SPIWrite 059e,06,0,7
SPIWrite 059f,9a,0,7
SPIWrite 05a0,84,0,7
SPIWrite 05a1,f8,0,7
SPIWrite 05a2,9d,0,7
SPIWrite 05a3,9a,0,7
SPIWrite 05a4,95,0,7
SPIWrite 05a5,42,0,7
SPIWrite 05a6,84,0,7
SPIWrite 05a7,f8,0,7
SPIWrite 05a8,a3,0,7
SPIWrite 05a9,9a,0,7
SPIWrite 05aa,b4,0,7
SPIWrite 05ab,bf,0,7
SPIWrite 05ac,cb,0,7
SPIWrite 05ad,46,0,7
SPIWrite 05ae,4f,0,7
SPIWrite 05af,f0,0,7
SPIWrite 05b0,ff,0,7
SPIWrite 05b1,3b,0,7
SPIWrite 05b2,05,0,7
SPIWrite 05b3,98,0,7
SPIWrite 05b4,ba,0,7
SPIWrite 05b5,46,0,7

SPIWrite 05b6,b0,0,7
SPIWrite 05b7,42,0,7
SPIWrite 05b8,0c,0,7
SPIWrite 05b9,dc,0,7
SPIWrite 05ba,30,0,7
SPIWrite 05bb,1a,0,7
SPIWrite 05bc,4f,0,7
SPIWrite 05bd,f0,0,7
SPIWrite 05be,ff,0,7
SPIWrite 05bf,31,0,7
SPIWrite 05c0,02,0,7
SPIWrite 05c1,28,0,7
SPIWrite 05c2,00,0,7
SPIWrite 05c3,91,0,7
SPIWrite 05c4,0b,0,7
SPIWrite 05c5,da,0,7
SPIWrite 05c6,0b,0,7
SPIWrite 05c7,e0,0,7
SPIWrite 05c8,39,0,7
SPIWrite 05c9,01,0,7
SPIWrite 05ca,00,0,7
SPIWrite 05cb,78,0,7
SPIWrite 05cc,5c,0,7
SPIWrite 05cd,55,0,7
SPIWrite 05ce,00,0,7
SPIWrite 05cf,20,0,7
SPIWrite 05d0,20,0,7
SPIWrite 05d1,01,0,7
SPIWrite 05d2,00,0,7
SPIWrite 05d3,ae,0,7
SPIWrite 05d4,80,0,7
SPIWrite 05d5,1b,0,7
SPIWrite 05d6,cd,0,7
SPIWrite 05d7,f8,0,7
SPIWrite 05d8,00,0,7
SPIWrite 05d9,90,0,7
SPIWrite 05da,02,0,7
SPIWrite 05db,28,0,7
SPIWrite 05dc,00,0,7
SPIWrite 05dd,db,0,7
SPIWrite 05de,ca,0,7
SPIWrite 05df,46,0,7
SPIWrite 05e0,bb,0,7
SPIWrite 05e1,f1,0,7
SPIWrite 05e2,01,0,7
SPIWrite 05e3,0f,0,7
SPIWrite 05e4,13,0,7
SPIWrite 05e5,bf,0,7
SPIWrite 05e6,ba,0,7
SPIWrite 05e7,f1,0,7
SPIWrite 05e8,01,0,7
SPIWrite 05e9,0f,0,7
SPIWrite 05ea,10,0,7
SPIWrite 05eb,1c,0,7
SPIWrite 05ec,02,0,7
SPIWrite 05ed,90,0,7
SPIWrite 05ee,02,0,7
SPIWrite 05ef,95,0,7
SPIWrite 05f0,05,0,7
SPIWrite 05f1,98,0,7
SPIWrite 05f2,b0,0,7
SPIWrite 05f3,42,0,7
SPIWrite 05f4,7f,0,7
SPIWrite 05f5,f4,0,7
SPIWrite 05f6,35,0,7
SPIWrite 05f7,af,0,7
SPIWrite 05f8,06,0,7
SPIWrite 05f9,98,0,7
SPIWrite 05fa,85,0,7

SPIWrite 05fb,42,0,7
SPIWrite 05fc,7f,0,7
SPIWrite 05fd,f4,0,7
SPIWrite 05fe,0e,0,7
SPIWrite 05ff,af,0,7
SPIWrite 0600,84,0,7
SPIWrite 0601,f8,0,7
SPIWrite 0602,9d,0,7
SPIWrite 0603,7a,0,7
SPIWrite 0604,84,0,7
SPIWrite 0605,f8,0,7
SPIWrite 0606,a3,0,7
SPIWrite 0607,7a,0,7
SPIWrite 0608,26,0,7
SPIWrite 0609,70,0,7
SPIWrite 060a,84,0,7
SPIWrite 060b,f8,0,7
SPIWrite 060c,02,0,7
SPIWrite 060d,90,0,7
SPIWrite 060e,d8,0,7
SPIWrite 060f,f8,0,7
SPIWrite 0610,08,0,7
SPIWrite 0611,14,0,7
SPIWrite 0612,48,0,7
SPIWrite 0613,46,0,7
SPIWrite 0614,88,0,7
SPIWrite 0615,47,0,7
SPIWrite 0616,a7,0,7
SPIWrite 0617,70,0,7
SPIWrite 0618,03,0,7
SPIWrite 0619,99,0,7
SPIWrite 061a,68,0,7
SPIWrite 061b,4d,0,7
SPIWrite 061c,20,0,7
SPIWrite 061d,68,0,7
SPIWrite 061e,61,0,7
SPIWrite 061f,f3,0,7
SPIWrite 0620,18,0,7
SPIWrite 0621,60,0,7
SPIWrite 0622,20,0,7
SPIWrite 0623,60,0,7
SPIWrite 0624,01,0,7
SPIWrite 0625,9e,0,7
SPIWrite 0626,2e,0,7
SPIWrite 0627,70,0,7
SPIWrite 0628,04,0,7
SPIWrite 0629,98,0,7
SPIWrite 062a,c0,0,7
SPIWrite 062b,08,0,7
SPIWrite 062c,60,0,7
SPIWrite 062d,d3,0,7
SPIWrite 062e,64,0,7
SPIWrite 062f,49,0,7
SPIWrite 0630,64,0,7
SPIWrite 0631,4c,0,7
SPIWrite 0632,0f,0,7
SPIWrite 0633,46,0,7
SPIWrite 0634,20,0,7
SPIWrite 0635,3c,0,7
SPIWrite 0636,26,0,7
SPIWrite 0637,78,0,7
SPIWrite 0638,65,0,7
SPIWrite 0639,78,0,7
SPIWrite 063a,38,0,7
SPIWrite 063b,78,0,7
SPIWrite 063c,4f,0,7
SPIWrite 063d,f0,0,7
SPIWrite 063e,01,0,7
SPIWrite 063f,09,0,7

SPIWrite 0640,01,0,7
SPIWrite 0641,28,0,7
SPIWrite 0642,01,0,7
SPIWrite 0643,90,0,7
SPIWrite 0644,4f,0,7
SPIWrite 0645,f0,0,7
SPIWrite 0646,00,0,7
SPIWrite 0647,07,0,7
SPIWrite 0648,81,0,7
SPIWrite 0649,f8,0,7
SPIWrite 064a,00,0,7
SPIWrite 064b,90,0,7
SPIWrite 064c,04,0,7
SPIWrite 064d,d0,0,7
SPIWrite 064e,d8,0,7
SPIWrite 064f,f8,0,7
SPIWrite 0650,1c,0,7
SPIWrite 0651,23,0,7
SPIWrite 0652,2e,0,7
SPIWrite 0653,20,0,7
SPIWrite 0654,49,0,7
SPIWrite 0655,46,0,7
SPIWrite 0656,90,0,7
SPIWrite 0657,47,0,7
SPIWrite 0658,21,0,7
SPIWrite 0659,68,0,7
SPIWrite 065a,20,0,7
SPIWrite 065b,68,0,7
SPIWrite 065c,c1,0,7
SPIWrite 065d,f3,0,7
SPIWrite 065e,00,0,7
SPIWrite 065f,63,0,7
SPIWrite 0660,03,0,7
SPIWrite 0661,93,0,7
SPIWrite 0662,40,0,7
SPIWrite 0663,f0,0,7
SPIWrite 0664,80,0,7
SPIWrite 0665,70,0,7
SPIWrite 0666,20,0,7
SPIWrite 0667,60,0,7
SPIWrite 0668,a7,0,7
SPIWrite 0669,70,0,7
SPIWrite 066a,06,0,7
SPIWrite 066b,9a,0,7
SPIWrite 066c,84,0,7
SPIWrite 066d,f8,0,7
SPIWrite 066e,9d,0,7
SPIWrite 066f,98,0,7
SPIWrite 0670,95,0,7
SPIWrite 0671,42,0,7
SPIWrite 0672,84,0,7
SPIWrite 0673,f8,0,7
SPIWrite 0674,a3,0,7
SPIWrite 0675,98,0,7
SPIWrite 0676,b4,0,7
SPIWrite 0677,bf,0,7
SPIWrite 0678,cb,0,7
SPIWrite 0679,46,0,7
SPIWrite 067a,4f,0,7
SPIWrite 067b,f0,0,7
SPIWrite 067c,ff,0,7
SPIWrite 067d,3b,0,7
SPIWrite 067e,05,0,7
SPIWrite 067f,98,0,7
SPIWrite 0680,ba,0,7
SPIWrite 0681,46,0,7
SPIWrite 0682,b0,0,7
SPIWrite 0683,42,0,7
SPIWrite 0684,0a,0,7

SPIWrite 0685,dc,0,7
SPIWrite 0686,30,0,7
SPIWrite 0687,1a,0,7
SPIWrite 0688,4f,0,7
SPIWrite 0689,f0,0,7
SPIWrite 068a,ff,0,7
SPIWrite 068b,31,0,7
SPIWrite 068c,02,0,7
SPIWrite 068d,28,0,7
SPIWrite 068e,00,0,7
SPIWrite 068f,91,0,7
SPIWrite 0690,09,0,7
SPIWrite 0691,da,0,7
SPIWrite 0692,09,0,7
SPIWrite 0693,e0,0,7
SPIWrite 0694,38,0,7
SPIWrite 0695,f4,0,7
SPIWrite 0696,ff,0,7
SPIWrite 0697,50,0,7
SPIWrite 0698,58,0,7
SPIWrite 0699,f6,0,7
SPIWrite 069a,ff,0,7
SPIWrite 069b,50,0,7
SPIWrite 069c,80,0,7
SPIWrite 069d,1b,0,7
SPIWrite 069e,cd,0,7
SPIWrite 069f,f8,0,7
SPIWrite 06a0,00,0,7
SPIWrite 06a1,90,0,7
SPIWrite 06a2,02,0,7
SPIWrite 06a3,28,0,7
SPIWrite 06a4,00,0,7
SPIWrite 06a5,db,0,7
SPIWrite 06a6,ca,0,7
SPIWrite 06a7,46,0,7
SPIWrite 06a8,bb,0,7
SPIWrite 06a9,f1,0,7
SPIWrite 06aa,01,0,7
SPIWrite 06ab,0f,0,7
SPIWrite 06ac,13,0,7
SPIWrite 06ad,bf,0,7
SPIWrite 06ae,ba,0,7
SPIWrite 06af,f1,0,7
SPIWrite 06b0,01,0,7
SPIWrite 06b1,0f,0,7
SPIWrite 06b2,10,0,7
SPIWrite 06b3,1c,0,7
SPIWrite 06b4,02,0,7
SPIWrite 06b5,90,0,7
SPIWrite 06b6,02,0,7
SPIWrite 06b7,95,0,7
SPIWrite 06b8,05,0,7
SPIWrite 06b9,98,0,7
SPIWrite 06ba,b0,0,7
SPIWrite 06bb,42,0,7
SPIWrite 06bc,7f,0,7
SPIWrite 06bd,f4,0,7
SPIWrite 06be,89,0,7
SPIWrite 06bf,ae,0,7
SPIWrite 06c0,06,0,7
SPIWrite 06c1,98,0,7
SPIWrite 06c2,85,0,7
SPIWrite 06c3,42,0,7
SPIWrite 06c4,7f,0,7
SPIWrite 06c5,f4,0,7
SPIWrite 06c6,62,0,7
SPIWrite 06c7,ae,0,7
SPIWrite 06c8,84,0,7
SPIWrite 06c9,f8,0,7

SPIWrite 06ca,9d,0,7
SPIWrite 06cb,78,0,7
SPIWrite 06cc,84,0,7
SPIWrite 06cd,f8,0,7
SPIWrite 06ce,a3,0,7
SPIWrite 06cf,78,0,7
SPIWrite 06d0,26,0,7
SPIWrite 06d1,70,0,7
SPIWrite 06d2,84,0,7
SPIWrite 06d3,f8,0,7
SPIWrite 06d4,02,0,7
SPIWrite 06d5,90,0,7
SPIWrite 06d6,d8,0,7
SPIWrite 06d7,f8,0,7
SPIWrite 06d8,08,0,7
SPIWrite 06d9,14,0,7
SPIWrite 06da,48,0,7
SPIWrite 06db,46,0,7
SPIWrite 06dc,88,0,7
SPIWrite 06dd,47,0,7
SPIWrite 06de,38,0,7
SPIWrite 06df,4d,0,7
SPIWrite 06e0,a7,0,7
SPIWrite 06e1,70,0,7
SPIWrite 06e2,03,0,7
SPIWrite 06e3,99,0,7
SPIWrite 06e4,20,0,7
SPIWrite 06e5,68,0,7
SPIWrite 06e6,61,0,7
SPIWrite 06e7,f3,0,7
SPIWrite 06e8,18,0,7
SPIWrite 06e9,60,0,7
SPIWrite 06ea,20,0,7
SPIWrite 06eb,60,0,7
SPIWrite 06ec,01,0,7
SPIWrite 06ed,9e,0,7
SPIWrite 06ee,2e,0,7
SPIWrite 06ef,70,0,7
SPIWrite 06f0,04,0,7
SPIWrite 06f1,98,0,7
SPIWrite 06f2,00,0,7
SPIWrite 06f3,09,0,7
SPIWrite 06f4,5a,0,7
SPIWrite 06f5,d3,0,7
SPIWrite 06f6,33,0,7
SPIWrite 06f7,4c,0,7
SPIWrite 06f8,33,0,7
SPIWrite 06f9,48,0,7
SPIWrite 06fa,26,0,7
SPIWrite 06fb,78,0,7
SPIWrite 06fc,4f,0,7
SPIWrite 06fd,f0,0,7
SPIWrite 06fe,01,0,7
SPIWrite 06ff,09,0,7
SPIWrite 0700,00,0,7
SPIWrite 0701,27,0,7
SPIWrite 0702,65,0,7
SPIWrite 0703,78,0,7
SPIWrite 0704,00,0,7
SPIWrite 0705,78,0,7
SPIWrite 0706,30,0,7
SPIWrite 0707,49,0,7
SPIWrite 0708,01,0,7
SPIWrite 0709,90,0,7
SPIWrite 070a,01,0,7
SPIWrite 070b,28,0,7
SPIWrite 070c,81,0,7
SPIWrite 070d,f8,0,7
SPIWrite 070e,00,0,7

SPIWrite 070f,90,0,7
SPIWrite 0710,04,0,7
SPIWrite 0711,d0,0,7
SPIWrite 0712,d8,0,7
SPIWrite 0713,f8,0,7
SPIWrite 0714,1c,0,7
SPIWrite 0715,23,0,7
SPIWrite 0716,2e,0,7
SPIWrite 0717,20,0,7
SPIWrite 0718,49,0,7
SPIWrite 0719,46,0,7
SPIWrite 071a,90,0,7
SPIWrite 071b,47,0,7
SPIWrite 071c,21,0,7
SPIWrite 071d,68,0,7
SPIWrite 071e,20,0,7
SPIWrite 071f,68,0,7
SPIWrite 0720,c1,0,7
SPIWrite 0721,f3,0,7
SPIWrite 0722,00,0,7
SPIWrite 0723,63,0,7
SPIWrite 0724,03,0,7
SPIWrite 0725,93,0,7
SPIWrite 0726,40,0,7
SPIWrite 0727,f0,0,7
SPIWrite 0728,80,0,7
SPIWrite 0729,70,0,7
SPIWrite 072a,20,0,7
SPIWrite 072b,60,0,7
SPIWrite 072c,a7,0,7
SPIWrite 072d,70,0,7
SPIWrite 072e,06,0,7
SPIWrite 072f,9a,0,7
SPIWrite 0730,84,0,7
SPIWrite 0731,f8,0,7
SPIWrite 0732,9d,0,7
SPIWrite 0733,9a,0,7
SPIWrite 0734,95,0,7
SPIWrite 0735,42,0,7
SPIWrite 0736,84,0,7
SPIWrite 0737,f8,0,7
SPIWrite 0738,a3,0,7
SPIWrite 0739,9a,0,7
SPIWrite 073a,b4,0,7
SPIWrite 073b,bf,0,7
SPIWrite 073c,cb,0,7
SPIWrite 073d,46,0,7
SPIWrite 073e,4f,0,7
SPIWrite 073f,f0,0,7
SPIWrite 0740,ff,0,7
SPIWrite 0741,3b,0,7
SPIWrite 0742,05,0,7
SPIWrite 0743,98,0,7
SPIWrite 0744,ba,0,7
SPIWrite 0745,46,0,7
SPIWrite 0746,b0,0,7
SPIWrite 0747,42,0,7
SPIWrite 0748,06,0,7
SPIWrite 0749,dc,0,7
SPIWrite 074a,30,0,7
SPIWrite 074b,1a,0,7
SPIWrite 074c,4f,0,7
SPIWrite 074d,f0,0,7
SPIWrite 074e,ff,0,7
SPIWrite 074f,31,0,7
SPIWrite 0750,02,0,7
SPIWrite 0751,28,0,7
SPIWrite 0752,00,0,7
SPIWrite 0753,91,0,7

SPIWrite 0754,05,0,7
SPIWrite 0755,da,0,7
SPIWrite 0756,05,0,7
SPIWrite 0757,e0,0,7
SPIWrite 0758,80,0,7
SPIWrite 0759,1b,0,7
SPIWrite 075a,cd,0,7
SPIWrite 075b,f8,0,7
SPIWrite 075c,00,0,7
SPIWrite 075d,90,0,7
SPIWrite 075e,02,0,7
SPIWrite 075f,28,0,7
SPIWrite 0760,00,0,7
SPIWrite 0761,db,0,7
SPIWrite 0762,ca,0,7
SPIWrite 0763,46,0,7
SPIWrite 0764,bb,0,7
SPIWrite 0765,f1,0,7
SPIWrite 0766,01,0,7
SPIWrite 0767,0f,0,7
SPIWrite 0768,13,0,7
SPIWrite 0769,bf,0,7
SPIWrite 076a,ba,0,7
SPIWrite 076b,f1,0,7
SPIWrite 076c,01,0,7
SPIWrite 076d,0f,0,7
SPIWrite 076e,10,0,7
SPIWrite 076f,1c,0,7
SPIWrite 0770,02,0,7
SPIWrite 0771,90,0,7
SPIWrite 0772,02,0,7
SPIWrite 0773,95,0,7
SPIWrite 0774,05,0,7
SPIWrite 0775,98,0,7
SPIWrite 0776,b0,0,7
SPIWrite 0777,42,0,7
SPIWrite 0778,7f,0,7
SPIWrite 0779,f4,0,7
SPIWrite 077a,e8,0,7
SPIWrite 077b,ad,0,7
SPIWrite 077c,06,0,7
SPIWrite 077d,98,0,7
SPIWrite 077e,85,0,7
SPIWrite 077f,42,0,7
SPIWrite 0780,7f,0,7
SPIWrite 0781,f4,0,7
SPIWrite 0782,c1,0,7
SPIWrite 0783,ad,0,7
SPIWrite 0784,84,0,7
SPIWrite 0785,f8,0,7
SPIWrite 0786,9d,0,7
SPIWrite 0787,7a,0,7
SPIWrite 0788,84,0,7
SPIWrite 0789,f8,0,7
SPIWrite 078a,a3,0,7
SPIWrite 078b,7a,0,7
SPIWrite 078c,26,0,7
SPIWrite 078d,70,0,7
SPIWrite 078e,84,0,7
SPIWrite 078f,f8,0,7
SPIWrite 0790,02,0,7
SPIWrite 0791,90,0,7
SPIWrite 0792,d8,0,7
SPIWrite 0793,f8,0,7
SPIWrite 0794,08,0,7
SPIWrite 0795,14,0,7
SPIWrite 0796,48,0,7
SPIWrite 0797,46,0,7
SPIWrite 0798,88,0,7

SPIWrite 0799,47,0,7
SPIWrite 079a,a7,0,7
SPIWrite 079b,70,0,7
SPIWrite 079c,03,0,7
SPIWrite 079d,98,0,7
SPIWrite 079e,0a,0,7
SPIWrite 079f,4d,0,7
SPIWrite 07a0,21,0,7
SPIWrite 07a1,68,0,7
SPIWrite 07a2,60,0,7
SPIWrite 07a3,f3,0,7
SPIWrite 07a4,18,0,7
SPIWrite 07a5,61,0,7
SPIWrite 07a6,21,0,7
SPIWrite 07a7,60,0,7
SPIWrite 07a8,01,0,7
SPIWrite 07a9,9e,0,7
SPIWrite 07aa,2e,0,7
SPIWrite 07ab,70,0,7
SPIWrite 07ac,09,0,7
SPIWrite 07ad,b0,0,7
SPIWrite 07ae,bd,0,7
SPIWrite 07af,e8,0,7
SPIWrite 07b0,f0,0,7
SPIWrite 07b1,8f,0,7
SPIWrite 07b2,c0,0,7
SPIWrite 07b3,46,0,7
SPIWrite 07b4,38,0,7
SPIWrite 07b5,f4,0,7
SPIWrite 07b6,ff,0,7
SPIWrite 07b7,51,0,7
SPIWrite 07b8,58,0,7
SPIWrite 07b9,f6,0,7
SPIWrite 07ba,ff,0,7
SPIWrite 07bb,51,0,7
SPIWrite 07bc,49,0,7
SPIWrite 07bd,01,0,7
SPIWrite 07be,00,0,7
SPIWrite 07bf,78,0,7
SPIWrite 07c0,39,0,7
SPIWrite 07c1,01,0,7
SPIWrite 07c2,00,0,7
SPIWrite 07c3,f8,0,7
SPIWrite 07c4,20,0,7
SPIWrite 07c5,01,0,7
SPIWrite 07c6,00,0,7
SPIWrite 07c7,af,0,7
SPIWrite 07c8,49,0,7
SPIWrite 07c9,01,0,7
SPIWrite 07ca,00,0,7
SPIWrite 07cb,f8,0,7
SPIWrite 07cc,70,0,7
SPIWrite 07cd,4a,0,7
SPIWrite 07ce,6e,0,7
SPIWrite 07cf,48,0,7
SPIWrite 07d0,80,0,7
SPIWrite 07d1,b5,0,7
SPIWrite 07d2,a2,0,7
SPIWrite 07d3,f5,0,7
SPIWrite 07d4,bc,0,7
SPIWrite 07d5,71,0,7
SPIWrite 07d6,07,0,7
SPIWrite 07d7,78,0,7
SPIWrite 07d8,08,0,7
SPIWrite 07d9,68,0,7
SPIWrite 07da,20,0,7
SPIWrite 07db,f0,0,7
SPIWrite 07dc,1c,0,7
SPIWrite 07dd,00,0,7

SPIWrite 07de,00,0,7
SPIWrite 07df,1d,0,7
SPIWrite 07e0,08,0,7
SPIWrite 07e1,60,0,7
SPIWrite 07e2,00,0,7
SPIWrite 07e3,23,0,7
SPIWrite 07e4,4b,0,7
SPIWrite 07e5,70,0,7
SPIWrite 07e6,c8,0,7
SPIWrite 07e7,68,0,7
SPIWrite 07e8,20,0,7
SPIWrite 07e9,f0,0,7
SPIWrite 07ea,1c,0,7
SPIWrite 07eb,00,0,7
SPIWrite 07ec,00,0,7
SPIWrite 07ed,1d,0,7
SPIWrite 07ee,c8,0,7
SPIWrite 07ef,60,0,7
SPIWrite 07f0,4b,0,7
SPIWrite 07f1,73,0,7
SPIWrite 07f2,08,0,7
SPIWrite 07f3,69,0,7
SPIWrite 07f4,20,0,7
SPIWrite 07f5,f0,0,7
SPIWrite 07f6,1c,0,7
SPIWrite 07f7,00,0,7
SPIWrite 07f8,00,0,7
SPIWrite 07f9,1d,0,7
SPIWrite 07fa,08,0,7
SPIWrite 07fb,61,0,7
SPIWrite 07fc,4b,0,7
SPIWrite 07fd,74,0,7
SPIWrite 07fe,48,0,7
SPIWrite 07ff,6c,0,7
SPIWrite 0800,20,0,7
SPIWrite 0801,f0,0,7
SPIWrite 0802,1c,0,7
SPIWrite 0803,00,0,7
SPIWrite 0804,00,0,7
SPIWrite 0805,1d,0,7
SPIWrite 0806,48,0,7
SPIWrite 0807,64,0,7
SPIWrite 0808,81,0,7
SPIWrite 0809,f8,0,7
SPIWrite 080a,45,0,7
SPIWrite 080b,30,0,7
SPIWrite 080c,48,0,7
SPIWrite 080d,6a,0,7
SPIWrite 080e,20,0,7
SPIWrite 080f,f0,0,7
SPIWrite 0810,1c,0,7
SPIWrite 0811,00,0,7
SPIWrite 0812,48,0,7
SPIWrite 0813,62,0,7
SPIWrite 0814,53,0,7
SPIWrite 0815,20,0,7
SPIWrite 0816,81,0,7
SPIWrite 0817,f8,0,7
SPIWrite 0818,25,0,7
SPIWrite 0819,00,0,7
SPIWrite 081a,5e,0,7
SPIWrite 081b,49,0,7
SPIWrite 081c,10,0,7
SPIWrite 081d,68,0,7
SPIWrite 081e,20,0,7
SPIWrite 081f,f4,0,7
SPIWrite 0820,00,0,7
SPIWrite 0821,30,0,7
SPIWrite 0822,10,0,7

SPIWrite 0823,60,0,7
SPIWrite 0824,10,0,7
SPIWrite 0825,68,0,7
SPIWrite 0826,20,0,7
SPIWrite 0827,f4,0,7
SPIWrite 0828,00,0,7
SPIWrite 0829,20,0,7
SPIWrite 082a,10,0,7
SPIWrite 082b,60,0,7
SPIWrite 082c,10,0,7
SPIWrite 082d,68,0,7
SPIWrite 082e,20,0,7
SPIWrite 082f,f4,0,7
SPIWrite 0830,00,0,7
SPIWrite 0831,10,0,7
SPIWrite 0832,10,0,7
SPIWrite 0833,60,0,7
SPIWrite 0834,10,0,7
SPIWrite 0835,68,0,7
SPIWrite 0836,20,0,7
SPIWrite 0837,f4,0,7
SPIWrite 0838,00,0,7
SPIWrite 0839,00,0,7
SPIWrite 083a,10,0,7
SPIWrite 083b,60,0,7
SPIWrite 083c,52,0,7
SPIWrite 083d,f8,0,7
SPIWrite 083e,0c,0,7
SPIWrite 083f,0c,0,7
SPIWrite 0840,00,0,7
SPIWrite 0841,2f,0,7
SPIWrite 0842,4f,0,7
SPIWrite 0843,f0,0,7
SPIWrite 0844,80,0,7
SPIWrite 0845,03,0,7
SPIWrite 0846,20,0,7
SPIWrite 0847,f0,0,7
SPIWrite 0848,02,0,7
SPIWrite 0849,00,0,7
SPIWrite 084a,42,0,7
SPIWrite 084b,f8,0,7
SPIWrite 084c,0c,0,7
SPIWrite 084d,0c,0,7
SPIWrite 084e,4f,0,7
SPIWrite 084f,48,0,7
SPIWrite 0850,81,0,7
SPIWrite 0851,f8,0,7
SPIWrite 0852,16,0,7
SPIWrite 0853,33,0,7
SPIWrite 0854,0a,0,7
SPIWrite 0855,68,0,7
SPIWrite 0856,07,0,7
SPIWrite 0857,70,0,7
SPIWrite 0858,0c,0,7
SPIWrite 0859,bf,0,7
SPIWrite 085a,22,0,7
SPIWrite 085b,f4,0,7
SPIWrite 085c,80,0,7
SPIWrite 085d,02,0,7
SPIWrite 085e,42,0,7
SPIWrite 085f,f4,0,7
SPIWrite 0860,80,0,7
SPIWrite 0861,02,0,7
SPIWrite 0862,0a,0,7
SPIWrite 0863,60,0,7
SPIWrite 0864,80,0,7
SPIWrite 0865,bd,0,7
SPIWrite 0866,4f,0,7
SPIWrite 0867,f0,0,7

SPIWrite 0868,22,0,7
SPIWrite 0869,40,0,7
SPIWrite 086a,90,0,7
SPIWrite 086b,f8,0,7
SPIWrite 086c,48,0,7
SPIWrite 086d,10,0,7
SPIWrite 086e,90,0,7
SPIWrite 086f,f8,0,7
SPIWrite 0870,49,0,7
SPIWrite 0871,20,0,7
SPIWrite 0872,1f,0,7
SPIWrite 0873,29,0,7
SPIWrite 0874,a8,0,7
SPIWrite 0875,bf,0,7
SPIWrite 0876,1f,0,7
SPIWrite 0877,21,0,7
SPIWrite 0878,44,0,7
SPIWrite 0879,4b,0,7
SPIWrite 087a,50,0,7
SPIWrite 087b,08,0,7
SPIWrite 087c,02,0,7
SPIWrite 087d,d3,0,7
SPIWrite 087e,18,0,7
SPIWrite 087f,46,0,7
SPIWrite 0880,40,0,7
SPIWrite 0881,1c,0,7
SPIWrite 0882,01,0,7
SPIWrite 0883,70,0,7
SPIWrite 0884,90,0,7
SPIWrite 0885,08,0,7
SPIWrite 0886,02,0,7
SPIWrite 0887,d3,0,7
SPIWrite 0888,18,0,7
SPIWrite 0889,46,0,7
SPIWrite 088a,80,0,7
SPIWrite 088b,1c,0,7
SPIWrite 088c,01,0,7
SPIWrite 088d,70,0,7
SPIWrite 088e,d0,0,7
SPIWrite 088f,08,0,7
SPIWrite 0890,02,0,7
SPIWrite 0891,d3,0,7
SPIWrite 0892,18,0,7
SPIWrite 0893,46,0,7
SPIWrite 0894,c0,0,7
SPIWrite 0895,1c,0,7
SPIWrite 0896,01,0,7
SPIWrite 0897,70,0,7
SPIWrite 0898,10,0,7
SPIWrite 0899,09,0,7
SPIWrite 089a,02,0,7
SPIWrite 089b,d3,0,7
SPIWrite 089c,18,0,7
SPIWrite 089d,46,0,7
SPIWrite 089e,00,0,7
SPIWrite 089f,1d,0,7
SPIWrite 08a0,01,0,7
SPIWrite 08a1,70,0,7
SPIWrite 08a2,70,0,7
SPIWrite 08a3,47,0,7
SPIWrite 08a4,98,0,7
SPIWrite 08a5,b5,0,7
SPIWrite 08a6,38,0,7
SPIWrite 08a7,4a,0,7
SPIWrite 08a8,38,0,7
SPIWrite 08a9,48,0,7
SPIWrite 08aa,17,0,7
SPIWrite 08ab,78,0,7
SPIWrite 08ac,1a,0,7

SPIWrite 08ad,38,0,7
SPIWrite 08ae,53,0,7
SPIWrite 08af,78,0,7
SPIWrite 08b0,07,0,7
SPIWrite 08b1,76,0,7
SPIWrite 08b2,91,0,7
SPIWrite 08b3,78,0,7
SPIWrite 08b4,03,0,7
SPIWrite 08b5,75,0,7
SPIWrite 08b6,41,0,7
SPIWrite 08b7,76,0,7
SPIWrite 08b8,ef,0,7
SPIWrite 08b9,b9,0,7
SPIWrite 08ba,11,0,7
SPIWrite 08bb,6b,0,7
SPIWrite 08bc,14,0,7
SPIWrite 08bd,6b,0,7
SPIWrite 08be,03,0,7
SPIWrite 08bf,80,0,7
SPIWrite 08c0,00,0,7
SPIWrite 08c1,23,0,7
SPIWrite 08c2,c1,0,7
SPIWrite 08c3,f3,0,7
SPIWrite 08c4,0b,0,7
SPIWrite 08c5,41,0,7
SPIWrite 08c6,83,0,7
SPIWrite 08c7,70,0,7
SPIWrite 08c8,32,0,7
SPIWrite 08c9,4f,0,7
SPIWrite 08ca,c3,0,7
SPIWrite 08cb,70,0,7
SPIWrite 08cc,41,0,7
SPIWrite 08cd,f4,0,7
SPIWrite 08ce,90,0,7
SPIWrite 08cf,61,0,7
SPIWrite 08d0,03,0,7
SPIWrite 08d1,71,0,7
SPIWrite 08d2,61,0,7
SPIWrite 08d3,f3,0,7
SPIWrite 08d4,1b,0,7
SPIWrite 08d5,44,0,7
SPIWrite 08d6,14,0,7
SPIWrite 08d7,63,0,7
SPIWrite 08d8,d1,0,7
SPIWrite 08d9,8d,0,7
SPIWrite 08da,43,0,7
SPIWrite 08db,71,0,7
SPIWrite 08dc,41,0,7
SPIWrite 08dd,f4,0,7
SPIWrite 08de,90,0,7
SPIWrite 08df,61,0,7
SPIWrite 08e0,d1,0,7
SPIWrite 08e1,85,0,7
SPIWrite 08e2,80,0,7
SPIWrite 08e3,24,0,7
SPIWrite 08e4,87,0,7
SPIWrite 08e5,f8,0,7
SPIWrite 08e6,0a,0,7
SPIWrite 08e7,43,0,7
SPIWrite 08e8,87,0,7
SPIWrite 08e9,f8,0,7
SPIWrite 08ea,0d,0,7
SPIWrite 08eb,43,0,7
SPIWrite 08ec,39,0,7
SPIWrite 08ed,68,0,7
SPIWrite 08ee,03,0,7
SPIWrite 08ef,76,0,7
SPIWrite 08f0,41,0,7
SPIWrite 08f1,f4,0,7

SPIWrite 08f2,10,0,7
SPIWrite 08f3,51,0,7
SPIWrite 08f4,39,0,7
SPIWrite 08f5,60,0,7
SPIWrite 08f6,98,0,7
SPIWrite 08f7,bd,0,7
SPIWrite 08f8,98,0,7
SPIWrite 08f9,b5,0,7
SPIWrite 08fa,4f,0,7
SPIWrite 08fb,f0,0,7
SPIWrite 08fc,22,0,7
SPIWrite 08fd,41,0,7
SPIWrite 08fe,26,0,7
SPIWrite 08ff,48,0,7
SPIWrite 0900,b1,0,7
SPIWrite 0901,f9,0,7
SPIWrite 0902,48,0,7
SPIWrite 0903,20,0,7
SPIWrite 0904,07,0,7
SPIWrite 0905,7e,0,7
SPIWrite 0906,91,0,7
SPIWrite 0907,f8,0,7
SPIWrite 0908,4a,0,7
SPIWrite 0909,30,0,7
SPIWrite 090a,12,0,7
SPIWrite 090b,f1,0,7
SPIWrite 090c,18,0,7
SPIWrite 090d,0f,0,7
SPIWrite 090e,08,0,7
SPIWrite 090f,db,0,7
SPIWrite 0910,00,0,7
SPIWrite 0911,2a,0,7
SPIWrite 0912,08,0,7
SPIWrite 0913,dd,0,7
SPIWrite 0914,44,0,7
SPIWrite 0915,7e,0,7
SPIWrite 0916,d1,0,7
SPIWrite 0917,10,0,7
SPIWrite 0918,c9,0,7
SPIWrite 0919,b2,0,7
SPIWrite 091a,8c,0,7
SPIWrite 091b,42,0,7
SPIWrite 091c,b8,0,7
SPIWrite 091d,bf,0,7
SPIWrite 091e,21,0,7
SPIWrite 091f,1c,0,7
SPIWrite 0920,02,0,7
SPIWrite 0921,e0,0,7
SPIWrite 0922,6f,0,7
SPIWrite 0923,f0,0,7
SPIWrite 0924,17,0,7
SPIWrite 0925,02,0,7
SPIWrite 0926,00,0,7
SPIWrite 0927,21,0,7
SPIWrite 0928,a2,0,7
SPIWrite 0929,eb,0,7
SPIWrite 092a,c1,0,7
SPIWrite 092b,02,0,7
SPIWrite 092c,18,0,7
SPIWrite 092d,32,0,7
SPIWrite 092e,d2,0,7
SPIWrite 092f,b2,0,7
SPIWrite 0930,c0,0,7
SPIWrite 0931,2a,0,7
SPIWrite 0932,a8,0,7
SPIWrite 0933,bf,0,7
SPIWrite 0934,bf,0,7
SPIWrite 0935,22,0,7
SPIWrite 0936,5c,0,7

SPIWrite 0937,08,0,7
SPIWrite 0938,06,0,7
SPIWrite 0939,d3,0,7
SPIWrite 093a,81,0,7
SPIWrite 093b,71,0,7
SPIWrite 093c,87,0,7
SPIWrite 093d,f0,0,7
SPIWrite 093e,01,0,7
SPIWrite 093f,04,0,7
SPIWrite 0940,c2,0,7
SPIWrite 0941,71,0,7
SPIWrite 0942,04,0,7
SPIWrite 0943,f0,0,7
SPIWrite 0944,01,0,7
SPIWrite 0945,04,0,7
SPIWrite 0946,84,0,7
SPIWrite 0947,70,0,7
SPIWrite 0948,9c,0,7
SPIWrite 0949,08,0,7
SPIWrite 094a,06,0,7
SPIWrite 094b,d3,0,7
SPIWrite 094c,01,0,7
SPIWrite 094d,72,0,7
SPIWrite 094e,87,0,7
SPIWrite 094f,f0,0,7
SPIWrite 0950,01,0,7
SPIWrite 0951,04,0,7
SPIWrite 0952,42,0,7
SPIWrite 0953,72,0,7
SPIWrite 0954,04,0,7
SPIWrite 0955,f0,0,7
SPIWrite 0956,01,0,7
SPIWrite 0957,04,0,7
SPIWrite 0958,c4,0,7
SPIWrite 0959,70,0,7
SPIWrite 095a,dc,0,7
SPIWrite 095b,08,0,7
SPIWrite 095c,06,0,7
SPIWrite 095d,d3,0,7
SPIWrite 095e,81,0,7
SPIWrite 095f,72,0,7
SPIWrite 0960,87,0,7
SPIWrite 0961,f0,0,7
SPIWrite 0962,01,0,7
SPIWrite 0963,04,0,7
SPIWrite 0964,c2,0,7
SPIWrite 0965,72,0,7
SPIWrite 0966,04,0,7
SPIWrite 0967,f0,0,7
SPIWrite 0968,01,0,7
SPIWrite 0969,04,0,7
SPIWrite 096a,04,0,7
SPIWrite 096b,71,0,7
SPIWrite 096c,1c,0,7
SPIWrite 096d,09,0,7
SPIWrite 096e,06,0,7
SPIWrite 096f,d3,0,7
SPIWrite 0970,01,0,7
SPIWrite 0971,73,0,7
SPIWrite 0972,87,0,7
SPIWrite 0973,f0,0,7
SPIWrite 0974,01,0,7
SPIWrite 0975,04,0,7
SPIWrite 0976,42,0,7
SPIWrite 0977,73,0,7
SPIWrite 0978,04,0,7
SPIWrite 0979,f0,0,7
SPIWrite 097a,01,0,7
SPIWrite 097b,04,0,7

SPIWrite 097c,44,0,7
SPIWrite 097d,71,0,7
SPIWrite 097e,17,0,7
SPIWrite 097f,b1,0,7
SPIWrite 0980,18,0,7
SPIWrite 0981,46,0,7
SPIWrite 0982,ff,0,7
SPIWrite 0983,f7,0,7
SPIWrite 0984,69,0,7
SPIWrite 0985,fc,0,7
SPIWrite 0986,98,0,7
SPIWrite 0987,bd,0,7
SPIWrite 0988,48,0,7
SPIWrite 0989,00,0,7
SPIWrite 098a,00,0,7
SPIWrite 098b,a2,0,7
SPIWrite 098c,1a,0,7
SPIWrite 098d,00,0,7
SPIWrite 098e,01,0,7
SPIWrite 098f,20,0,7
SPIWrite 0990,7c,0,7
SPIWrite 0991,05,0,7
SPIWrite 0992,02,0,7
SPIWrite 0993,a8,0,7
SPIWrite 0994,00,0,7
SPIWrite 0995,e1,0,7
SPIWrite 0996,00,0,7
SPIWrite 0997,e0,0,7
SPIWrite 0998,00,0,7
SPIWrite 0999,00,0,7
SPIWrite 099a,01,0,7
SPIWrite 099b,20,0,7
SPIWrite 099c,f8,0,7
SPIWrite 099d,b5,0,7
SPIWrite 099e,3b,0,7
SPIWrite 099f,4d,0,7
SPIWrite 09a0,df,0,7
SPIWrite 09a1,f8,0,7
SPIWrite 09a2,e4,0,7
SPIWrite 09a3,e0,0,7
SPIWrite 09a4,4f,0,7
SPIWrite 09a5,f0,0,7
SPIWrite 09a6,00,0,7
SPIWrite 09a7,0c,0,7
SPIWrite 09a8,60,0,7
SPIWrite 09a9,46,0,7
SPIWrite 09aa,2b,0,7
SPIWrite 09ab,78,0,7
SPIWrite 09ac,8e,0,7
SPIWrite 09ad,f8,0,7
SPIWrite 09ae,00,0,7
SPIWrite 09af,c0,0,7
SPIWrite 09b0,5e,0,7
SPIWrite 09b1,1e,0,7
SPIWrite 09b2,16,0,7
SPIWrite 09b3,e0,0,7
SPIWrite 09b4,02,0,7
SPIWrite 09b5,22,0,7
SPIWrite 09b6,c0,0,7
SPIWrite 09b7,f2,0,7
SPIWrite 09b8,00,0,7
SPIWrite 09b9,02,0,7
SPIWrite 09ba,52,0,7
SPIWrite 09bb,1e,0,7
SPIWrite 09bc,fd,0,7
SPIWrite 09bd,d1,0,7
SPIWrite 09be,00,0,7
SPIWrite 09bf,bf,0,7
SPIWrite 09c0,00,0,7

SPIWrite 09c1,bf,0,7
SPIWrite 09c2,40,0,7
SPIWrite 09c3,1c,0,7
SPIWrite 09c4,c0,0,7
SPIWrite 09c5,b2,0,7
SPIWrite 09c6,11,0,7
SPIWrite 09c7,e0,0,7
SPIWrite 09c8,81,0,7
SPIWrite 09c9,00,0,7
SPIWrite 09ca,01,0,7
SPIWrite 09cb,eb,0,7
SPIWrite 09cc,c0,0,7
SPIWrite 09cd,01,0,7
SPIWrite 09ce,6a,0,7
SPIWrite 09cf,18,0,7
SPIWrite 09d0,54,0,7
SPIWrite 09d1,68,0,7
SPIWrite 09d2,91,0,7
SPIWrite 09d3,68,0,7
SPIWrite 09d4,27,0,7
SPIWrite 09d5,68,0,7
SPIWrite 09d6,d2,0,7
SPIWrite 09d7,68,0,7
SPIWrite 09d8,40,0,7
SPIWrite 09d9,1c,0,7
SPIWrite 09da,c0,0,7
SPIWrite 09db,b2,0,7
SPIWrite 09dc,39,0,7
SPIWrite 09dd,40,0,7
SPIWrite 09de,0a,0,7
SPIWrite 09df,43,0,7
SPIWrite 09e0,22,0,7
SPIWrite 09e1,60,0,7
SPIWrite 09e2,86,0,7
SPIWrite 09e3,42,0,7
SPIWrite 09e4,f0,0,7
SPIWrite 09e5,dc,0,7
SPIWrite 09e6,2a,0,7
SPIWrite 09e7,48,0,7
SPIWrite 09e8,01,0,7
SPIWrite 09e9,78,0,7
SPIWrite 09ea,60,0,7
SPIWrite 09eb,46,0,7
SPIWrite 09ec,81,0,7
SPIWrite 09ed,42,0,7
SPIWrite 09ee,e1,0,7
SPIWrite 09ef,dc,0,7
SPIWrite 09f0,98,0,7
SPIWrite 09f1,00,0,7
SPIWrite 09f2,00,0,7
SPIWrite 09f3,eb,0,7
SPIWrite 09f4,c3,0,7
SPIWrite 09f5,00,0,7
SPIWrite 09f6,41,0,7
SPIWrite 09f7,19,0,7
SPIWrite 09f8,51,0,7
SPIWrite 09f9,f8,0,7
SPIWrite 09fa,08,0,7
SPIWrite 09fb,2c,0,7
SPIWrite 09fc,28,0,7
SPIWrite 09fd,58,0,7
SPIWrite 09fe,51,0,7
SPIWrite 09ff,f8,0,7
SPIWrite 0a00,04,0,7
SPIWrite 0a01,1c,0,7
SPIWrite 0a02,13,0,7
SPIWrite 0a03,68,0,7
SPIWrite 0a04,19,0,7
SPIWrite 0a05,40,0,7

SPIWrite 0a06,08,0,7
SPIWrite 0a07,43,0,7
SPIWrite 0a08,10,0,7
SPIWrite 0a09,60,0,7
SPIWrite 0a0a,8e,0,7
SPIWrite 0a0b,f8,0,7
SPIWrite 0a0c,00,0,7
SPIWrite 0a0d,c0,0,7
SPIWrite 0a0e,f8,0,7
SPIWrite 0a0f,bd,0,7
SPIWrite 0a10,f8,0,7
SPIWrite 0a11,b5,0,7
SPIWrite 0a12,1e,0,7
SPIWrite 0a13,4b,0,7
SPIWrite 0a14,1c,0,7
SPIWrite 0a15,49,0,7
SPIWrite 0a16,4f,0,7
SPIWrite 0a17,f0,0,7
SPIWrite 0a18,00,0,7
SPIWrite 0a19,0c,0,7
SPIWrite 0a1a,60,0,7
SPIWrite 0a1b,46,0,7
SPIWrite 0a1c,5c,0,7
SPIWrite 0a1d,78,0,7
SPIWrite 0a1e,81,0,7
SPIWrite 0a1f,f8,0,7
SPIWrite 0a20,00,0,7
SPIWrite 0a21,c0,0,7
SPIWrite 0a22,66,0,7
SPIWrite 0a23,1e,0,7
SPIWrite 0a24,19,0,7
SPIWrite 0a25,e0,0,7
SPIWrite 0a26,02,0,7
SPIWrite 0a27,22,0,7
SPIWrite 0a28,c0,0,7
SPIWrite 0a29,f2,0,7
SPIWrite 0a2a,00,0,7
SPIWrite 0a2b,02,0,7
SPIWrite 0a2c,52,0,7
SPIWrite 0a2d,1e,0,7
SPIWrite 0a2e,fd,0,7
SPIWrite 0a2f,d1,0,7
SPIWrite 0a30,00,0,7
SPIWrite 0a31,bf,0,7
SPIWrite 0a32,00,0,7
SPIWrite 0a33,bf,0,7
SPIWrite 0a34,40,0,7
SPIWrite 0a35,1c,0,7
SPIWrite 0a36,c0,0,7
SPIWrite 0a37,b2,0,7
SPIWrite 0a38,14,0,7
SPIWrite 0a39,e0,0,7
SPIWrite 0a3a,81,0,7
SPIWrite 0a3b,00,0,7
SPIWrite 0a3c,01,0,7
SPIWrite 0a3d,eb,0,7
SPIWrite 0a3e,c0,0,7
SPIWrite 0a3f,01,0,7
SPIWrite 0a40,5a,0,7
SPIWrite 0a41,18,0,7
SPIWrite 0a42,d2,0,7
SPIWrite 0a43,f8,0,7
SPIWrite 0a44,94,0,7
SPIWrite 0a45,50,0,7
SPIWrite 0a46,d2,0,7
SPIWrite 0a47,f8,0,7
SPIWrite 0a48,98,0,7
SPIWrite 0a49,10,0,7
SPIWrite 0a4a,2f,0,7

SPIWrite 0a4b,68,0,7
SPIWrite 0a4c,d2,0,7
SPIWrite 0a4d,f8,0,7
SPIWrite 0a4e,9c,0,7
SPIWrite 0a4f,20,0,7
SPIWrite 0a50,40,0,7
SPIWrite 0a51,1c,0,7
SPIWrite 0a52,c0,0,7
SPIWrite 0a53,b2,0,7
SPIWrite 0a54,39,0,7
SPIWrite 0a55,40,0,7
SPIWrite 0a56,0a,0,7
SPIWrite 0a57,43,0,7
SPIWrite 0a58,2a,0,7
SPIWrite 0a59,60,0,7
SPIWrite 0a5a,86,0,7
SPIWrite 0a5b,42,0,7
SPIWrite 0a5c,ed,0,7
SPIWrite 0a5d,dc,0,7
SPIWrite 0a5e,0c,0,7
SPIWrite 0a5f,48,0,7
SPIWrite 0a60,01,0,7
SPIWrite 0a61,78,0,7
SPIWrite 0a62,60,0,7
SPIWrite 0a63,46,0,7
SPIWrite 0a64,81,0,7
SPIWrite 0a65,42,0,7
SPIWrite 0a66,de,0,7
SPIWrite 0a67,dc,0,7
SPIWrite 0a68,a0,0,7
SPIWrite 0a69,00,0,7
SPIWrite 0a6a,00,0,7
SPIWrite 0a6b,eb,0,7
SPIWrite 0a6c,c4,0,7
SPIWrite 0a6d,00,0,7
SPIWrite 0a6e,1b,0,7
SPIWrite 0a6f,18,0,7
SPIWrite 0a70,d3,0,7
SPIWrite 0a71,f8,0,7
SPIWrite 0a72,88,0,7
SPIWrite 0a73,20,0,7
SPIWrite 0a74,d3,0,7
SPIWrite 0a75,f8,0,7
SPIWrite 0a76,90,0,7
SPIWrite 0a77,10,0,7
SPIWrite 0a78,d3,0,7
SPIWrite 0a79,f8,0,7
SPIWrite 0a7a,8c,0,7
SPIWrite 0a7b,00,0,7
SPIWrite 0a7c,14,0,7
SPIWrite 0a7d,68,0,7
SPIWrite 0a7e,20,0,7
SPIWrite 0a7f,40,0,7
SPIWrite 0a80,01,0,7
SPIWrite 0a81,43,0,7
SPIWrite 0a82,11,0,7
SPIWrite 0a83,60,0,7
SPIWrite 0a84,f8,0,7
SPIWrite 0a85,bd,0,7
SPIWrite 0a86,70,0,7
SPIWrite 0a87,47,0,7
SPIWrite 0a88,97,0,7
SPIWrite 0a89,01,0,7
SPIWrite 0a8a,00,0,7
SPIWrite 0a8b,a2,0,7
SPIWrite 0a8c,74,0,7
SPIWrite 0a8d,4f,0,7
SPIWrite 0a8e,00,0,7
SPIWrite 0a8f,20,0,7

SPIWrite 0a90,0f,0,7
SPIWrite 0a91,00,0,7
SPIWrite 0a92,01,0,7
SPIWrite 0a93,20,0,7
SPIWrite 0a94,29,0,7
SPIWrite 0a95,49,0,7
SPIWrite 0a96,10,0,7
SPIWrite 0a97,b5,0,7
SPIWrite 0a98,4f,0,7
SPIWrite 0a99,f4,0,7
SPIWrite 0a9a,24,0,7
SPIWrite 0a9b,30,0,7
SPIWrite 0a9c,c1,0,7
SPIWrite 0a9d,f8,0,7
SPIWrite 0a9e,08,0,7
SPIWrite 0a9f,09,0,7
SPIWrite 0aa0,0c,0,7
SPIWrite 0aa1,20,0,7
SPIWrite 0aa2,c0,0,7
SPIWrite 0aa3,f2,0,7
SPIWrite 0aa4,00,0,7
SPIWrite 0aa5,00,0,7
SPIWrite 0aa6,40,0,7
SPIWrite 0aa7,1e,0,7
SPIWrite 0aa8,fd,0,7
SPIWrite 0aa9,d1,0,7
SPIWrite 0aaa,00,0,7
SPIWrite 0aab,bf,0,7
SPIWrite 0aac,00,0,7
SPIWrite 0aad,bf,0,7
SPIWrite 0aae,25,0,7
SPIWrite 0aaf,4b,0,7
SPIWrite 0ab0,25,0,7
SPIWrite 0ab1,4a,0,7
SPIWrite 0ab2,26,0,7
SPIWrite 0ab3,48,0,7
SPIWrite 0ab4,c3,0,7
SPIWrite 0ab5,f8,0,7
SPIWrite 0ab6,bc,0,7
SPIWrite 0ab7,21,0,7
SPIWrite 0ab8,25,0,7
SPIWrite 0ab9,4a,0,7
SPIWrite 0aba,c3,0,7
SPIWrite 0abb,f8,0,7
SPIWrite 0abc,c0,0,7
SPIWrite 0abd,01,0,7
SPIWrite 0abe,25,0,7
SPIWrite 0abf,48,0,7
SPIWrite 0ac0,c3,0,7
SPIWrite 0ac1,f8,0,7
SPIWrite 0ac2,c4,0,7
SPIWrite 0ac3,21,0,7
SPIWrite 0ac4,24,0,7
SPIWrite 0ac5,4a,0,7
SPIWrite 0ac6,c3,0,7
SPIWrite 0ac7,f8,0,7
SPIWrite 0ac8,68,0,7
SPIWrite 0ac9,05,0,7
SPIWrite 0aca,24,0,7
SPIWrite 0acb,48,0,7
SPIWrite 0acc,c3,0,7
SPIWrite 0acd,f8,0,7
SPIWrite 0ace,84,0,7
SPIWrite 0acf,25,0,7
SPIWrite 0ad0,23,0,7
SPIWrite 0ad1,4a,0,7
SPIWrite 0ad2,c3,0,7
SPIWrite 0ad3,f8,0,7
SPIWrite 0ad4,90,0,7

SPIWrite 0ad5,05,0,7
SPIWrite 0ad6,23,0,7
SPIWrite 0ad7,48,0,7
SPIWrite 0ad8,c3,0,7
SPIWrite 0ad9,f8,0,7
SPIWrite 0ada,14,0,7
SPIWrite 0adb,21,0,7
SPIWrite 0adc,22,0,7
SPIWrite 0add,4a,0,7
SPIWrite 0ade,c3,0,7
SPIWrite 0adf,f8,0,7
SPIWrite 0ae0,18,0,7
SPIWrite 0ae1,01,0,7
SPIWrite 0ae2,22,0,7
SPIWrite 0ae3,48,0,7
SPIWrite 0ae4,c3,0,7
SPIWrite 0ae5,f8,0,7
SPIWrite 0ae6,28,0,7
SPIWrite 0ae7,24,0,7
SPIWrite 0ae8,15,0,7
SPIWrite 0ae9,4a,0,7
SPIWrite 0aea,c3,0,7
SPIWrite 0aeb,f8,0,7
SPIWrite 0aec,2c,0,7
SPIWrite 0aed,04,0,7
SPIWrite 0aee,00,0,7
SPIWrite 0aef,20,0,7
SPIWrite 0af0,d0,0,7
SPIWrite 0af1,73,0,7
SPIWrite 0af2,d0,0,7
SPIWrite 0af3,76,0,7
SPIWrite 0af4,10,0,7
SPIWrite 0af5,77,0,7
SPIWrite 0af6,50,0,7
SPIWrite 0af7,77,0,7
SPIWrite 0af8,90,0,7
SPIWrite 0af9,77,0,7
SPIWrite 0afa,10,0,7
SPIWrite 0afb,75,0,7
SPIWrite 0afc,90,0,7
SPIWrite 0afd,76,0,7
SPIWrite 0afe,50,0,7
SPIWrite 0aff,75,0,7
SPIWrite 0b00,20,0,7
SPIWrite 0b01,4c,0,7
SPIWrite 0b02,c3,0,7
SPIWrite 0b03,f8,0,7
SPIWrite 0b04,00,0,7
SPIWrite 0b05,42,0,7
SPIWrite 0b06,27,0,7
SPIWrite 0b07,24,0,7
SPIWrite 0b08,54,0,7
SPIWrite 0b09,76,0,7
SPIWrite 0b0a,80,0,7
SPIWrite 0b0b,23,0,7
SPIWrite 0b0c,0b,0,7
SPIWrite 0b0d,70,0,7
SPIWrite 0b0e,4b,0,7
SPIWrite 0b0f,70,0,7
SPIWrite 0b10,8b,0,7
SPIWrite 0b11,70,0,7
SPIWrite 0b12,c8,0,7
SPIWrite 0b13,70,0,7
SPIWrite 0b14,08,0,7
SPIWrite 0b15,71,0,7
SPIWrite 0b16,48,0,7
SPIWrite 0b17,71,0,7
SPIWrite 0b18,15,0,7
SPIWrite 0b19,4c,0,7

SPIWrite 0b1a,88,0,7
SPIWrite 0b1b,71,0,7
SPIWrite 0b1c,15,0,7
SPIWrite 0b1d,48,0,7
SPIWrite 0b1e,c4,0,7
SPIWrite 0b1f,f8,0,7
SPIWrite 0b20,b4,0,7
SPIWrite 0b21,00,0,7
SPIWrite 0b22,15,0,7
SPIWrite 0b23,48,0,7
SPIWrite 0b24,c4,0,7
SPIWrite 0b25,f8,0,7
SPIWrite 0b26,b8,0,7
SPIWrite 0b27,00,0,7
SPIWrite 0b28,0b,0,7
SPIWrite 0b29,72,0,7
SPIWrite 0b2a,14,0,7
SPIWrite 0b2b,48,0,7
SPIWrite 0b2c,c4,0,7
SPIWrite 0b2d,f8,0,7
SPIWrite 0b2e,bc,0,7
SPIWrite 0b2f,00,0,7
SPIWrite 0b30,13,0,7
SPIWrite 0b31,48,0,7
SPIWrite 0b32,c4,0,7
SPIWrite 0b33,f8,0,7
SPIWrite 0b34,c0,0,7
SPIWrite 0b35,00,0,7
SPIWrite 0b36,0b,0,7
SPIWrite 0b37,74,0,7
SPIWrite 0b38,cb,0,7
SPIWrite 0b39,74,0,7
SPIWrite 0b3a,10,0,7
SPIWrite 0b3b,bd,0,7
SPIWrite 0b3c,00,0,7
SPIWrite 0b3d,e4,0,7
SPIWrite 0b3e,00,0,7
SPIWrite 0b3f,e0,0,7
SPIWrite 0b40,00,0,7
SPIWrite 0b41,00,0,7
SPIWrite 0b42,01,0,7
SPIWrite 0b43,20,0,7
SPIWrite 0b44,40,0,7
SPIWrite 0b45,52,0,7
SPIWrite 0b46,00,0,7
SPIWrite 0b47,20,0,7
SPIWrite 0b48,67,0,7
SPIWrite 0b49,8a,0,7
SPIWrite 0b4a,02,0,7
SPIWrite 0b4b,00,0,7
SPIWrite 0b4c,7d,0,7
SPIWrite 0b4d,89,0,7
SPIWrite 0b4e,02,0,7
SPIWrite 0b4f,00,0,7
SPIWrite 0b50,f1,0,7
SPIWrite 0b51,89,0,7
SPIWrite 0b52,02,0,7
SPIWrite 0b53,00,0,7
SPIWrite 0b54,19,0,7
SPIWrite 0b55,8c,0,7
SPIWrite 0b56,02,0,7
SPIWrite 0b57,00,0,7
SPIWrite 0b58,1d,0,7
SPIWrite 0b59,8d,0,7
SPIWrite 0b5a,02,0,7
SPIWrite 0b5b,00,0,7
SPIWrite 0b5c,e1,0,7
SPIWrite 0b5d,8c,0,7
SPIWrite 0b5e,02,0,7

SPIWrite 0b5f,00,0,7
SPIWrite 0b60,51,0,7
SPIWrite 0b61,8c,0,7
SPIWrite 0b62,02,0,7
SPIWrite 0b63,00,0,7
SPIWrite 0b64,8d,0,7
SPIWrite 0b65,8c,0,7
SPIWrite 0b66,02,0,7
SPIWrite 0b67,00,0,7
SPIWrite 0b68,c9,0,7
SPIWrite 0b69,8d,0,7
SPIWrite 0b6a,02,0,7
SPIWrite 0b6b,00,0,7
SPIWrite 0b6c,f5,0,7
SPIWrite 0b6d,8d,0,7
SPIWrite 0b6e,02,0,7
SPIWrite 0b6f,00,0,7
SPIWrite 0b70,58,0,7
SPIWrite 0b71,58,0,7
SPIWrite 0b72,00,0,7
SPIWrite 0b73,20,0,7
SPIWrite 0b74,85,0,7
SPIWrite 0b75,88,0,7
SPIWrite 0b76,02,0,7
SPIWrite 0b77,00,0,7
SPIWrite 0b78,d9,0,7
SPIWrite 0b79,88,0,7
SPIWrite 0b7a,02,0,7
SPIWrite 0b7b,00,0,7
SPIWrite 0b7c,ad,0,7
SPIWrite 0b7d,87,0,7
SPIWrite 0b7e,02,0,7
SPIWrite 0b7f,00,0,7
SPIWrite 0b80,47,0,7
SPIWrite 0b81,88,0,7
SPIWrite 0b82,02,0,7
SPIWrite 0b83,00,0,7
SPIWrite 0b84,29,0,7
SPIWrite 0b85,8e,0,7
SPIWrite 0b86,02,0,7
SPIWrite 0b87,00,0,7
SPIWrite 0b88,84,0,7
SPIWrite 0b89,46,0,7
SPIWrite 0b8a,35,0,7
SPIWrite 0b8b,48,0,7
SPIWrite 0b8c,b0,0,7
SPIWrite 0b8d,b5,0,7
SPIWrite 0b8e,00,0,7
SPIWrite 0b8f,f1,0,7
SPIWrite 0b90,0e,0,7
SPIWrite 0b91,01,0,7
SPIWrite 0b92,0f,0,7
SPIWrite 0b93,78,0,7
SPIWrite 0b94,00,0,7
SPIWrite 0b95,2f,0,7
SPIWrite 0b96,4b,0,7
SPIWrite 0b97,d1,0,7
SPIWrite 0b98,2f,0,7
SPIWrite 0b99,4a,0,7
SPIWrite 0b9a,30,0,7
SPIWrite 0b9b,49,0,7
SPIWrite 0b9c,d2,0,7
SPIWrite 0b9d,f8,0,7
SPIWrite 0b9e,e8,0,7
SPIWrite 0b9f,3c,0,7
SPIWrite 0ba0,dc,0,7
SPIWrite 0ba1,0a,0,7
SPIWrite 0ba2,05,0,7
SPIWrite 0ba3,d3,0,7

SPIWrite 0ba4,92,0,7
SPIWrite 0ba5,f8,0,7
SPIWrite 0ba6,de,0,7
SPIWrite 0ba7,5c,0,7
SPIWrite 0ba8,0c,0,7
SPIWrite 0ba9,68,0,7
SPIWrite 0baa,65,0,7
SPIWrite 0bab,f3,0,7
SPIWrite 0bac,c3,0,7
SPIWrite 0bad,04,0,7
SPIWrite 0bae,0c,0,7
SPIWrite 0baf,60,0,7
SPIWrite 0bb0,1c,0,7
SPIWrite 0bb1,0b,0,7
SPIWrite 0bb2,05,0,7
SPIWrite 0bb3,d3,0,7
SPIWrite 0bb4,92,0,7
SPIWrite 0bb5,f8,0,7
SPIWrite 0bb6,e2,0,7
SPIWrite 0bb7,5c,0,7
SPIWrite 0bb8,0c,0,7
SPIWrite 0bb9,68,0,7
SPIWrite 0bba,65,0,7
SPIWrite 0bbb,f3,0,7
SPIWrite 0bbc,04,0,7
SPIWrite 0bbd,14,0,7
SPIWrite 0bbe,0c,0,7
SPIWrite 0bbf,60,0,7
SPIWrite 0bc0,5c,0,7
SPIWrite 0bc1,0b,0,7
SPIWrite 0bc2,05,0,7
SPIWrite 0bc3,d3,0,7
SPIWrite 0bc4,92,0,7
SPIWrite 0bc5,f8,0,7
SPIWrite 0bc6,df,0,7
SPIWrite 0bc7,5c,0,7
SPIWrite 0bc8,0c,0,7
SPIWrite 0bc9,68,0,7
SPIWrite 0bca,65,0,7
SPIWrite 0bcb,f3,0,7
SPIWrite 0bcc,45,0,7
SPIWrite 0bcd,14,0,7
SPIWrite 0bce,0c,0,7
SPIWrite 0bcf,60,0,7
SPIWrite 0bd0,9c,0,7
SPIWrite 0bd1,0b,0,7
SPIWrite 0bd2,05,0,7
SPIWrite 0bd3,d3,0,7
SPIWrite 0bd4,92,0,7
SPIWrite 0bd5,f8,0,7
SPIWrite 0bd6,e0,0,7
SPIWrite 0bd7,5c,0,7
SPIWrite 0bd8,8c,0,7
SPIWrite 0bd9,68,0,7
SPIWrite 0bda,65,0,7
SPIWrite 0bdb,f3,0,7
SPIWrite 0bdc,41,0,7
SPIWrite 0bdd,04,0,7
SPIWrite 0bde,8c,0,7
SPIWrite 0bdf,60,0,7
SPIWrite 0be0,db,0,7
SPIWrite 0be1,0b,0,7
SPIWrite 0be2,05,0,7
SPIWrite 0be3,d3,0,7
SPIWrite 0be4,92,0,7
SPIWrite 0be5,f8,0,7
SPIWrite 0be6,e1,0,7
SPIWrite 0be7,4c,0,7
SPIWrite 0be8,cb,0,7

SPIWrite 0be9,68,0,7
SPIWrite 0bea,64,0,7
SPIWrite 0beb,f3,0,7
SPIWrite 0bec,41,0,7
SPIWrite 0bed,03,0,7
SPIWrite 0bee,cb,0,7
SPIWrite 0bef,60,0,7
SPIWrite 0bf0,01,0,7
SPIWrite 0bf1,25,0,7
SPIWrite 0bf2,92,0,7
SPIWrite 0bf3,f8,0,7
SPIWrite 0bf4,e2,0,7
SPIWrite 0bf5,3c,0,7
SPIWrite 0bf6,92,0,7
SPIWrite 0bf7,f8,0,7
SPIWrite 0bf8,de,0,7
SPIWrite 0bf9,4c,0,7
SPIWrite 0bfa,92,0,7
SPIWrite 0bfb,f8,0,7
SPIWrite 0bfc,df,0,7
SPIWrite 0bfd,1c,0,7
SPIWrite 0bfe,92,0,7
SPIWrite 0bff,f8,0,7
SPIWrite 0c00,e0,0,7
SPIWrite 0c01,7c,0,7
SPIWrite 0c02,85,0,7
SPIWrite 0c03,73,0,7
SPIWrite 0c04,83,0,7
SPIWrite 0c05,f0,0,7
SPIWrite 0c06,ff,0,7
SPIWrite 0c07,03,0,7
SPIWrite 0c08,84,0,7
SPIWrite 0c09,f0,0,7
SPIWrite 0c0a,ff,0,7
SPIWrite 0c0b,04,0,7
SPIWrite 0c0c,82,0,7
SPIWrite 0c0d,f8,0,7
SPIWrite 0c0e,e2,0,7
SPIWrite 0c0f,3c,0,7
SPIWrite 0c10,81,0,7
SPIWrite 0c11,f0,0,7
SPIWrite 0c12,ff,0,7
SPIWrite 0c13,01,0,7
SPIWrite 0c14,82,0,7
SPIWrite 0c15,f8,0,7
SPIWrite 0c16,de,0,7
SPIWrite 0c17,4c,0,7
SPIWrite 0c18,92,0,7
SPIWrite 0c19,f8,0,7
SPIWrite 0c1a,e1,0,7
SPIWrite 0c1b,5c,0,7
SPIWrite 0c1c,82,0,7
SPIWrite 0c1d,f8,0,7
SPIWrite 0c1e,df,0,7
SPIWrite 0c1f,1c,0,7
SPIWrite 0c20,87,0,7
SPIWrite 0c21,f0,0,7
SPIWrite 0c22,ff,0,7
SPIWrite 0c23,00,0,7
SPIWrite 0c24,82,0,7
SPIWrite 0c25,f8,0,7
SPIWrite 0c26,e0,0,7
SPIWrite 0c27,0c,0,7
SPIWrite 0c28,85,0,7
SPIWrite 0c29,f0,0,7
SPIWrite 0c2a,ff,0,7
SPIWrite 0c2b,03,0,7
SPIWrite 0c2c,82,0,7
SPIWrite 0c2d,f8,0,7

SPIWrite 0c2e,e1,0,7
SPIWrite 0c2f,3c,0,7
SPIWrite 0c30,60,0,7
SPIWrite 0c31,46,0,7
SPIWrite 0c32,f5,0,7
SPIWrite 0c33,f7,0,7
SPIWrite 0c34,8c,0,7
SPIWrite 0c35,fe,0,7
SPIWrite 0c36,b0,0,7
SPIWrite 0c37,bd,0,7
SPIWrite 0c38,10,0,7
SPIWrite 0c39,b5,0,7
SPIWrite 0c3a,0b,0,7
SPIWrite 0c3b,4a,0,7
SPIWrite 0c3c,09,0,7
SPIWrite 0c3d,4c,0,7
SPIWrite 0c3e,08,0,7
SPIWrite 0c3f,48,0,7
SPIWrite 0c40,00,0,7
SPIWrite 0c41,21,0,7
SPIWrite 0c42,c4,0,7
SPIWrite 0c43,f8,0,7
SPIWrite 0c44,08,0,7
SPIWrite 0c45,24,0,7
SPIWrite 0c46,0e,0,7
SPIWrite 0c47,30,0,7
SPIWrite 0c48,01,0,7
SPIWrite 0c49,70,0,7
SPIWrite 0c4a,e7,0,7
SPIWrite 0c4b,f7,0,7
SPIWrite 0c4c,ed,0,7
SPIWrite 0c4d,fa,0,7
SPIWrite 0c4e,07,0,7
SPIWrite 0c4f,49,0,7
SPIWrite 0c50,c4,0,7
SPIWrite 0c51,f8,0,7
SPIWrite 0c52,08,0,7
SPIWrite 0c53,14,0,7
SPIWrite 0c54,10,0,7
SPIWrite 0c55,bd,0,7
SPIWrite 0c56,c0,0,7
SPIWrite 0c57,46,0,7
SPIWrite 0c58,d8,0,7
SPIWrite 0c59,33,0,7
SPIWrite 0c5a,00,0,7
SPIWrite 0c5b,20,0,7
SPIWrite 0c5c,48,0,7
SPIWrite 0c5d,00,0,7
SPIWrite 0c5e,03,0,7
SPIWrite 0c5f,a8,0,7
SPIWrite 0c60,00,0,7
SPIWrite 0c61,00,0,7
SPIWrite 0c62,01,0,7
SPIWrite 0c63,20,0,7
SPIWrite 0c64,40,0,7
SPIWrite 0c65,52,0,7
SPIWrite 0c66,00,0,7
SPIWrite 0c67,20,0,7
SPIWrite 0c68,69,0,7
SPIWrite 0c69,8b,0,7
SPIWrite 0c6a,02,0,7
SPIWrite 0c6b,00,0,7
SPIWrite 0c6c,2f,0,7
SPIWrite 0c6d,e9,0,7
SPIWrite 0c6e,01,0,7
SPIWrite 0c6f,00,0,7
SPIWrite 0c70,21,0,7
SPIWrite 0c71,48,0,7
SPIWrite 0c72,10,0,7

SPIWrite 0c73,b5,0,7
SPIWrite 0c74,df,0,7
SPIWrite 0c75,f7,0,7
SPIWrite 0c76,d2,0,7
SPIWrite 0c77,fc,0,7
SPIWrite 0c78,4f,0,7
SPIWrite 0c79,f0,0,7
SPIWrite 0c7a,2e,0,7
SPIWrite 0c7b,40,0,7
SPIWrite 0c7c,d0,0,7
SPIWrite 0c7d,f8,0,7
SPIWrite 0c7e,f8,0,7
SPIWrite 0c7f,22,0,7
SPIWrite 0c80,1e,0,7
SPIWrite 0c81,4b,0,7
SPIWrite 0c82,d0,0,7
SPIWrite 0c83,f8,0,7
SPIWrite 0c84,58,0,7
SPIWrite 0c85,16,0,7
SPIWrite 0c86,4f,0,7
SPIWrite 0c87,f0,0,7
SPIWrite 0c88,2f,0,7
SPIWrite 0c89,44,0,7
SPIWrite 0c8a,6f,0,7
SPIWrite 0c8b,f3,0,7
SPIWrite 0c8c,9f,0,7
SPIWrite 0c8d,42,0,7
SPIWrite 0c8e,6f,0,7
SPIWrite 0c8f,f3,0,7
SPIWrite 0c90,9f,0,7
SPIWrite 0c91,41,0,7
SPIWrite 0c92,1a,0,7
SPIWrite 0c93,62,0,7
SPIWrite 0c94,d4,0,7
SPIWrite 0c95,f8,0,7
SPIWrite 0c96,f8,0,7
SPIWrite 0c97,02,0,7
SPIWrite 0c98,59,0,7
SPIWrite 0c99,62,0,7
SPIWrite 0c9a,6f,0,7
SPIWrite 0c9b,f3,0,7
SPIWrite 0c9c,9f,0,7
SPIWrite 0c9d,40,0,7
SPIWrite 0c9e,d4,0,7
SPIWrite 0c9f,f8,0,7
SPIWrite 0ca0,58,0,7
SPIWrite 0ca1,16,0,7
SPIWrite 0ca2,98,0,7
SPIWrite 0ca3,62,0,7
SPIWrite 0ca4,6f,0,7
SPIWrite 0ca5,f3,0,7
SPIWrite 0ca6,9f,0,7
SPIWrite 0ca7,41,0,7
SPIWrite 0ca8,d9,0,7
SPIWrite 0ca9,62,0,7
SPIWrite 0caa,10,0,7
SPIWrite 0cab,bd,0,7
SPIWrite 0cac,12,0,7
SPIWrite 0cad,48,0,7
SPIWrite 0cae,10,0,7
SPIWrite 0caf,b5,0,7
SPIWrite 0cb0,e0,0,7
SPIWrite 0cb1,f7,0,7
SPIWrite 0cb2,6a,0,7
SPIWrite 0cb3,f8,0,7
SPIWrite 0cb4,4f,0,7
SPIWrite 0cb5,f0,0,7
SPIWrite 0cb6,2e,0,7
SPIWrite 0cb7,42,0,7

SPIWrite 0cb8,10,0,7
SPIWrite 0cb9,48,0,7
SPIWrite 0cba,d2,0,7
SPIWrite 0cbb,f8,0,7
SPIWrite 0cbc,f8,0,7
SPIWrite 0cbd,32,0,7
SPIWrite 0cbe,01,0,7
SPIWrite 0cbf,6a,0,7
SPIWrite 0cc0,61,0,7
SPIWrite 0cc1,f3,0,7
SPIWrite 0cc2,11,0,7
SPIWrite 0cc3,03,0,7
SPIWrite 0cc4,c2,0,7
SPIWrite 0cc5,f8,0,7
SPIWrite 0cc6,f8,0,7
SPIWrite 0cc7,32,0,7
SPIWrite 0cc8,41,0,7
SPIWrite 0cc9,6a,0,7
SPIWrite 0cca,d2,0,7
SPIWrite 0ccb,f8,0,7
SPIWrite 0ccc,58,0,7
SPIWrite 0ccd,46,0,7
SPIWrite 0cce,4f,0,7
SPIWrite 0ccf,f0,0,7
SPIWrite 0cd0,2f,0,7
SPIWrite 0cd1,43,0,7
SPIWrite 0cd2,61,0,7
SPIWrite 0cd3,f3,0,7
SPIWrite 0cd4,11,0,7
SPIWrite 0cd5,04,0,7
SPIWrite 0cd6,81,0,7
SPIWrite 0cd7,6a,0,7
SPIWrite 0cd8,c2,0,7
SPIWrite 0cd9,f8,0,7
SPIWrite 0cda,58,0,7
SPIWrite 0cdb,46,0,7
SPIWrite 0cdc,c0,0,7
SPIWrite 0cdd,6a,0,7
SPIWrite 0cde,d3,0,7
SPIWrite 0cdf,f8,0,7
SPIWrite 0ce0,f8,0,7
SPIWrite 0ce1,22,0,7
SPIWrite 0ce2,61,0,7
SPIWrite 0ce3,f3,0,7
SPIWrite 0ce4,11,0,7
SPIWrite 0ce5,02,0,7
SPIWrite 0ce6,c3,0,7
SPIWrite 0ce7,f8,0,7
SPIWrite 0ce8,f8,0,7
SPIWrite 0ce9,22,0,7
SPIWrite 0cea,d3,0,7
SPIWrite 0ceb,f8,0,7
SPIWrite 0cec,58,0,7
SPIWrite 0ced,16,0,7
SPIWrite 0cee,60,0,7
SPIWrite 0cef,f3,0,7
SPIWrite 0cf0,11,0,7
SPIWrite 0cf1,01,0,7
SPIWrite 0cf2,c3,0,7
SPIWrite 0cf3,f8,0,7
SPIWrite 0cf4,58,0,7
SPIWrite 0cf5,16,0,7
SPIWrite 0cf6,10,0,7
SPIWrite 0cf7,bd,0,7
SPIWrite 0cf8,b2,0,7
SPIWrite 0cf9,43,0,7
SPIWrite 0cfa,00,0,7
SPIWrite 0cfb,20,0,7
SPIWrite 0cfc,00,0,7

SPIWrite 0cfd,00,0,7
SPIWrite 0cfe,01,0,7
SPIWrite 0cff,20,0,7
SPIWrite 0d00,38,0,7
SPIWrite 0d01,b5,0,7
SPIWrite 0d02,1d,0,7
SPIWrite 0d03,4c,0,7
SPIWrite 0d04,04,0,7
SPIWrite 0d05,f1,0,7
SPIWrite 0d06,70,0,7
SPIWrite 0d07,00,0,7
SPIWrite 0d08,00,0,7
SPIWrite 0d09,78,0,7
SPIWrite 0d0a,03,0,7
SPIWrite 0d0b,28,0,7
SPIWrite 0d0c,02,0,7
SPIWrite 0d0d,d0,0,7
SPIWrite 0d0e,f0,0,7
SPIWrite 0d0f,f7,0,7
SPIWrite 0d10,a5,0,7
SPIWrite 0d11,ff,0,7
SPIWrite 0d12,38,0,7
SPIWrite 0d13,bd,0,7
SPIWrite 0d14,01,0,7
SPIWrite 0d15,25,0,7
SPIWrite 0d16,84,0,7
SPIWrite 0d17,f8,0,7
SPIWrite 0d18,70,0,7
SPIWrite 0d19,50,0,7
SPIWrite 0d1a,f0,0,7
SPIWrite 0d1b,f7,0,7
SPIWrite 0d1c,9f,0,7
SPIWrite 0d1d,ff,0,7
SPIWrite 0d1e,02,0,7
SPIWrite 0d1f,20,0,7
SPIWrite 0d20,84,0,7
SPIWrite 0d21,f8,0,7
SPIWrite 0d22,70,0,7
SPIWrite 0d23,00,0,7
SPIWrite 0d24,f0,0,7
SPIWrite 0d25,f7,0,7
SPIWrite 0d26,9a,0,7
SPIWrite 0d27,ff,0,7
SPIWrite 0d28,03,0,7
SPIWrite 0d29,20,0,7
SPIWrite 0d2a,84,0,7
SPIWrite 0d2b,f8,0,7
SPIWrite 0d2c,70,0,7
SPIWrite 0d2d,00,0,7
SPIWrite 0d2e,4f,0,7
SPIWrite 0d2f,f0,0,7
SPIWrite 0d30,b0,0,7
SPIWrite 0d31,41,0,7
SPIWrite 0d32,81,0,7
SPIWrite 0d33,f8,0,7
SPIWrite 0d34,3e,0,7
SPIWrite 0d35,51,0,7
SPIWrite 0d36,81,0,7
SPIWrite 0d37,f8,0,7
SPIWrite 0d38,3c,0,7
SPIWrite 0d39,51,0,7
SPIWrite 0d3a,38,0,7
SPIWrite 0d3b,bd,0,7
SPIWrite 0d3c,70,0,7
SPIWrite 0d3d,b5,0,7
SPIWrite 0d3e,0e,0,7
SPIWrite 0d3f,4c,0,7
SPIWrite 0d40,04,0,7
SPIWrite 0d41,f1,0,7

SPIWrite 0d42,70,0,7
SPIWrite 0d43,00,0,7
SPIWrite 0d44,00,0,7
SPIWrite 0d45,78,0,7
SPIWrite 0d46,03,0,7
SPIWrite 0d47,28,0,7
SPIWrite 0d48,02,0,7
SPIWrite 0d49,d0,0,7
SPIWrite 0d4a,f1,0,7
SPIWrite 0d4b,f7,0,7
SPIWrite 0d4c,3b,0,7
SPIWrite 0d4d,f8,0,7
SPIWrite 0d4e,70,0,7
SPIWrite 0d4f,bd,0,7
SPIWrite 0d50,01,0,7
SPIWrite 0d51,20,0,7
SPIWrite 0d52,84,0,7
SPIWrite 0d53,f8,0,7
SPIWrite 0d54,70,0,7
SPIWrite 0d55,00,0,7
SPIWrite 0d56,f1,0,7
SPIWrite 0d57,f7,0,7
SPIWrite 0d58,35,0,7
SPIWrite 0d59,f8,0,7
SPIWrite 0d5a,4f,0,7
SPIWrite 0d5b,f0,0,7
SPIWrite 0d5c,b0,0,7
SPIWrite 0d5d,46,0,7
SPIWrite 0d5e,02,0,7
SPIWrite 0d5f,20,0,7
SPIWrite 0d60,96,0,7
SPIWrite 0d61,f8,0,7
SPIWrite 0d62,45,0,7
SPIWrite 0d63,51,0,7
SPIWrite 0d64,84,0,7
SPIWrite 0d65,f8,0,7
SPIWrite 0d66,70,0,7
SPIWrite 0d67,00,0,7
SPIWrite 0d68,f1,0,7
SPIWrite 0d69,f7,0,7
SPIWrite 0d6a,2c,0,7
SPIWrite 0d6b,f8,0,7
SPIWrite 0d6c,03,0,7
SPIWrite 0d6d,20,0,7
SPIWrite 0d6e,84,0,7
SPIWrite 0d6f,f8,0,7
SPIWrite 0d70,70,0,7
SPIWrite 0d71,00,0,7
SPIWrite 0d72,86,0,7
SPIWrite 0d73,f8,0,7
SPIWrite 0d74,45,0,7
SPIWrite 0d75,51,0,7
SPIWrite 0d76,70,0,7
SPIWrite 0d77,bd,0,7
SPIWrite 0d78,d8,0,7
SPIWrite 0d79,33,0,7
SPIWrite 0d7a,00,0,7
SPIWrite 0d7b,20,0,7
SPIWrite 0d7c,80,0,7
SPIWrite 0d7d,b5,0,7
SPIWrite 0d7e,17,0,7
SPIWrite 0d7f,49,0,7
SPIWrite 0d80,15,0,7
SPIWrite 0d81,48,0,7
SPIWrite 0d82,8a,0,7
SPIWrite 0d83,7e,0,7
SPIWrite 0d84,00,0,7
SPIWrite 0d85,68,0,7
SPIWrite 0d86,01,0,7

SPIWrite 0d87,2a,0,7
SPIWrite 0d88,00,0,7
SPIWrite 0d89,f0,0,7
SPIWrite 0d8a,3f,0,7
SPIWrite 0d8b,00,0,7
SPIWrite 0d8c,c0,0,7
SPIWrite 0d8d,f3,0,7
SPIWrite 0d8e,c2,0,7
SPIWrite 0d8f,07,0,7
SPIWrite 0d90,21,0,7
SPIWrite 0d91,d1,0,7
SPIWrite 0d92,10,0,7
SPIWrite 0d93,f0,0,7
SPIWrite 0d94,07,0,7
SPIWrite 0d95,00,0,7
SPIWrite 0d96,0b,0,7
SPIWrite 0d97,d0,0,7
SPIWrite 0d98,40,0,7
SPIWrite 0d99,1e,0,7
SPIWrite 0d9a,07,0,7
SPIWrite 0d9b,d0,0,7
SPIWrite 0d9c,40,0,7
SPIWrite 0d9d,1e,0,7
SPIWrite 0d9e,03,0,7
SPIWrite 0d9f,d0,0,7
SPIWrite 0da0,40,0,7
SPIWrite 0da1,1e,0,7
SPIWrite 0da2,08,0,7
SPIWrite 0da3,d1,0,7
SPIWrite 0da4,88,0,7
SPIWrite 0da5,7f,0,7
SPIWrite 0da6,04,0,7
SPIWrite 0da7,e0,0,7
SPIWrite 0da8,48,0,7
SPIWrite 0da9,7f,0,7
SPIWrite 0daa,02,0,7
SPIWrite 0dab,e0,0,7
SPIWrite 0dac,08,0,7
SPIWrite 0dad,7f,0,7
SPIWrite 0dae,00,0,7
SPIWrite 0daf,e0,0,7
SPIWrite 0db0,c8,0,7
SPIWrite 0db1,7e,0,7
SPIWrite 0db2,0b,0,7
SPIWrite 0db3,4a,0,7
SPIWrite 0db4,10,0,7
SPIWrite 0db5,70,0,7
SPIWrite 0db6,5f,0,7
SPIWrite 0db7,b1,0,7
SPIWrite 0db8,7f,0,7
SPIWrite 0db9,1e,0,7
SPIWrite 0dba,07,0,7
SPIWrite 0dbb,d0,0,7
SPIWrite 0dbc,7f,0,7
SPIWrite 0dbd,1e,0,7
SPIWrite 0dbe,03,0,7
SPIWrite 0dbf,d0,0,7
SPIWrite 0dc0,7f,0,7
SPIWrite 0dc1,1e,0,7
SPIWrite 0dc2,08,0,7
SPIWrite 0dc3,d1,0,7
SPIWrite 0dc4,88,0,7
SPIWrite 0dc5,7f,0,7
SPIWrite 0dc6,04,0,7
SPIWrite 0dc7,e0,0,7
SPIWrite 0dc8,48,0,7
SPIWrite 0dc9,7f,0,7
SPIWrite 0dca,02,0,7
SPIWrite 0dcb,e0,0,7

SPIWrite 0dcc,08,0,7
SPIWrite 0dcd,7f,0,7
SPIWrite 0dce,00,0,7
SPIWrite 0dcf,e0,0,7
SPIWrite 0dd0,c8,0,7
SPIWrite 0dd1,7e,0,7
SPIWrite 0dd2,04,0,7
SPIWrite 0dd3,49,0,7
SPIWrite 0dd4,08,0,7
SPIWrite 0dd5,70,0,7
SPIWrite 0dd6,80,0,7
SPIWrite 0dd7,bd,0,7
SPIWrite 0dd8,d4,0,7
SPIWrite 0dd9,06,0,7
SPIWrite 0dda,06,0,7
SPIWrite 0ddb,a8,0,7
SPIWrite 0ddc,00,0,7
SPIWrite 0ddd,00,0,7
SPIWrite 0dde,01,0,7
SPIWrite 0ddf,20,0,7
SPIWrite 0de0,50,0,7
SPIWrite 0de1,01,0,7
SPIWrite 0de2,00,0,7
SPIWrite 0de3,ae,0,7
SPIWrite 0de4,50,0,7
SPIWrite 0de5,01,0,7
SPIWrite 0de6,00,0,7
SPIWrite 0de7,af,0,7
SPIWrite 0de8,15,0,7
SPIWrite 0de9,48,0,7
SPIWrite 0dea,08,0,7
SPIWrite 0deb,b5,0,7
SPIWrite 0dec,ea,0,7
SPIWrite 0ded,f7,0,7
SPIWrite 0dee,2a,0,7
SPIWrite 0def,fa,0,7
SPIWrite 0df0,4f,0,7
SPIWrite 0df1,f0,0,7
SPIWrite 0df2,2e,0,7
SPIWrite 0df3,41,0,7
SPIWrite 0df4,13,0,7
SPIWrite 0df5,48,0,7
SPIWrite 0df6,d1,0,7
SPIWrite 0df7,f8,0,7
SPIWrite 0df8,98,0,7
SPIWrite 0df9,29,0,7
SPIWrite 0dfa,d1,0,7
SPIWrite 0dfb,f8,0,7
SPIWrite 0dfc,b8,0,7
SPIWrite 0dfd,1b,0,7
SPIWrite 0dfe,02,0,7
SPIWrite 0dff,63,0,7
SPIWrite 0e00,4f,0,7
SPIWrite 0e01,f0,0,7
SPIWrite 0e02,2f,0,7
SPIWrite 0e03,42,0,7
SPIWrite 0e04,41,0,7
SPIWrite 0e05,63,0,7
SPIWrite 0e06,d2,0,7
SPIWrite 0e07,f8,0,7
SPIWrite 0e08,98,0,7
SPIWrite 0e09,19,0,7
SPIWrite 0e0a,81,0,7
SPIWrite 0e0b,63,0,7
SPIWrite 0e0c,d2,0,7
SPIWrite 0e0d,f8,0,7
SPIWrite 0e0e,b8,0,7
SPIWrite 0e0f,1b,0,7
SPIWrite 0e10,c1,0,7

SPIWrite 0e11,63,0,7
SPIWrite 0e12,08,0,7
SPIWrite 0e13,bd,0,7
SPIWrite 0e14,0a,0,7
SPIWrite 0e15,48,0,7
SPIWrite 0e16,08,0,7
SPIWrite 0e17,b5,0,7
SPIWrite 0e18,ea,0,7
SPIWrite 0e19,f7,0,7
SPIWrite 0e1a,76,0,7
SPIWrite 0e1b,fb,0,7
SPIWrite 0e1c,09,0,7
SPIWrite 0e1d,48,0,7
SPIWrite 0e1e,4f,0,7
SPIWrite 0e1f,f0,0,7
SPIWrite 0e20,2e,0,7
SPIWrite 0e21,43,0,7
SPIWrite 0e22,01,0,7
SPIWrite 0e23,6b,0,7
SPIWrite 0e24,42,0,7
SPIWrite 0e25,6b,0,7
SPIWrite 0e26,c3,0,7
SPIWrite 0e27,f8,0,7
SPIWrite 0e28,98,0,7
SPIWrite 0e29,19,0,7
SPIWrite 0e2a,81,0,7
SPIWrite 0e2b,6b,0,7
SPIWrite 0e2c,c3,0,7
SPIWrite 0e2d,f8,0,7
SPIWrite 0e2e,b8,0,7
SPIWrite 0e2f,2b,0,7
SPIWrite 0e30,4f,0,7
SPIWrite 0e31,f0,0,7
SPIWrite 0e32,2f,0,7
SPIWrite 0e33,42,0,7
SPIWrite 0e34,c2,0,7
SPIWrite 0e35,f8,0,7
SPIWrite 0e36,98,0,7
SPIWrite 0e37,19,0,7
SPIWrite 0e38,c0,0,7
SPIWrite 0e39,6b,0,7
SPIWrite 0e3a,c2,0,7
SPIWrite 0e3b,f8,0,7
SPIWrite 0e3c,b8,0,7
SPIWrite 0e3d,0b,0,7
SPIWrite 0e3e,08,0,7
SPIWrite 0e3f,bd,0,7
SPIWrite 0e40,b2,0,7
SPIWrite 0e41,43,0,7
SPIWrite 0e42,00,0,7
SPIWrite 0e43,20,0,7
SPIWrite 0e44,00,0,7
SPIWrite 0e45,00,0,7
SPIWrite 0e46,01,0,7
SPIWrite 0e47,20,0,7
SPIWrite 0e48,2d,0,7
SPIWrite 0e49,e9,0,7
SPIWrite 0e4a,8e,0,7
SPIWrite 0e4b,41,0,7
SPIWrite 0e4c,80,0,7
SPIWrite 0e4d,46,0,7
SPIWrite 0e4e,1f,0,7
SPIWrite 0e4f,46,0,7
SPIWrite 0e50,68,0,7
SPIWrite 0e51,46,0,7
SPIWrite 0e52,f0,0,7
SPIWrite 0e53,f7,0,7
SPIWrite 0e54,2e,0,7
SPIWrite 0e55,fb,0,7

SPIWrite 0e56,00,0,7
SPIWrite 0e57,2f,0,7
SPIWrite 0e58,14,0,7
SPIWrite 0e59,bf,0,7
SPIWrite 0e5a,04,0,7
SPIWrite 0e5b,20,0,7
SPIWrite 0e5c,08,0,7
SPIWrite 0e5d,20,0,7
SPIWrite 0e5e,bd,0,7
SPIWrite 0e5f,f8,0,7
SPIWrite 0e60,06,0,7
SPIWrite 0e61,10,0,7
SPIWrite 0e62,02,0,7
SPIWrite 0e63,29,0,7
SPIWrite 0e64,0e,0,7
SPIWrite 0e65,d1,0,7
SPIWrite 0e66,bd,0,7
SPIWrite 0e67,f9,0,7
SPIWrite 0e68,04,0,7
SPIWrite 0e69,10,0,7
SPIWrite 0e6a,11,0,7
SPIWrite 0e6b,fb,0,7
SPIWrite 0e6c,00,0,7
SPIWrite 0e6d,f1,0,7
SPIWrite 0e6e,ad,0,7
SPIWrite 0e6f,f8,0,7
SPIWrite 0e70,04,0,7
SPIWrite 0e71,10,0,7
SPIWrite 0e72,9d,0,7
SPIWrite 0e73,f8,0,7
SPIWrite 0e74,08,0,7
SPIWrite 0e75,10,0,7
SPIWrite 0e76,47,0,7
SPIWrite 0e77,00,0,7
SPIWrite 0e78,c9,0,7
SPIWrite 0e79,19,0,7
SPIWrite 0e7a,ad,0,7
SPIWrite 0e7b,f8,0,7
SPIWrite 0e7c,06,0,7
SPIWrite 0e7d,10,0,7
SPIWrite 0e7e,00,0,7
SPIWrite 0e7f,23,0,7
SPIWrite 0e80,8d,0,7
SPIWrite 0e81,f8,0,7
SPIWrite 0e82,08,0,7
SPIWrite 0e83,30,0,7
SPIWrite 0e84,9d,0,7
SPIWrite 0e85,e8,0,7
SPIWrite 0e86,07,0,7
SPIWrite 0e87,00,0,7
SPIWrite 0e88,88,0,7
SPIWrite 0e89,e8,0,7
SPIWrite 0e8a,07,0,7
SPIWrite 0e8b,00,0,7
SPIWrite 0e8c,bd,0,7
SPIWrite 0e8d,e8,0,7
SPIWrite 0e8e,8e,0,7
SPIWrite 0e8f,81,0,7
SPIWrite 0e90,00,0,7
SPIWrite 0e91,00,0,7
SPIWrite 0e92,00,0,7
SPIWrite 0e93,00,0,7
SPIWrite 0e94,00,0,7
SPIWrite 0e95,00,0,7
SPIWrite 0e96,00,0,7
SPIWrite 0e97,00,0,7
SPIWrite 0e98,00,0,7
SPIWrite 0e99,00,0,7
SPIWrite 0e9a,00,0,7

SPIWrite 0e9b,00,0,7
SPIWrite 0e9c,00,0,7
SPIWrite 0e9d,00,0,7
SPIWrite 0e9e,00,0,7
SPIWrite 0e9f,00,0,7
SPIWrite 0ea0,00,0,7
SPIWrite 0ea1,00,0,7
SPIWrite 0ea2,00,0,7
SPIWrite 0ea3,00,0,7
SPIWrite 0ea4,00,0,7
SPIWrite 0ea5,00,0,7
SPIWrite 0ea6,00,0,7
SPIWrite 0ea7,00,0,7
SPIWrite 0ea8,00,0,7
SPIWrite 0ea9,00,0,7
SPIWrite 0eaa,00,0,7
SPIWrite 0eab,00,0,7
SPIWrite 0eac,00,0,7
SPIWrite 0ead,00,0,7
SPIWrite 0eae,00,0,7
SPIWrite 0eaf,00,0,7
SPIWrite 0eb0,00,0,7
SPIWrite 0eb1,00,0,7
SPIWrite 0eb2,00,0,7
SPIWrite 0eb3,00,0,7
SPIWrite 0eb4,00,0,7
SPIWrite 0eb5,00,0,7
SPIWrite 0eb6,00,0,7
SPIWrite 0eb7,00,0,7
SPIWrite 0eb8,00,0,7
SPIWrite 0eb9,00,0,7
SPIWrite 0eba,00,0,7
SPIWrite 0ebb,00,0,7
SPIWrite 0ebc,00,0,7
SPIWrite 0ebd,00,0,7
SPIWrite 0ebe,00,0,7
SPIWrite 0ebf,00,0,7
SPIWrite 0ec0,00,0,7
SPIWrite 0ec1,00,0,7
SPIWrite 0ec2,00,0,7
SPIWrite 0ec3,00,0,7
SPIWrite 0ec4,00,0,7
SPIWrite 0ec5,00,0,7
SPIWrite 0ec6,00,0,7
SPIWrite 0ec7,00,0,7
SPIWrite 0ec8,00,0,7
SPIWrite 0ec9,00,0,7
SPIWrite 0eca,00,0,7
SPIWrite 0ecb,00,0,7
SPIWrite 0ecc,00,0,7
SPIWrite 0ecd,00,0,7
SPIWrite 0ece,00,0,7
SPIWrite 0ecf,00,0,7
SPIWrite 0ed0,00,0,7
SPIWrite 0ed1,00,0,7
SPIWrite 0ed2,00,0,7
SPIWrite 0ed3,00,0,7
SPIWrite 0ed4,00,0,7
SPIWrite 0ed5,00,0,7
SPIWrite 0ed6,00,0,7
SPIWrite 0ed7,00,0,7
SPIWrite 0ed8,00,0,7
SPIWrite 0ed9,00,0,7
SPIWrite 0eda,00,0,7
SPIWrite 0edb,00,0,7
SPIWrite 0edc,00,0,7
SPIWrite 0edd,00,0,7
SPIWrite 0ede,00,0,7
SPIWrite 0edf,00,0,7

SPIWrite 0ee0,00,0,7
SPIWrite 0ee1,00,0,7
SPIWrite 0ee2,00,0,7
SPIWrite 0ee3,00,0,7
SPIWrite 0ee4,00,0,7
SPIWrite 0ee5,00,0,7
SPIWrite 0ee6,00,0,7
SPIWrite 0ee7,00,0,7
SPIWrite 0ee8,00,0,7
SPIWrite 0ee9,00,0,7
SPIWrite 0eea,00,0,7
SPIWrite 0eeb,00,0,7
SPIWrite 0eec,00,0,7
SPIWrite 0eed,00,0,7
SPIWrite 0eee,00,0,7
SPIWrite 0eef,00,0,7
SPIWrite 0ef0,00,0,7
SPIWrite 0ef1,00,0,7
SPIWrite 0ef2,00,0,7
SPIWrite 0ef3,00,0,7
SPIWrite 0ef4,00,0,7
SPIWrite 0ef5,00,0,7
SPIWrite 0ef6,00,0,7
SPIWrite 0ef7,00,0,7
SPIWrite 0ef8,00,0,7
SPIWrite 0ef9,00,0,7
SPIWrite 0efa,00,0,7
SPIWrite 0efb,00,0,7
SPIWrite 0efc,00,0,7
SPIWrite 0efd,00,0,7
SPIWrite 0efe,00,0,7
SPIWrite 0eff,00,0,7
SPIWrite 0f00,00,0,7
SPIWrite 0f01,00,0,7
SPIWrite 0f02,00,0,7
SPIWrite 0f03,00,0,7
SPIWrite 0f04,00,0,7
SPIWrite 0f05,00,0,7
SPIWrite 0f06,00,0,7
SPIWrite 0f07,00,0,7
SPIWrite 0f08,00,0,7
SPIWrite 0f09,00,0,7
SPIWrite 0f0a,00,0,7
SPIWrite 0f0b,00,0,7
SPIWrite 0f0c,00,0,7
SPIWrite 0f0d,00,0,7
SPIWrite 0f0e,00,0,7
SPIWrite 0f0f,00,0,7
SPIWrite 0f10,00,0,7
SPIWrite 0f11,00,0,7
SPIWrite 0f12,00,0,7
SPIWrite 0f13,00,0,7
SPIWrite 0f14,00,0,7
SPIWrite 0f15,00,0,7
SPIWrite 0f16,00,0,7
SPIWrite 0f17,00,0,7
SPIWrite 0f18,00,0,7
SPIWrite 0f19,00,0,7
SPIWrite 0f1a,00,0,7
SPIWrite 0f1b,00,0,7
SPIWrite 0f1c,00,0,7
SPIWrite 0f1d,00,0,7
SPIWrite 0f1e,00,0,7
SPIWrite 0f1f,00,0,7
SPIWrite 0f20,00,0,7
SPIWrite 0f21,00,0,7
SPIWrite 0f22,00,0,7
SPIWrite 0f23,00,0,7
SPIWrite 0f24,00,0,7

SPIWrite 0f25,00,0,7
SPIWrite 0f26,00,0,7
SPIWrite 0f27,00,0,7
SPIWrite 0f28,00,0,7
SPIWrite 0f29,00,0,7
SPIWrite 0f2a,00,0,7
SPIWrite 0f2b,00,0,7
SPIWrite 0f2c,00,0,7
SPIWrite 0f2d,00,0,7
SPIWrite 0f2e,00,0,7
SPIWrite 0f2f,00,0,7
SPIWrite 0f30,00,0,7
SPIWrite 0f31,00,0,7
SPIWrite 0f32,00,0,7
SPIWrite 0f33,00,0,7
SPIWrite 0f34,00,0,7
SPIWrite 0f35,00,0,7
SPIWrite 0f36,00,0,7
SPIWrite 0f37,00,0,7
SPIWrite 0f38,00,0,7
SPIWrite 0f39,00,0,7
SPIWrite 0f3a,00,0,7
SPIWrite 0f3b,00,0,7
SPIWrite 0f3c,00,0,7
SPIWrite 0f3d,00,0,7
SPIWrite 0f3e,00,0,7
SPIWrite 0f3f,00,0,7
SPIWrite 0f40,00,0,7
SPIWrite 0f41,00,0,7
SPIWrite 0f42,00,0,7
SPIWrite 0f43,00,0,7
SPIWrite 0f44,00,0,7
SPIWrite 0f45,00,0,7
SPIWrite 0f46,00,0,7
SPIWrite 0f47,00,0,7
SPIWrite 0f48,00,0,7
SPIWrite 0f49,00,0,7
SPIWrite 0f4a,00,0,7
SPIWrite 0f4b,00,0,7
SPIWrite 0f4c,00,0,7
SPIWrite 0f4d,00,0,7
SPIWrite 0f4e,00,0,7
SPIWrite 0f4f,00,0,7
SPIWrite 0f50,00,0,7
SPIWrite 0f51,00,0,7
SPIWrite 0f52,00,0,7
SPIWrite 0f53,00,0,7
SPIWrite 0f54,00,0,7
SPIWrite 0f55,00,0,7
SPIWrite 0f56,00,0,7
SPIWrite 0f57,00,0,7
SPIWrite 0f58,00,0,7
SPIWrite 0f59,00,0,7
SPIWrite 0f5a,00,0,7
SPIWrite 0f5b,00,0,7
SPIWrite 0f5c,00,0,7
SPIWrite 0f5d,00,0,7
SPIWrite 0f5e,00,0,7
SPIWrite 0f5f,00,0,7
SPIWrite 0f60,00,0,7
SPIWrite 0f61,00,0,7
SPIWrite 0f62,00,0,7
SPIWrite 0f63,00,0,7
SPIWrite 0f64,00,0,7
SPIWrite 0f65,00,0,7
SPIWrite 0f66,00,0,7
SPIWrite 0f67,00,0,7
SPIWrite 0f68,00,0,7
SPIWrite 0f69,00,0,7

SPIWrite 0f6a,00,0,7
SPIWrite 0f6b,00,0,7
SPIWrite 0f6c,00,0,7
SPIWrite 0f6d,00,0,7
SPIWrite 0f6e,00,0,7
SPIWrite 0f6f,00,0,7
SPIWrite 0f70,00,0,7
SPIWrite 0f71,00,0,7
SPIWrite 0f72,00,0,7
SPIWrite 0f73,00,0,7
SPIWrite 0f74,00,0,7
SPIWrite 0f75,00,0,7
SPIWrite 0f76,00,0,7
SPIWrite 0f77,00,0,7
SPIWrite 0f78,00,0,7
SPIWrite 0f79,00,0,7
SPIWrite 0f7a,00,0,7
SPIWrite 0f7b,00,0,7
SPIWrite 0f7c,00,0,7
SPIWrite 0f7d,00,0,7
SPIWrite 0f7e,00,0,7
SPIWrite 0f7f,00,0,7
SPIWrite 0f80,00,0,7
SPIWrite 0f81,00,0,7
SPIWrite 0f82,00,0,7
SPIWrite 0f83,00,0,7
SPIWrite 0f84,00,0,7
SPIWrite 0f85,00,0,7
SPIWrite 0f86,00,0,7
SPIWrite 0f87,00,0,7
SPIWrite 0f88,00,0,7
SPIWrite 0f89,00,0,7
SPIWrite 0f8a,00,0,7
SPIWrite 0f8b,00,0,7
SPIWrite 0f8c,00,0,7
SPIWrite 0f8d,00,0,7
SPIWrite 0f8e,00,0,7
SPIWrite 0f8f,00,0,7
SPIWrite 0f90,00,0,7
SPIWrite 0f91,00,0,7
SPIWrite 0f92,00,0,7
SPIWrite 0f93,00,0,7
SPIWrite 0f94,00,0,7
SPIWrite 0f95,00,0,7
SPIWrite 0f96,00,0,7
SPIWrite 0f97,00,0,7
SPIWrite 0f98,00,0,7
SPIWrite 0f99,00,0,7
SPIWrite 0f9a,00,0,7
SPIWrite 0f9b,00,0,7
SPIWrite 0f9c,00,0,7
SPIWrite 0f9d,00,0,7
SPIWrite 0f9e,00,0,7
SPIWrite 0f9f,00,0,7
SPIWrite 0fa0,00,0,7
SPIWrite 0fa1,00,0,7
SPIWrite 0fa2,00,0,7
SPIWrite 0fa3,00,0,7
SPIWrite 0fa4,00,0,7
SPIWrite 0fa5,00,0,7
SPIWrite 0fa6,00,0,7
SPIWrite 0fa7,00,0,7
SPIWrite 0fa8,00,0,7
SPIWrite 0fa9,00,0,7
SPIWrite 0faa,00,0,7
SPIWrite 0fab,00,0,7
SPIWrite 0fac,00,0,7
SPIWrite 0fad,00,0,7
SPIWrite 0fae,00,0,7

SPIWrite 0faf,00,0,7
SPIWrite 0fb0,00,0,7
SPIWrite 0fb1,00,0,7
SPIWrite 0fb2,00,0,7
SPIWrite 0fb3,00,0,7
SPIWrite 0fb4,00,0,7
SPIWrite 0fb5,00,0,7
SPIWrite 0fb6,00,0,7
SPIWrite 0fb7,00,0,7
SPIWrite 0fb8,00,0,7
SPIWrite 0fb9,00,0,7
SPIWrite 0fba,00,0,7
SPIWrite 0fbb,00,0,7
SPIWrite 0fbc,00,0,7
SPIWrite 0fbd,00,0,7
SPIWrite 0fbe,00,0,7
SPIWrite 0fbf,00,0,7
SPIWrite 0fc0,00,0,7
SPIWrite 0fc1,00,0,7
SPIWrite 0fc2,00,0,7
SPIWrite 0fc3,00,0,7
SPIWrite 0fc4,00,0,7
SPIWrite 0fc5,00,0,7
SPIWrite 0fc6,00,0,7
SPIWrite 0fc7,00,0,7
SPIWrite 0fc8,00,0,7
SPIWrite 0fc9,00,0,7
SPIWrite 0fca,00,0,7
SPIWrite 0fcb,00,0,7
SPIWrite 0fcc,00,0,7
SPIWrite 0fcd,00,0,7
SPIWrite 0fce,00,0,7
SPIWrite 0fcf,00,0,7
SPIWrite 0fd0,00,0,7
SPIWrite 0fd1,00,0,7
SPIWrite 0fd2,00,0,7
SPIWrite 0fd3,00,0,7
SPIWrite 0fd4,00,0,7
SPIWrite 0fd5,00,0,7
SPIWrite 0fd6,00,0,7
SPIWrite 0fd7,00,0,7
SPIWrite 0fd8,00,0,7
SPIWrite 0fd9,00,0,7
SPIWrite 0fda,00,0,7
SPIWrite 0fdb,00,0,7
SPIWrite 0fdc,00,0,7
SPIWrite 0fdd,00,0,7
SPIWrite 0fde,00,0,7
SPIWrite 0fdf,00,0,7
SPIWrite 0fe0,00,0,7
SPIWrite 0fe1,00,0,7
SPIWrite 0fe2,00,0,7
SPIWrite 0fe3,00,0,7
SPIWrite 0fe4,00,0,7
SPIWrite 0fe5,00,0,7
SPIWrite 0fe6,00,0,7
SPIWrite 0fe7,00,0,7
SPIWrite 0fe8,00,0,7
SPIWrite 0fe9,00,0,7
SPIWrite 0fea,00,0,7
SPIWrite 0feb,00,0,7
SPIWrite 0fec,00,0,7
SPIWrite 0fed,00,0,7
SPIWrite 0fee,00,0,7
SPIWrite 0fef,00,0,7
SPIWrite 0ff0,00,0,7
SPIWrite 0ff1,00,0,7
SPIWrite 0ff2,00,0,7
SPIWrite 0ff3,00,0,7

SPIWrite 0ff4,00,0,7
SPIWrite 0ff5,00,0,7
SPIWrite 0ff6,00,0,7
SPIWrite 0ff7,00,0,7
SPIWrite 0ff8,00,0,7
SPIWrite 0ff9,00,0,7
SPIWrite 0ffa,00,0,7
SPIWrite 0ffb,00,0,7
SPIWrite 0ffc,00,0,7
SPIWrite 0ffd,00,0,7
SPIWrite 0ffe,00,0,7
SPIWrite 0fff,00,0,7
SPIWrite 1000,00,0,7
SPIWrite 1001,00,0,7
SPIWrite 1002,00,0,7
SPIWrite 1003,00,0,7
SPIWrite 1004,00,0,7
SPIWrite 1005,00,0,7
SPIWrite 1006,00,0,7
SPIWrite 1007,00,0,7
SPIWrite 1008,00,0,7
SPIWrite 1009,00,0,7
SPIWrite 100a,00,0,7
SPIWrite 100b,00,0,7
SPIWrite 100c,00,0,7
SPIWrite 100d,00,0,7
SPIWrite 100e,00,0,7
SPIWrite 100f,00,0,7
SPIWrite 1010,00,0,7
SPIWrite 1011,00,0,7
SPIWrite 1012,00,0,7
SPIWrite 1013,00,0,7
SPIWrite 1014,00,0,7
SPIWrite 1015,00,0,7
SPIWrite 1016,00,0,7
SPIWrite 1017,00,0,7
SPIWrite 1018,00,0,7
SPIWrite 1019,00,0,7
SPIWrite 101a,00,0,7
SPIWrite 101b,00,0,7
SPIWrite 101c,00,0,7
SPIWrite 101d,00,0,7
SPIWrite 101e,00,0,7
SPIWrite 101f,00,0,7

WAIT 0.5

SPIWrite 0012,00,4,4 //PAGE: sys_calib_macro_memory=0x0;(Meaning:); Address(0x12[4:4],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1;(Meaning:); Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,10,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7
SPIWrite 0193,81,0,7

SPIPoll 00f0,1,1,1

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0;(Meaning:); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)

\\Read MACRO_READY=0x1; Address(0x20[0:0],)

\\Read MACRO_ACK=0x1; Address(0x20[1:1],)

\\Read MACRO_DONE=0x1; Address(0x20[2:2],)

SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)

SPIWrite 0012,10,4,4 //PAGE: sys_calib_macro_memory=0x1;(Meaning:); Address(0x12[4:4],)

SPIWrite 0040,00,0,7

SPIWrite 0020,00,0,7

SPIWrite 0021,7c,0,7

SPIWrite 0022,01,0,7

SPIWrite 0023,20,0,7

SPIWrite 0024,91,0,7

SPIWrite 0025,00,0,7

SPIWrite 0026,00,0,7

SPIWrite 0027,00,0,7

SPIWrite 0028,cd,0,7

SPIWrite 0029,d3,0,7

SPIWrite 002a,01,0,7

SPIWrite 002b,00,0,7

SPIWrite 002c,e9,0,7

SPIWrite 002d,d3,0,7

SPIWrite 002e,01,0,7

SPIWrite 002f,00,0,7

SPIWrite 0030,db,0,7

SPIWrite 0031,d3,0,7

SPIWrite 0032,01,0,7

SPIWrite 0033,00,0,7

SPIWrite 0034,05,0,7

SPIWrite 0035,d4,0,7

SPIWrite 0036,01,0,7

SPIWrite 0037,00,0,7

SPIWrite 0038,a3,0,7

SPIWrite 0039,d3,0,7

SPIWrite 003a,01,0,7

SPIWrite 003b,00,0,7

SPIWrite 003c,00,0,7

SPIWrite 003d,00,0,7

SPIWrite 003e,00,0,7

SPIWrite 003f,00,0,7

SPIWrite 0040,00,0,7

SPIWrite 0041,00,0,7

SPIWrite 0042,00,0,7

SPIWrite 0043,00,0,7

SPIWrite 0044,00,0,7

SPIWrite 0045,00,0,7

SPIWrite 0046,00,0,7

SPIWrite 0047,00,0,7

SPIWrite 0048,00,0,7

SPIWrite 0049,00,0,7

SPIWrite 004a,00,0,7

SPIWrite 004b,00,0,7

SPIWrite 004c,b1,0,7

SPIWrite 004d,d3,0,7

SPIWrite 004e,01,0,7

SPIWrite 004f,00,0,7

SPIWrite 0050,f7,0,7

SPIWrite 0051,d3,0,7

SPIWrite 0052,01,0,7

SPIWrite 0053,00,0,7

SPIWrite 0054,00,0,7

SPIWrite 0055,00,0,7
SPIWrite 0056,00,0,7
SPIWrite 0057,00,0,7
SPIWrite 0058,bf,0,7
SPIWrite 0059,d3,0,7
SPIWrite 005a,01,0,7
SPIWrite 005b,00,0,7
SPIWrite 005c,af,0,7
SPIWrite 005d,d3,0,7
SPIWrite 005e,01,0,7
SPIWrite 005f,00,0,7
SPIWrite 0060,9f,0,7
SPIWrite 0061,d5,0,7
SPIWrite 0062,01,0,7
SPIWrite 0063,00,0,7
SPIWrite 0064,57,0,7
SPIWrite 0065,d4,0,7
SPIWrite 0066,01,0,7
SPIWrite 0067,00,0,7
SPIWrite 0068,61,0,7
SPIWrite 0069,d4,0,7
SPIWrite 006a,01,0,7
SPIWrite 006b,00,0,7
SPIWrite 006c,31,0,7
SPIWrite 006d,d4,0,7
SPIWrite 006e,01,0,7
SPIWrite 006f,00,0,7
SPIWrite 0070,43,0,7
SPIWrite 0071,d4,0,7
SPIWrite 0072,01,0,7
SPIWrite 0073,00,0,7
SPIWrite 0074,55,0,7
SPIWrite 0075,d4,0,7
SPIWrite 0076,01,0,7
SPIWrite 0077,00,0,7
SPIWrite 0078,21,0,7
SPIWrite 0079,d4,0,7
SPIWrite 007a,01,0,7
SPIWrite 007b,00,0,7
SPIWrite 007c,55,0,7
SPIWrite 007d,d4,0,7
SPIWrite 007e,01,0,7
SPIWrite 007f,00,0,7
SPIWrite 0080,15,0,7
SPIWrite 0081,ff,0,7
SPIWrite 0082,00,0,7
SPIWrite 0083,00,0,7
SPIWrite 0084,55,0,7
SPIWrite 0085,d4,0,7
SPIWrite 0086,01,0,7
SPIWrite 0087,00,0,7
SPIWrite 0088,79,0,7
SPIWrite 0089,80,0,7
SPIWrite 008a,02,0,7
SPIWrite 008b,00,0,7
SPIWrite 008c,55,0,7
SPIWrite 008d,d4,0,7
SPIWrite 008e,01,0,7
SPIWrite 008f,00,0,7
SPIWrite 0090,55,0,7
SPIWrite 0091,d4,0,7
SPIWrite 0092,01,0,7
SPIWrite 0093,00,0,7
SPIWrite 0094,57,0,7
SPIWrite 0095,81,0,7
SPIWrite 0096,02,0,7
SPIWrite 0097,00,0,7
SPIWrite 0098,55,0,7
SPIWrite 0099,d4,0,7

SPIWrite 009a,01,0,7
SPIWrite 009b,00,0,7
SPIWrite 009c,55,0,7
SPIWrite 009d,d4,0,7
SPIWrite 009e,01,0,7
SPIWrite 009f,00,0,7
SPIWrite 00a0,6d,0,7
SPIWrite 00a1,d4,0,7
SPIWrite 00a2,01,0,7
SPIWrite 00a3,00,0,7
SPIWrite 00a4,55,0,7
SPIWrite 00a5,d4,0,7
SPIWrite 00a6,01,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a8,55,0,7
SPIWrite 00a9,d4,0,7
SPIWrite 00aa,01,0,7
SPIWrite 00ab,00,0,7
SPIWrite 00ac,79,0,7
SPIWrite 00ad,fd,0,7
SPIWrite 00ae,00,0,7
SPIWrite 00af,00,0,7
SPIWrite 00b0,55,0,7
SPIWrite 00b1,d4,0,7
SPIWrite 00b2,01,0,7
SPIWrite 00b3,00,0,7
SPIWrite 00b4,55,0,7
SPIWrite 00b5,d4,0,7
SPIWrite 00b6,01,0,7
SPIWrite 00b7,00,0,7
SPIWrite 00b8,5d,0,7
SPIWrite 00b9,8d,0,7
SPIWrite 00ba,02,0,7
SPIWrite 00bb,00,0,7
SPIWrite 00bc,00,0,7
SPIWrite 00bd,00,0,7
SPIWrite 00be,00,0,7
SPIWrite 00bf,00,0,7
SPIWrite 00c0,00,0,7
SPIWrite 00c1,00,0,7
SPIWrite 00c2,00,0,7
SPIWrite 00c3,00,0,7
SPIWrite 00c4,00,0,7
SPIWrite 00c5,00,0,7
SPIWrite 00c6,00,0,7
SPIWrite 00c7,00,0,7
SPIWrite 00c8,00,0,7
SPIWrite 00c9,00,0,7
SPIWrite 00ca,00,0,7
SPIWrite 00cb,00,0,7
SPIWrite 00cc,00,0,7
SPIWrite 00cd,00,0,7
SPIWrite 00ce,00,0,7
SPIWrite 00cf,00,0,7
SPIWrite 00d0,00,0,7
SPIWrite 00d1,00,0,7
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SPIWrite 00db,00,0,7
SPIWrite 00dc,00,0,7
SPIWrite 00dd,00,0,7
SPIWrite 00de,00,0,7

SPIWrite 00df,00,0,7
SPIWrite 00e0,00,0,7
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SPIWrite 00e7,00,0,7
SPIWrite 00e8,00,0,7
SPIWrite 00e9,00,0,7
SPIWrite 00ea,00,0,7
SPIWrite 00eb,00,0,7
SPIWrite 00ec,00,0,7
SPIWrite 00ed,00,0,7
SPIWrite 00ee,00,0,7
SPIWrite 00ef,00,0,7
SPIWrite 00f0,00,0,7
SPIWrite 00f1,00,0,7
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SPIWrite 00f5,00,0,7
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SPIWrite 00f7,00,0,7
SPIWrite 00f8,00,0,7
SPIWrite 00f9,00,0,7
SPIWrite 00fa,00,0,7
SPIWrite 00fb,00,0,7
SPIWrite 00fc,00,0,7
SPIWrite 00fd,00,0,7
SPIWrite 00fe,00,0,7
SPIWrite 00ff,00,0,7
SPIWrite 0100,00,0,7
SPIWrite 0101,00,0,7
SPIWrite 0102,00,0,7
SPIWrite 0103,00,0,7
SPIWrite 0104,00,0,7
SPIWrite 0105,00,0,7
SPIWrite 0106,00,0,7
SPIWrite 0107,00,0,7
SPIWrite 0108,00,0,7
SPIWrite 0109,00,0,7
SPIWrite 010a,00,0,7
SPIWrite 010b,00,0,7
SPIWrite 010c,00,0,7
SPIWrite 010d,00,0,7
SPIWrite 010e,00,0,7
SPIWrite 010f,00,0,7
SPIWrite 0110,00,0,7
SPIWrite 0111,00,0,7
SPIWrite 0112,00,0,7
SPIWrite 0113,00,0,7
SPIWrite 0114,00,0,7
SPIWrite 0115,00,0,7
SPIWrite 0116,00,0,7
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SPIWrite 0118,00,0,7
SPIWrite 0119,00,0,7
SPIWrite 011a,00,0,7
SPIWrite 011b,00,0,7
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SPIWrite 0120,00,0,7
SPIWrite 0121,00,0,7
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SPIWrite 0123,00,0,7

SPIWrite 0124,00,0,7
SPIWrite 0125,00,0,7
SPIWrite 0126,00,0,7
SPIWrite 0127,00,0,7
SPIWrite 0128,00,0,7
SPIWrite 0129,00,0,7
SPIWrite 012a,00,0,7
SPIWrite 012b,00,0,7
SPIWrite 012c,00,0,7
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SPIWrite 012e,00,0,7
SPIWrite 012f,00,0,7
SPIWrite 0130,00,0,7
SPIWrite 0131,00,0,7
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SPIWrite 0139,00,0,7
SPIWrite 013a,00,0,7
SPIWrite 013b,00,0,7
SPIWrite 013c,00,0,7
SPIWrite 013d,00,0,7
SPIWrite 013e,00,0,7
SPIWrite 013f,00,0,7
SPIWrite 0140,00,0,7
SPIWrite 0141,00,0,7
SPIWrite 0142,00,0,7
SPIWrite 0143,00,0,7
SPIWrite 0144,00,0,7
SPIWrite 0145,00,0,7
SPIWrite 0146,00,0,7
SPIWrite 0147,00,0,7
SPIWrite 0148,00,0,7
SPIWrite 0149,00,0,7
SPIWrite 014a,00,0,7
SPIWrite 014b,00,0,7
SPIWrite 014c,00,0,7
SPIWrite 014d,00,0,7
SPIWrite 014e,00,0,7
SPIWrite 014f,00,0,7
SPIWrite 0150,00,0,7
SPIWrite 0151,00,0,7
SPIWrite 0152,00,0,7
SPIWrite 0153,00,0,7
SPIWrite 0154,00,0,7
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SPIWrite 0156,00,0,7
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SPIWrite 0160,00,0,7
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SPIWrite 016d,00,0,7
SPIWrite 016e,00,0,7
SPIWrite 016f,00,0,7
SPIWrite 0170,00,0,7
SPIWrite 0171,00,0,7
SPIWrite 0172,00,0,7
SPIWrite 0173,00,0,7
SPIWrite 0174,00,0,7
SPIWrite 0175,00,0,7
SPIWrite 0176,00,0,7
SPIWrite 0177,00,0,7
SPIWrite 0178,00,0,7
SPIWrite 0179,00,0,7
SPIWrite 017a,00,0,7
SPIWrite 017b,00,0,7
SPIWrite 017c,00,0,7
SPIWrite 017d,00,0,7
SPIWrite 017e,00,0,7
SPIWrite 017f,00,0,7
SPIWrite 0180,00,0,7
SPIWrite 0181,00,0,7
SPIWrite 0182,00,0,7
SPIWrite 0183,00,0,7
SPIWrite 0184,00,0,7
SPIWrite 0185,00,0,7
SPIWrite 0186,00,0,7
SPIWrite 0187,00,0,7
SPIWrite 0188,00,0,7
SPIWrite 0189,00,0,7
SPIWrite 018a,00,0,7
SPIWrite 018b,00,0,7
SPIWrite 018c,00,0,7
SPIWrite 018d,00,0,7
SPIWrite 018e,00,0,7
SPIWrite 018f,00,0,7
SPIWrite 0190,00,0,7
SPIWrite 0191,00,0,7
SPIWrite 0192,00,0,7
SPIWrite 0193,00,0,7
SPIWrite 0194,00,0,7
SPIWrite 0195,00,0,7
SPIWrite 0196,00,0,7
SPIWrite 0197,00,0,7
SPIWrite 0198,00,0,7
SPIWrite 0199,00,0,7
SPIWrite 019a,00,0,7
SPIWrite 019b,00,0,7
SPIWrite 019c,00,0,7
SPIWrite 019d,00,0,7
SPIWrite 019e,00,0,7
SPIWrite 019f,00,0,7
SPIWrite 01a0,00,0,7
SPIWrite 01a1,00,0,7
SPIWrite 01a2,00,0,7
SPIWrite 01a3,00,0,7
SPIWrite 01a4,00,0,7
SPIWrite 01a5,00,0,7
SPIWrite 01a6,00,0,7
SPIWrite 01a7,00,0,7
SPIWrite 01a8,00,0,7
SPIWrite 01a9,00,0,7
SPIWrite 01aa,00,0,7
SPIWrite 01ab,00,0,7
SPIWrite 01ac,00,0,7
SPIWrite 01ad,00,0,7

SPIWrite 01ae,00,0,7
SPIWrite 01af,00,0,7
SPIWrite 01b0,00,0,7
SPIWrite 01b1,00,0,7
SPIWrite 01b2,00,0,7
SPIWrite 01b3,00,0,7
SPIWrite 01b4,00,0,7
SPIWrite 01b5,00,0,7
SPIWrite 01b6,00,0,7
SPIWrite 01b7,00,0,7
SPIWrite 01b8,00,0,7
SPIWrite 01b9,00,0,7
SPIWrite 01ba,00,0,7
SPIWrite 01bb,00,0,7
SPIWrite 01bc,00,0,7
SPIWrite 01bd,00,0,7
SPIWrite 01be,00,0,7
SPIWrite 01bf,00,0,7
SPIWrite 01c0,00,0,7
SPIWrite 01c1,00,0,7
SPIWrite 01c2,00,0,7
SPIWrite 01c3,00,0,7
SPIWrite 01c4,00,0,7
SPIWrite 01c5,00,0,7
SPIWrite 01c6,00,0,7
SPIWrite 01c7,00,0,7
SPIWrite 01c8,00,0,7
SPIWrite 01c9,00,0,7
SPIWrite 01ca,00,0,7
SPIWrite 01cb,00,0,7
SPIWrite 01cc,00,0,7
SPIWrite 01cd,00,0,7
SPIWrite 01ce,00,0,7
SPIWrite 01cf,00,0,7
SPIWrite 01d0,00,0,7
SPIWrite 01d1,00,0,7
SPIWrite 01d2,00,0,7
SPIWrite 01d3,00,0,7
SPIWrite 01d4,00,0,7
SPIWrite 01d5,00,0,7
SPIWrite 01d6,00,0,7
SPIWrite 01d7,00,0,7
SPIWrite 01d8,00,0,7
SPIWrite 01d9,00,0,7
SPIWrite 01da,00,0,7
SPIWrite 01db,00,0,7
SPIWrite 01dc,00,0,7
SPIWrite 01dd,00,0,7
SPIWrite 01de,00,0,7
SPIWrite 01df,00,0,7
SPIWrite 01e0,00,0,7
SPIWrite 01e1,00,0,7
SPIWrite 01e2,00,0,7
SPIWrite 01e3,00,0,7
SPIWrite 01e4,00,0,7
SPIWrite 01e5,00,0,7
SPIWrite 01e6,00,0,7
SPIWrite 01e7,00,0,7
SPIWrite 01e8,00,0,7
SPIWrite 01e9,00,0,7
SPIWrite 01ea,00,0,7
SPIWrite 01eb,00,0,7
SPIWrite 01ec,00,0,7
SPIWrite 01ed,00,0,7
SPIWrite 01ee,00,0,7
SPIWrite 01ef,00,0,7
SPIWrite 01f0,00,0,7
SPIWrite 01f1,00,0,7
SPIWrite 01f2,00,0,7

SPIWrite 01f3,00,0,7
SPIWrite 01f4,00,0,7
SPIWrite 01f5,00,0,7
SPIWrite 01f6,00,0,7
SPIWrite 01f7,00,0,7
SPIWrite 01f8,00,0,7
SPIWrite 01f9,00,0,7
SPIWrite 01fa,00,0,7
SPIWrite 01fb,00,0,7
SPIWrite 01fc,00,0,7
SPIWrite 01fd,00,0,7
SPIWrite 01fe,00,0,7
SPIWrite 01ff,00,0,7
SPIWrite 0200,00,0,7
SPIWrite 0201,00,0,7
SPIWrite 0202,00,0,7
SPIWrite 0203,00,0,7
SPIWrite 0204,00,0,7
SPIWrite 0205,00,0,7
SPIWrite 0206,00,0,7
SPIWrite 0207,00,0,7
SPIWrite 0208,00,0,7
SPIWrite 0209,00,0,7
SPIWrite 020a,00,0,7
SPIWrite 020b,00,0,7
SPIWrite 020c,00,0,7
SPIWrite 020d,00,0,7
SPIWrite 020e,00,0,7
SPIWrite 020f,00,0,7
SPIWrite 0210,00,0,7
SPIWrite 0211,00,0,7
SPIWrite 0212,00,0,7
SPIWrite 0213,00,0,7
SPIWrite 0214,00,0,7
SPIWrite 0215,00,0,7
SPIWrite 0216,00,0,7
SPIWrite 0217,00,0,7
SPIWrite 0218,00,0,7
SPIWrite 0219,00,0,7
SPIWrite 021a,00,0,7
SPIWrite 021b,00,0,7
SPIWrite 021c,00,0,7
SPIWrite 021d,00,0,7
SPIWrite 021e,00,0,7
SPIWrite 021f,00,0,7
SPIWrite 0220,00,0,7
SPIWrite 0221,00,0,7
SPIWrite 0222,00,0,7
SPIWrite 0223,00,0,7
SPIWrite 0224,00,0,7
SPIWrite 0225,00,0,7
SPIWrite 0226,00,0,7
SPIWrite 0227,00,0,7
SPIWrite 0228,00,0,7
SPIWrite 0229,00,0,7
SPIWrite 022a,00,0,7
SPIWrite 022b,00,0,7
SPIWrite 022c,00,0,7
SPIWrite 022d,00,0,7
SPIWrite 022e,00,0,7
SPIWrite 022f,00,0,7
SPIWrite 0230,00,0,7
SPIWrite 0231,00,0,7
SPIWrite 0232,00,0,7
SPIWrite 0233,00,0,7
SPIWrite 0234,00,0,7
SPIWrite 0235,00,0,7
SPIWrite 0236,00,0,7
SPIWrite 0237,00,0,7

SPIWrite 0238,00,0,7
SPIWrite 0239,00,0,7
SPIWrite 023a,00,0,7
SPIWrite 023b,00,0,7
SPIWrite 023c,00,0,7
SPIWrite 023d,00,0,7
SPIWrite 023e,00,0,7
SPIWrite 023f,00,0,7
SPIWrite 0240,00,0,7
SPIWrite 0241,00,0,7
SPIWrite 0242,00,0,7
SPIWrite 0243,00,0,7
SPIWrite 0244,00,0,7
SPIWrite 0245,00,0,7
SPIWrite 0246,00,0,7
SPIWrite 0247,00,0,7
SPIWrite 0248,00,0,7
SPIWrite 0249,00,0,7
SPIWrite 024a,00,0,7
SPIWrite 024b,00,0,7
SPIWrite 024c,00,0,7
SPIWrite 024d,00,0,7
SPIWrite 024e,00,0,7
SPIWrite 024f,00,0,7
SPIWrite 0250,00,0,7
SPIWrite 0251,00,0,7
SPIWrite 0252,00,0,7
SPIWrite 0253,00,0,7
SPIWrite 0254,00,0,7
SPIWrite 0255,00,0,7
SPIWrite 0256,00,0,7
SPIWrite 0257,00,0,7
SPIWrite 0258,00,0,7
SPIWrite 0259,00,0,7
SPIWrite 025a,00,0,7
SPIWrite 025b,00,0,7
SPIWrite 025c,00,0,7
SPIWrite 025d,00,0,7
SPIWrite 025e,00,0,7
SPIWrite 025f,00,0,7
SPIWrite 0260,00,0,7
SPIWrite 0261,00,0,7
SPIWrite 0262,00,0,7
SPIWrite 0263,00,0,7
SPIWrite 0264,00,0,7
SPIWrite 0265,00,0,7
SPIWrite 0266,00,0,7
SPIWrite 0267,00,0,7
SPIWrite 0268,00,0,7
SPIWrite 0269,00,0,7
SPIWrite 026a,00,0,7
SPIWrite 026b,00,0,7
SPIWrite 026c,00,0,7
SPIWrite 026d,00,0,7
SPIWrite 026e,00,0,7
SPIWrite 026f,00,0,7
SPIWrite 0270,00,0,7
SPIWrite 0271,00,0,7
SPIWrite 0272,00,0,7
SPIWrite 0273,00,0,7
SPIWrite 0274,00,0,7
SPIWrite 0275,00,0,7
SPIWrite 0276,00,0,7
SPIWrite 0277,00,0,7
SPIWrite 0278,00,0,7
SPIWrite 0279,00,0,7
SPIWrite 027a,00,0,7
SPIWrite 027b,00,0,7
SPIWrite 027c,00,0,7

SPIWrite 027d,00,0,7
SPIWrite 027e,00,0,7
SPIWrite 027f,00,0,7
SPIWrite 0280,00,0,7
SPIWrite 0281,00,0,7
SPIWrite 0282,00,0,7
SPIWrite 0283,00,0,7
SPIWrite 0284,00,0,7
SPIWrite 0285,00,0,7
SPIWrite 0286,00,0,7
SPIWrite 0287,00,0,7
SPIWrite 0288,00,0,7
SPIWrite 0289,00,0,7
SPIWrite 028a,00,0,7
SPIWrite 028b,00,0,7
SPIWrite 028c,00,0,7
SPIWrite 028d,00,0,7
SPIWrite 028e,00,0,7
SPIWrite 028f,00,0,7
SPIWrite 0290,00,0,7
SPIWrite 0291,00,0,7
SPIWrite 0292,00,0,7
SPIWrite 0293,00,0,7
SPIWrite 0294,00,0,7
SPIWrite 0295,00,0,7
SPIWrite 0296,00,0,7
SPIWrite 0297,00,0,7
SPIWrite 0298,00,0,7
SPIWrite 0299,00,0,7
SPIWrite 029a,00,0,7
SPIWrite 029b,00,0,7
SPIWrite 029c,00,0,7
SPIWrite 029d,00,0,7
SPIWrite 029e,00,0,7
SPIWrite 029f,00,0,7
SPIWrite 02a0,00,0,7
SPIWrite 02a1,00,0,7
SPIWrite 02a2,00,0,7
SPIWrite 02a3,00,0,7
SPIWrite 02a4,00,0,7
SPIWrite 02a5,00,0,7
SPIWrite 02a6,00,0,7
SPIWrite 02a7,00,0,7
SPIWrite 02a8,00,0,7
SPIWrite 02a9,00,0,7
SPIWrite 02aa,00,0,7
SPIWrite 02ab,00,0,7
SPIWrite 02ac,00,0,7
SPIWrite 02ad,00,0,7
SPIWrite 02ae,00,0,7
SPIWrite 02af,00,0,7
SPIWrite 02b0,00,0,7
SPIWrite 02b1,00,0,7
SPIWrite 02b2,00,0,7
SPIWrite 02b3,00,0,7
SPIWrite 02b4,00,0,7
SPIWrite 02b5,00,0,7
SPIWrite 02b6,00,0,7
SPIWrite 02b7,00,0,7
SPIWrite 02b8,00,0,7
SPIWrite 02b9,00,0,7
SPIWrite 02ba,00,0,7
SPIWrite 02bb,00,0,7
SPIWrite 02bc,00,0,7
SPIWrite 02bd,00,0,7
SPIWrite 02be,00,0,7
SPIWrite 02bf,00,0,7
SPIWrite 02c0,00,0,7
SPIWrite 02c1,00,0,7

SPIWrite 02c2,00,0,7
SPIWrite 02c3,00,0,7
SPIWrite 02c4,00,0,7
SPIWrite 02c5,00,0,7
SPIWrite 02c6,00,0,7
SPIWrite 02c7,00,0,7
SPIWrite 02c8,00,0,7
SPIWrite 02c9,00,0,7
SPIWrite 02ca,00,0,7
SPIWrite 02cb,00,0,7
SPIWrite 02cc,00,0,7
SPIWrite 02cd,00,0,7
SPIWrite 02ce,00,0,7
SPIWrite 02cf,00,0,7
SPIWrite 02d0,00,0,7
SPIWrite 02d1,00,0,7
SPIWrite 02d2,00,0,7
SPIWrite 02d3,00,0,7
SPIWrite 02d4,00,0,7
SPIWrite 02d5,00,0,7
SPIWrite 02d6,00,0,7
SPIWrite 02d7,00,0,7
SPIWrite 02d8,00,0,7
SPIWrite 02d9,00,0,7
SPIWrite 02da,00,0,7
SPIWrite 02db,00,0,7
SPIWrite 02dc,00,0,7
SPIWrite 02dd,00,0,7
SPIWrite 02de,00,0,7
SPIWrite 02df,00,0,7
SPIWrite 02e0,00,0,7
SPIWrite 02e1,00,0,7
SPIWrite 02e2,00,0,7
SPIWrite 02e3,00,0,7
SPIWrite 02e4,00,0,7
SPIWrite 02e5,00,0,7
SPIWrite 02e6,00,0,7
SPIWrite 02e7,00,0,7
SPIWrite 02e8,00,0,7
SPIWrite 02e9,00,0,7
SPIWrite 02ea,00,0,7
SPIWrite 02eb,00,0,7
SPIWrite 02ec,00,0,7
SPIWrite 02ed,00,0,7
SPIWrite 02ee,00,0,7
SPIWrite 02ef,00,0,7
SPIWrite 02f0,00,0,7
SPIWrite 02f1,00,0,7
SPIWrite 02f2,00,0,7
SPIWrite 02f3,00,0,7
SPIWrite 02f4,00,0,7
SPIWrite 02f5,00,0,7
SPIWrite 02f6,00,0,7
SPIWrite 02f7,00,0,7
SPIWrite 02f8,00,0,7
SPIWrite 02f9,00,0,7
SPIWrite 02fa,00,0,7
SPIWrite 02fb,00,0,7
SPIWrite 02fc,00,0,7
SPIWrite 02fd,00,0,7
SPIWrite 02fe,00,0,7
SPIWrite 02ff,00,0,7
SPIWrite 0300,00,0,7
SPIWrite 0301,00,0,7
SPIWrite 0302,00,0,7
SPIWrite 0303,00,0,7
SPIWrite 0304,00,0,7
SPIWrite 0305,00,0,7
SPIWrite 0306,00,0,7

SPIWrite 0307,00,0,7
SPIWrite 0308,00,0,7
SPIWrite 0309,00,0,7
SPIWrite 030a,00,0,7
SPIWrite 030b,00,0,7
SPIWrite 030c,00,0,7
SPIWrite 030d,00,0,7
SPIWrite 030e,00,0,7
SPIWrite 030f,00,0,7
SPIWrite 0310,00,0,7
SPIWrite 0311,00,0,7
SPIWrite 0312,00,0,7
SPIWrite 0313,00,0,7
SPIWrite 0314,00,0,7
SPIWrite 0315,00,0,7
SPIWrite 0316,00,0,7
SPIWrite 0317,00,0,7
SPIWrite 0318,00,0,7
SPIWrite 0319,00,0,7
SPIWrite 031a,00,0,7
SPIWrite 031b,00,0,7
SPIWrite 031c,00,0,7
SPIWrite 031d,00,0,7
SPIWrite 031e,00,0,7
SPIWrite 031f,00,0,7
SPIWrite 0320,00,0,7
SPIWrite 0321,00,0,7
SPIWrite 0322,00,0,7
SPIWrite 0323,00,0,7
SPIWrite 0324,00,0,7
SPIWrite 0325,00,0,7
SPIWrite 0326,00,0,7
SPIWrite 0327,00,0,7
SPIWrite 0328,00,0,7
SPIWrite 0329,00,0,7
SPIWrite 032a,00,0,7
SPIWrite 032b,00,0,7
SPIWrite 032c,00,0,7
SPIWrite 032d,00,0,7
SPIWrite 032e,00,0,7
SPIWrite 032f,00,0,7
SPIWrite 0330,00,0,7
SPIWrite 0331,00,0,7
SPIWrite 0332,00,0,7
SPIWrite 0333,00,0,7
SPIWrite 0334,00,0,7
SPIWrite 0335,00,0,7
SPIWrite 0336,00,0,7
SPIWrite 0337,00,0,7
SPIWrite 0338,00,0,7
SPIWrite 0339,00,0,7
SPIWrite 033a,00,0,7
SPIWrite 033b,00,0,7
SPIWrite 033c,00,0,7
SPIWrite 033d,00,0,7
SPIWrite 033e,00,0,7
SPIWrite 033f,00,0,7
SPIWrite 0340,00,0,7
SPIWrite 0341,00,0,7
SPIWrite 0342,00,0,7
SPIWrite 0343,00,0,7
SPIWrite 0344,00,0,7
SPIWrite 0345,00,0,7
SPIWrite 0346,00,0,7
SPIWrite 0347,00,0,7
SPIWrite 0348,00,0,7
SPIWrite 0349,00,0,7
SPIWrite 034a,00,0,7
SPIWrite 034b,00,0,7

SPIWrite 034c,00,0,7
SPIWrite 034d,00,0,7
SPIWrite 034e,00,0,7
SPIWrite 034f,00,0,7
SPIWrite 0350,00,0,7
SPIWrite 0351,00,0,7
SPIWrite 0352,00,0,7
SPIWrite 0353,00,0,7
SPIWrite 0354,00,0,7
SPIWrite 0355,00,0,7
SPIWrite 0356,00,0,7
SPIWrite 0357,00,0,7
SPIWrite 0358,00,0,7
SPIWrite 0359,00,0,7
SPIWrite 035a,00,0,7
SPIWrite 035b,00,0,7
SPIWrite 035c,00,0,7
SPIWrite 035d,00,0,7
SPIWrite 035e,00,0,7
SPIWrite 035f,00,0,7
SPIWrite 0360,00,0,7
SPIWrite 0361,00,0,7
SPIWrite 0362,00,0,7
SPIWrite 0363,00,0,7
SPIWrite 0364,00,0,7
SPIWrite 0365,00,0,7
SPIWrite 0366,00,0,7
SPIWrite 0367,00,0,7
SPIWrite 0368,00,0,7
SPIWrite 0369,00,0,7
SPIWrite 036a,00,0,7
SPIWrite 036b,00,0,7
SPIWrite 036c,00,0,7
SPIWrite 036d,00,0,7
SPIWrite 036e,00,0,7
SPIWrite 036f,00,0,7
SPIWrite 0370,00,0,7
SPIWrite 0371,00,0,7
SPIWrite 0372,00,0,7
SPIWrite 0373,00,0,7
SPIWrite 0374,00,0,7
SPIWrite 0375,00,0,7
SPIWrite 0376,00,0,7
SPIWrite 0377,00,0,7
SPIWrite 0378,00,0,7
SPIWrite 0379,00,0,7
SPIWrite 037a,00,0,7
SPIWrite 037b,00,0,7
SPIWrite 037c,00,0,7
SPIWrite 037d,00,0,7
SPIWrite 037e,00,0,7
SPIWrite 037f,00,0,7
SPIWrite 0380,00,0,7
SPIWrite 0381,00,0,7
SPIWrite 0382,00,0,7
SPIWrite 0383,00,0,7
SPIWrite 0384,00,0,7
SPIWrite 0385,00,0,7
SPIWrite 0386,00,0,7
SPIWrite 0387,00,0,7
SPIWrite 0388,00,0,7
SPIWrite 0389,00,0,7
SPIWrite 038a,00,0,7
SPIWrite 038b,00,0,7
SPIWrite 038c,00,0,7
SPIWrite 038d,00,0,7
SPIWrite 038e,00,0,7
SPIWrite 038f,00,0,7
SPIWrite 0390,00,0,7

SPIWrite 0391,00,0,7
SPIWrite 0392,00,0,7
SPIWrite 0393,00,0,7
SPIWrite 0394,00,0,7
SPIWrite 0395,00,0,7
SPIWrite 0396,00,0,7
SPIWrite 0397,00,0,7
SPIWrite 0398,00,0,7
SPIWrite 0399,00,0,7
SPIWrite 039a,00,0,7
SPIWrite 039b,00,0,7
SPIWrite 039c,00,0,7
SPIWrite 039d,00,0,7
SPIWrite 039e,00,0,7
SPIWrite 039f,00,0,7
SPIWrite 03a0,00,0,7
SPIWrite 03a1,00,0,7
SPIWrite 03a2,00,0,7
SPIWrite 03a3,00,0,7
SPIWrite 03a4,00,0,7
SPIWrite 03a5,00,0,7
SPIWrite 03a6,00,0,7
SPIWrite 03a7,00,0,7
SPIWrite 03a8,00,0,7
SPIWrite 03a9,00,0,7
SPIWrite 03aa,00,0,7
SPIWrite 03ab,00,0,7
SPIWrite 03ac,00,0,7
SPIWrite 03ad,00,0,7
SPIWrite 03ae,00,0,7
SPIWrite 03af,00,0,7
SPIWrite 03b0,00,0,7
SPIWrite 03b1,00,0,7
SPIWrite 03b2,00,0,7
SPIWrite 03b3,00,0,7
SPIWrite 03b4,00,0,7
SPIWrite 03b5,00,0,7
SPIWrite 03b6,00,0,7
SPIWrite 03b7,00,0,7
SPIWrite 03b8,00,0,7
SPIWrite 03b9,00,0,7
SPIWrite 03ba,00,0,7
SPIWrite 03bb,00,0,7
SPIWrite 03bc,00,0,7
SPIWrite 03bd,00,0,7
SPIWrite 03be,00,0,7
SPIWrite 03bf,00,0,7
SPIWrite 03c0,00,0,7
SPIWrite 03c1,00,0,7
SPIWrite 03c2,00,0,7
SPIWrite 03c3,00,0,7
SPIWrite 03c4,00,0,7
SPIWrite 03c5,00,0,7
SPIWrite 03c6,00,0,7
SPIWrite 03c7,00,0,7
SPIWrite 03c8,00,0,7
SPIWrite 03c9,00,0,7
SPIWrite 03ca,00,0,7
SPIWrite 03cb,00,0,7
SPIWrite 03cc,00,0,7
SPIWrite 03cd,00,0,7
SPIWrite 03ce,00,0,7
SPIWrite 03cf,00,0,7
SPIWrite 03d0,00,0,7
SPIWrite 03d1,00,0,7
SPIWrite 03d2,00,0,7
SPIWrite 03d3,00,0,7
SPIWrite 03d4,00,0,7
SPIWrite 03d5,00,0,7

SPIWrite 03d6,00,0,7
SPIWrite 03d7,00,0,7
SPIWrite 03d8,00,0,7
SPIWrite 03d9,00,0,7
SPIWrite 03da,00,0,7
SPIWrite 03db,00,0,7
SPIWrite 03dc,00,0,7
SPIWrite 03dd,00,0,7
SPIWrite 03de,00,0,7
SPIWrite 03df,00,0,7
SPIWrite 03e0,00,0,7
SPIWrite 03e1,00,0,7
SPIWrite 03e2,00,0,7
SPIWrite 03e3,00,0,7
SPIWrite 03e4,00,0,7
SPIWrite 03e5,00,0,7
SPIWrite 03e6,00,0,7
SPIWrite 03e7,00,0,7
SPIWrite 03e8,00,0,7
SPIWrite 03e9,00,0,7
SPIWrite 03ea,00,0,7
SPIWrite 03eb,00,0,7
SPIWrite 03ec,00,0,7
SPIWrite 03ed,00,0,7
SPIWrite 03ee,00,0,7
SPIWrite 03ef,00,0,7
SPIWrite 03f0,00,0,7
SPIWrite 03f1,00,0,7
SPIWrite 03f2,00,0,7
SPIWrite 03f3,00,0,7
SPIWrite 03f4,00,0,7
SPIWrite 03f5,00,0,7
SPIWrite 03f6,00,0,7
SPIWrite 03f7,00,0,7
SPIWrite 03f8,00,0,7
SPIWrite 03f9,00,0,7
SPIWrite 03fa,00,0,7
SPIWrite 03fb,00,0,7
SPIWrite 03fc,00,0,7
SPIWrite 03fd,00,0,7
SPIWrite 03fe,00,0,7
SPIWrite 03ff,00,0,7
SPIWrite 0400,00,0,7
SPIWrite 0401,00,0,7
SPIWrite 0402,00,0,7
SPIWrite 0403,00,0,7
SPIWrite 0404,00,0,7
SPIWrite 0405,00,0,7
SPIWrite 0406,00,0,7
SPIWrite 0407,00,0,7
SPIWrite 0408,00,0,7
SPIWrite 0409,00,0,7
SPIWrite 040a,00,0,7
SPIWrite 040b,00,0,7
SPIWrite 040c,00,0,7
SPIWrite 040d,00,0,7
SPIWrite 040e,00,0,7
SPIWrite 040f,00,0,7
SPIWrite 0410,00,0,7
SPIWrite 0411,00,0,7
SPIWrite 0412,00,0,7
SPIWrite 0413,00,0,7
SPIWrite 0414,00,0,7
SPIWrite 0415,00,0,7
SPIWrite 0416,00,0,7
SPIWrite 0417,00,0,7
SPIWrite 0418,00,0,7
SPIWrite 0419,00,0,7
SPIWrite 041a,00,0,7

SPIWrite 041b,00,0,7
SPIWrite 041c,00,0,7
SPIWrite 041d,00,0,7
SPIWrite 041e,00,0,7
SPIWrite 041f,00,0,7
SPIWrite 0420,00,0,7
SPIWrite 0421,00,0,7
SPIWrite 0422,00,0,7
SPIWrite 0423,00,0,7
SPIWrite 0424,00,0,7
SPIWrite 0425,00,0,7
SPIWrite 0426,00,0,7
SPIWrite 0427,00,0,7
SPIWrite 0428,00,0,7
SPIWrite 0429,00,0,7
SPIWrite 042a,00,0,7
SPIWrite 042b,00,0,7
SPIWrite 042c,00,0,7
SPIWrite 042d,00,0,7
SPIWrite 042e,00,0,7
SPIWrite 042f,00,0,7
SPIWrite 0430,00,0,7
SPIWrite 0431,00,0,7
SPIWrite 0432,00,0,7
SPIWrite 0433,00,0,7
SPIWrite 0434,00,0,7
SPIWrite 0435,00,0,7
SPIWrite 0436,00,0,7
SPIWrite 0437,00,0,7
SPIWrite 0438,00,0,7
SPIWrite 0439,00,0,7
SPIWrite 043a,00,0,7
SPIWrite 043b,00,0,7
SPIWrite 043c,00,0,7
SPIWrite 043d,00,0,7
SPIWrite 043e,00,0,7
SPIWrite 043f,00,0,7
SPIWrite 0440,00,0,7
SPIWrite 0441,00,0,7
SPIWrite 0442,00,0,7
SPIWrite 0443,00,0,7
SPIWrite 0444,00,0,7
SPIWrite 0445,00,0,7
SPIWrite 0446,00,0,7
SPIWrite 0447,00,0,7
SPIWrite 0448,00,0,7
SPIWrite 0449,00,0,7
SPIWrite 044a,00,0,7
SPIWrite 044b,00,0,7
SPIWrite 044c,00,0,7
SPIWrite 044d,00,0,7
SPIWrite 044e,00,0,7
SPIWrite 044f,00,0,7
SPIWrite 0450,00,0,7
SPIWrite 0451,00,0,7
SPIWrite 0452,00,0,7
SPIWrite 0453,00,0,7
SPIWrite 0454,00,0,7
SPIWrite 0455,00,0,7
SPIWrite 0456,00,0,7
SPIWrite 0457,00,0,7
SPIWrite 0458,00,0,7
SPIWrite 0459,00,0,7
SPIWrite 045a,00,0,7
SPIWrite 045b,00,0,7
SPIWrite 045c,00,0,7
SPIWrite 045d,00,0,7
SPIWrite 045e,00,0,7
SPIWrite 045f,00,0,7

SPIWrite 0460,00,0,7
SPIWrite 0461,00,0,7
SPIWrite 0462,00,0,7
SPIWrite 0463,00,0,7
SPIWrite 0464,00,0,7
SPIWrite 0465,00,0,7
SPIWrite 0466,00,0,7
SPIWrite 0467,00,0,7
SPIWrite 0468,00,0,7
SPIWrite 0469,00,0,7
SPIWrite 046a,00,0,7
SPIWrite 046b,00,0,7
SPIWrite 046c,00,0,7
SPIWrite 046d,00,0,7
SPIWrite 046e,00,0,7
SPIWrite 046f,00,0,7
SPIWrite 0470,00,0,7
SPIWrite 0471,00,0,7
SPIWrite 0472,00,0,7
SPIWrite 0473,00,0,7
SPIWrite 0474,00,0,7
SPIWrite 0475,00,0,7
SPIWrite 0476,00,0,7
SPIWrite 0477,00,0,7
SPIWrite 0478,00,0,7
SPIWrite 0479,00,0,7
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SPIWrite 0480,00,0,7
SPIWrite 0481,00,0,7
SPIWrite 0482,00,0,7
SPIWrite 0483,00,0,7
SPIWrite 0484,00,0,7
SPIWrite 0485,00,0,7
SPIWrite 0486,00,0,7
SPIWrite 0487,00,0,7
SPIWrite 0488,00,0,7
SPIWrite 0489,00,0,7
SPIWrite 048a,00,0,7
SPIWrite 048b,00,0,7
SPIWrite 048c,00,0,7
SPIWrite 048d,00,0,7
SPIWrite 048e,00,0,7
SPIWrite 048f,00,0,7
SPIWrite 0490,00,0,7
SPIWrite 0491,00,0,7
SPIWrite 0492,00,0,7
SPIWrite 0493,00,0,7
SPIWrite 0494,00,0,7
SPIWrite 0495,00,0,7
SPIWrite 0496,00,0,7
SPIWrite 0497,00,0,7
SPIWrite 0498,00,0,7
SPIWrite 0499,00,0,7
SPIWrite 049a,00,0,7
SPIWrite 049b,00,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,00,0,7
SPIWrite 04a0,00,0,7
SPIWrite 04a1,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a3,00,0,7
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SPIWrite 04a9,00,0,7
SPIWrite 04aa,00,0,7
SPIWrite 04ab,00,0,7
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SPIWrite 04ba,00,0,7
SPIWrite 04bb,00,0,7
SPIWrite 04bc,00,0,7
SPIWrite 04bd,00,0,7
SPIWrite 04be,00,0,7
SPIWrite 04bf,00,0,7
SPIWrite 04c0,00,0,7
SPIWrite 04c1,00,0,7
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SPIWrite 04ca,00,0,7
SPIWrite 04cb,00,0,7
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SPIWrite 04da,00,0,7
SPIWrite 04db,00,0,7
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SPIWrite 04e0,00,0,7
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SPIWrite 04fa,00,0,7
SPIWrite 04fb,00,0,7
SPIWrite 04fc,00,0,7
SPIWrite 04fd,00,0,7
SPIWrite 04fe,00,0,7
SPIWrite 04ff,00,0,7
SPIWrite 0500,00,0,7
SPIWrite 0501,00,0,7
SPIWrite 0502,00,0,7
SPIWrite 0503,00,0,7
SPIWrite 0504,00,0,7
SPIWrite 0505,00,0,7
SPIWrite 0506,00,0,7
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SPIWrite 0508,00,0,7
SPIWrite 0509,00,0,7
SPIWrite 050a,00,0,7
SPIWrite 050b,00,0,7
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SPIWrite 0510,00,0,7
SPIWrite 0511,00,0,7
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SPIWrite 051a,00,0,7
SPIWrite 051b,00,0,7
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SPIWrite 051f,00,0,7
SPIWrite 0520,00,0,7
SPIWrite 0521,00,0,7
SPIWrite 0522,00,0,7
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SPIWrite 0530,00,0,7
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SPIWrite 0533,00,0,7
SPIWrite 0534,00,0,7
SPIWrite 0535,00,0,7
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SPIWrite 0538,00,0,7
SPIWrite 0539,00,0,7
SPIWrite 053a,00,0,7
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SPIWrite 0540,00,0,7
SPIWrite 0541,00,0,7
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SPIWrite 0548,00,0,7
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SPIWrite 0550,00,0,7
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SPIWrite 0560,00,0,7
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SPIWrite 0569,00,0,7
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SPIWrite 0570,00,0,7
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SPIWrite 0590,00,0,7
SPIWrite 0591,00,0,7
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SPIWrite 0593,00,0,7
SPIWrite 0594,00,0,7
SPIWrite 0595,00,0,7
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SPIWrite 0599,00,0,7
SPIWrite 059a,00,0,7
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SPIWrite 059e,00,0,7
SPIWrite 059f,00,0,7
SPIWrite 05a0,00,0,7
SPIWrite 05a1,00,0,7
SPIWrite 05a2,00,0,7
SPIWrite 05a3,00,0,7
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SPIWrite 05ca,00,0,7
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SPIWrite 05ec,00,0,7
SPIWrite 05ed,00,0,7
SPIWrite 05ee,00,0,7
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SPIWrite 05fa,00,0,7
SPIWrite 05fb,00,0,7
SPIWrite 05fc,00,0,7
SPIWrite 05fd,00,0,7

SPIWrite 05fe,00,0,7
SPIWrite 05ff,00,0,7
SPIWrite 0600,00,0,7
SPIWrite 0601,00,0,7
SPIWrite 0602,00,0,7
SPIWrite 0603,00,0,7
SPIWrite 0604,00,0,7
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SPIWrite 0606,00,0,7
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SPIWrite 0608,00,0,7
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SPIWrite 0610,00,0,7
SPIWrite 0611,00,0,7
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SPIWrite 0614,00,0,7
SPIWrite 0615,00,0,7
SPIWrite 0616,00,0,7
SPIWrite 0617,00,0,7
SPIWrite 0618,00,0,7
SPIWrite 0619,00,0,7
SPIWrite 061a,00,0,7
SPIWrite 061b,00,0,7
SPIWrite 061c,00,0,7
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SPIWrite 061e,00,0,7
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SPIWrite 0620,00,0,7
SPIWrite 0621,00,0,7
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SPIWrite 0628,00,0,7
SPIWrite 0629,00,0,7
SPIWrite 062a,00,0,7
SPIWrite 062b,00,0,7
SPIWrite 062c,00,0,7
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SPIWrite 062e,00,0,7
SPIWrite 062f,00,0,7
SPIWrite 0630,00,0,7
SPIWrite 0631,00,0,7
SPIWrite 0632,00,0,7
SPIWrite 0633,00,0,7
SPIWrite 0634,00,0,7
SPIWrite 0635,00,0,7
SPIWrite 0636,00,0,7
SPIWrite 0637,00,0,7
SPIWrite 0638,00,0,7
SPIWrite 0639,00,0,7
SPIWrite 063a,00,0,7
SPIWrite 063b,00,0,7
SPIWrite 063c,00,0,7
SPIWrite 063d,00,0,7
SPIWrite 063e,00,0,7
SPIWrite 063f,00,0,7
SPIWrite 0640,00,0,7
SPIWrite 0641,00,0,7
SPIWrite 0642,00,0,7

SPIWrite 0643,00,0,7
SPIWrite 0644,00,0,7
SPIWrite 0645,00,0,7
SPIWrite 0646,00,0,7
SPIWrite 0647,00,0,7
SPIWrite 0648,00,0,7
SPIWrite 0649,00,0,7
SPIWrite 064a,00,0,7
SPIWrite 064b,00,0,7
SPIWrite 064c,00,0,7
SPIWrite 064d,00,0,7
SPIWrite 064e,00,0,7
SPIWrite 064f,00,0,7
SPIWrite 0650,00,0,7
SPIWrite 0651,00,0,7
SPIWrite 0652,00,0,7
SPIWrite 0653,00,0,7
SPIWrite 0654,00,0,7
SPIWrite 0655,00,0,7
SPIWrite 0656,00,0,7
SPIWrite 0657,00,0,7
SPIWrite 0658,00,0,7
SPIWrite 0659,00,0,7
SPIWrite 065a,00,0,7
SPIWrite 065b,00,0,7
SPIWrite 065c,00,0,7
SPIWrite 065d,00,0,7
SPIWrite 065e,00,0,7
SPIWrite 065f,00,0,7
SPIWrite 0660,00,0,7
SPIWrite 0661,00,0,7
SPIWrite 0662,00,0,7
SPIWrite 0663,00,0,7
SPIWrite 0664,00,0,7
SPIWrite 0665,00,0,7
SPIWrite 0666,00,0,7
SPIWrite 0667,00,0,7
SPIWrite 0668,00,0,7
SPIWrite 0669,00,0,7
SPIWrite 066a,00,0,7
SPIWrite 066b,00,0,7
SPIWrite 066c,00,0,7
SPIWrite 066d,00,0,7
SPIWrite 066e,00,0,7
SPIWrite 066f,00,0,7
SPIWrite 0670,00,0,7
SPIWrite 0671,00,0,7
SPIWrite 0672,00,0,7
SPIWrite 0673,00,0,7
SPIWrite 0674,00,0,7
SPIWrite 0675,00,0,7
SPIWrite 0676,00,0,7
SPIWrite 0677,00,0,7
SPIWrite 0678,00,0,7
SPIWrite 0679,00,0,7
SPIWrite 067a,00,0,7
SPIWrite 067b,00,0,7
SPIWrite 067c,00,0,7
SPIWrite 067d,00,0,7
SPIWrite 067e,00,0,7
SPIWrite 067f,00,0,7
SPIWrite 0680,00,0,7
SPIWrite 0681,00,0,7
SPIWrite 0682,00,0,7
SPIWrite 0683,00,0,7
SPIWrite 0684,00,0,7
SPIWrite 0685,00,0,7
SPIWrite 0686,00,0,7
SPIWrite 0687,00,0,7

SPIWrite 0688,00,0,7
SPIWrite 0689,00,0,7
SPIWrite 068a,00,0,7
SPIWrite 068b,00,0,7
SPIWrite 068c,00,0,7
SPIWrite 068d,00,0,7
SPIWrite 068e,00,0,7
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SPIWrite 0690,00,0,7
SPIWrite 0691,00,0,7
SPIWrite 0692,00,0,7
SPIWrite 0693,00,0,7
SPIWrite 0694,00,0,7
SPIWrite 0695,00,0,7
SPIWrite 0696,00,0,7
SPIWrite 0697,00,0,7
SPIWrite 0698,00,0,7
SPIWrite 0699,00,0,7
SPIWrite 069a,00,0,7
SPIWrite 069b,00,0,7
SPIWrite 069c,00,0,7
SPIWrite 069d,00,0,7
SPIWrite 069e,00,0,7
SPIWrite 069f,00,0,7
SPIWrite 06a0,00,0,7
SPIWrite 06a1,00,0,7
SPIWrite 06a2,00,0,7
SPIWrite 06a3,00,0,7
SPIWrite 06a4,00,0,7
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SPIWrite 06fb,00,0,7
SPIWrite 06fc,00,0,7
SPIWrite 06fd,00,0,7
SPIWrite 06fe,00,0,7
SPIWrite 06ff,00,0,7
SPIWrite 0700,00,0,7
SPIWrite 0701,00,0,7
SPIWrite 0702,00,0,7
SPIWrite 0703,00,0,7
SPIWrite 0704,00,0,7
SPIWrite 0705,00,0,7
SPIWrite 0706,00,0,7
SPIWrite 0707,00,0,7
SPIWrite 0708,00,0,7
SPIWrite 0709,00,0,7
SPIWrite 070a,00,0,7
SPIWrite 070b,00,0,7
SPIWrite 070c,00,0,7
SPIWrite 070d,00,0,7
SPIWrite 070e,00,0,7
SPIWrite 070f,00,0,7
SPIWrite 0710,00,0,7
SPIWrite 0711,00,0,7

SPIWrite 0712,00,0,7
SPIWrite 0713,00,0,7
SPIWrite 0714,00,0,7
SPIWrite 0715,00,0,7
SPIWrite 0716,00,0,7
SPIWrite 0717,00,0,7
SPIWrite 0718,00,0,7
SPIWrite 0719,00,0,7
SPIWrite 071a,00,0,7
SPIWrite 071b,00,0,7
SPIWrite 071c,00,0,7
SPIWrite 071d,00,0,7
SPIWrite 071e,00,0,7
SPIWrite 071f,00,0,7
SPIWrite 0720,00,0,7
SPIWrite 0721,00,0,7
SPIWrite 0722,00,0,7
SPIWrite 0723,00,0,7
SPIWrite 0724,00,0,7
SPIWrite 0725,00,0,7
SPIWrite 0726,00,0,7
SPIWrite 0727,00,0,7
SPIWrite 0728,00,0,7
SPIWrite 0729,00,0,7
SPIWrite 072a,00,0,7
SPIWrite 072b,00,0,7
SPIWrite 072c,00,0,7
SPIWrite 072d,00,0,7
SPIWrite 072e,00,0,7
SPIWrite 072f,00,0,7
SPIWrite 0730,00,0,7
SPIWrite 0731,00,0,7
SPIWrite 0732,00,0,7
SPIWrite 0733,00,0,7
SPIWrite 0734,00,0,7
SPIWrite 0735,00,0,7
SPIWrite 0736,00,0,7
SPIWrite 0737,00,0,7
SPIWrite 0738,00,0,7
SPIWrite 0739,00,0,7
SPIWrite 073a,00,0,7
SPIWrite 073b,00,0,7
SPIWrite 073c,00,0,7
SPIWrite 073d,00,0,7
SPIWrite 073e,00,0,7
SPIWrite 073f,00,0,7
SPIWrite 0740,00,0,7
SPIWrite 0741,00,0,7
SPIWrite 0742,00,0,7
SPIWrite 0743,00,0,7
SPIWrite 0744,00,0,7
SPIWrite 0745,00,0,7
SPIWrite 0746,00,0,7
SPIWrite 0747,00,0,7
SPIWrite 0748,00,0,7
SPIWrite 0749,00,0,7
SPIWrite 074a,00,0,7
SPIWrite 074b,00,0,7
SPIWrite 074c,00,0,7
SPIWrite 074d,00,0,7
SPIWrite 074e,00,0,7
SPIWrite 074f,00,0,7
SPIWrite 0750,00,0,7
SPIWrite 0751,00,0,7
SPIWrite 0752,00,0,7
SPIWrite 0753,00,0,7
SPIWrite 0754,00,0,7
SPIWrite 0755,00,0,7
SPIWrite 0756,00,0,7

SPIWrite 0757,00,0,7
SPIWrite 0758,00,0,7
SPIWrite 0759,00,0,7
SPIWrite 075a,00,0,7
SPIWrite 075b,00,0,7
SPIWrite 075c,00,0,7
SPIWrite 075d,00,0,7
SPIWrite 075e,00,0,7
SPIWrite 075f,00,0,7
SPIWrite 0760,00,0,7
SPIWrite 0761,00,0,7
SPIWrite 0762,00,0,7
SPIWrite 0763,00,0,7
SPIWrite 0764,00,0,7
SPIWrite 0765,00,0,7
SPIWrite 0766,00,0,7
SPIWrite 0767,00,0,7
SPIWrite 0768,00,0,7
SPIWrite 0769,00,0,7
SPIWrite 076a,00,0,7
SPIWrite 076b,00,0,7
SPIWrite 076c,00,0,7
SPIWrite 076d,00,0,7
SPIWrite 076e,00,0,7
SPIWrite 076f,00,0,7
SPIWrite 0770,00,0,7
SPIWrite 0771,00,0,7
SPIWrite 0772,00,0,7
SPIWrite 0773,00,0,7
SPIWrite 0774,00,0,7
SPIWrite 0775,00,0,7
SPIWrite 0776,00,0,7
SPIWrite 0777,00,0,7
SPIWrite 0778,00,0,7
SPIWrite 0779,00,0,7
SPIWrite 077a,00,0,7
SPIWrite 077b,00,0,7
SPIWrite 077c,00,0,7
SPIWrite 077d,00,0,7
SPIWrite 077e,00,0,7
SPIWrite 077f,00,0,7
SPIWrite 0780,00,0,7
SPIWrite 0781,00,0,7
SPIWrite 0782,00,0,7
SPIWrite 0783,00,0,7
SPIWrite 0784,00,0,7
SPIWrite 0785,00,0,7
SPIWrite 0786,00,0,7
SPIWrite 0787,00,0,7
SPIWrite 0788,00,0,7
SPIWrite 0789,00,0,7
SPIWrite 078a,00,0,7
SPIWrite 078b,00,0,7
SPIWrite 078c,00,0,7
SPIWrite 078d,00,0,7
SPIWrite 078e,00,0,7
SPIWrite 078f,00,0,7
SPIWrite 0790,00,0,7
SPIWrite 0791,00,0,7
SPIWrite 0792,00,0,7
SPIWrite 0793,00,0,7
SPIWrite 0794,00,0,7
SPIWrite 0795,00,0,7
SPIWrite 0796,00,0,7
SPIWrite 0797,00,0,7
SPIWrite 0798,00,0,7
SPIWrite 0799,00,0,7
SPIWrite 079a,00,0,7
SPIWrite 079b,00,0,7

SPIWrite 079c,00,0,7
SPIWrite 079d,00,0,7
SPIWrite 079e,00,0,7
SPIWrite 079f,00,0,7
SPIWrite 07a0,00,0,7
SPIWrite 07a1,00,0,7
SPIWrite 07a2,00,0,7
SPIWrite 07a3,00,0,7
SPIWrite 07a4,00,0,7
SPIWrite 07a5,00,0,7
SPIWrite 07a6,00,0,7
SPIWrite 07a7,00,0,7
SPIWrite 07a8,00,0,7
SPIWrite 07a9,00,0,7
SPIWrite 07aa,00,0,7
SPIWrite 07ab,00,0,7
SPIWrite 07ac,00,0,7
SPIWrite 07ad,00,0,7
SPIWrite 07ae,00,0,7
SPIWrite 07af,00,0,7
SPIWrite 07b0,00,0,7
SPIWrite 07b1,00,0,7
SPIWrite 07b2,00,0,7
SPIWrite 07b3,00,0,7
SPIWrite 07b4,00,0,7
SPIWrite 07b5,00,0,7
SPIWrite 07b6,00,0,7
SPIWrite 07b7,00,0,7
SPIWrite 07b8,00,0,7
SPIWrite 07b9,00,0,7
SPIWrite 07ba,00,0,7
SPIWrite 07bb,00,0,7
SPIWrite 07bc,00,0,7
SPIWrite 07bd,00,0,7
SPIWrite 07be,00,0,7
SPIWrite 07bf,00,0,7
SPIWrite 07c0,00,0,7
SPIWrite 07c1,00,0,7
SPIWrite 07c2,00,0,7
SPIWrite 07c3,00,0,7
SPIWrite 07c4,00,0,7
SPIWrite 07c5,00,0,7
SPIWrite 07c6,00,0,7
SPIWrite 07c7,00,0,7
SPIWrite 07c8,00,0,7
SPIWrite 07c9,00,0,7
SPIWrite 07ca,00,0,7
SPIWrite 07cb,00,0,7
SPIWrite 07cc,00,0,7
SPIWrite 07cd,00,0,7
SPIWrite 07ce,00,0,7
SPIWrite 07cf,00,0,7
SPIWrite 07d0,00,0,7
SPIWrite 07d1,00,0,7
SPIWrite 07d2,00,0,7
SPIWrite 07d3,00,0,7
SPIWrite 07d4,00,0,7
SPIWrite 07d5,00,0,7
SPIWrite 07d6,00,0,7
SPIWrite 07d7,00,0,7
SPIWrite 07d8,00,0,7
SPIWrite 07d9,00,0,7
SPIWrite 07da,00,0,7
SPIWrite 07db,00,0,7
SPIWrite 07dc,00,0,7
SPIWrite 07dd,00,0,7
SPIWrite 07de,00,0,7
SPIWrite 07df,00,0,7
SPIWrite 07e0,00,0,7

```
SPIWrite 07e1,00,0,7
SPIWrite 07e2,00,0,7
SPIWrite 07e3,00,0,7
SPIWrite 07e4,00,0,7
SPIWrite 07e5,00,0,7
SPIWrite 07e6,00,0,7
SPIWrite 07e7,00,0,7
SPIWrite 07e8,00,0,7
SPIWrite 07e9,00,0,7
SPIWrite 07ea,00,0,7
SPIWrite 07eb,00,0,7
SPIWrite 07ec,00,0,7
SPIWrite 07ed,00,0,7
SPIWrite 07ee,00,0,7
SPIWrite 07ef,00,0,7
SPIWrite 07f0,00,0,7
SPIWrite 07f1,00,0,7
SPIWrite 07f2,00,0,7
SPIWrite 07f3,00,0,7
SPIWrite 07f4,00,0,7
SPIWrite 07f5,00,0,7
SPIWrite 07f6,00,0,7
SPIWrite 07f7,00,0,7
SPIWrite 07f8,00,0,7
SPIWrite 07f9,00,0,7
SPIWrite 07fa,00,0,7
SPIWrite 07fb,00,0,7
SPIWrite 07fc,00,0,7
SPIWrite 07fd,00,0,7
SPIWrite 07fe,00,0,7
SPIWrite 07ff,00,0,7
SPIWrite 0800,00,0,7
SPIWrite 0801,00,0,7
SPIWrite 0802,00,0,7
SPIWrite 0803,00,0,7
SPIWrite 0804,00,0,7
SPIWrite 0805,00,0,7
SPIWrite 0806,00,0,7
SPIWrite 0807,00,0,7
SPIWrite 0808,00,0,7
SPIWrite 0809,00,0,7
SPIWrite 080a,00,0,7
SPIWrite 080b,00,0,7
SPIWrite 080c,00,0,7
SPIWrite 080d,00,0,7
SPIWrite 080e,00,0,7
SPIWrite 080f,00,0,7
SPIWrite 0810,00,0,7
SPIWrite 0811,00,0,7
SPIWrite 0812,00,0,7
SPIWrite 0813,00,0,7
SPIWrite 0814,00,0,7
SPIWrite 0815,00,0,7
SPIWrite 0816,00,0,7
SPIWrite 0817,00,0,7
SPIWrite 0818,00,0,7
SPIWrite 0819,00,0,7
SPIWrite 081a,00,0,7
SPIWrite 081b,00,0,7
SPIWrite 081c,00,0,7
SPIWrite 081d,00,0,7
SPIWrite 081e,00,0,7
SPIWrite 081f,00,0,7
SPIWrite 0012,00,4,4 //PAGE: sys_calib_macro_memory=0x0; (Meaning: ); Address(0x12[4:4],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1; (Meaning: ); Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,10,0,7
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SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,10,0,7
SPIWrite 00a4,00,0,7
SPIWrite 0193,81,0,7

SPIPoll 00f0,1,1,1

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0; (Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)

\\Read  MACRO_READY=0x1;    Address(0x20[0:0],)

\\Read  MACRO_ACK=0x1;    Address(0x20[1:1],)

\\Read  MACRO_DONE=0x1;    Address(0x20[2:2],)

SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1; (Meaning: );    Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,10,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,01,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7
SPIWrite 0193,81,0,7

SPIPoll 00f0,1,1,1

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0; (Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)

\\Read  MACRO_READY=0x1;    Address(0x20[0:0],)

\\Read  MACRO_ACK=0x1;    Address(0x20[1:1],)

\\Read  MACRO_DONE=0x1;    Address(0x20[2:2],)

SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

SPIWrite 0013,00,0,7    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
\\ STEP: rstMCU/step2

\\START: Loading PLL EFuse trims
```

```

SPIWrite 0013,10,0,7    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0144,00,0,7
SPIWrite 0144,00,0,7
SPIWrite 0013,00,0,7    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,02,0,7    //PAGE: cm4top_dram=0x1;(Meaning: );    Address(0x13[1:1],)
SPIWrite 3f10,20,0,7
SPIWrite 3f12,ce,0,7
SPIWrite 3f13,ff,0,7
SPIWrite 3f14,7f,0,7
SPIWrite 3f15,00,0,7
SPIWrite 3ee6,01,0,7
SPIWrite 3f64,01,0,7
SPIWrite 3f65,01,0,7
SPIWrite 3f66,01,0,7
SPIWrite 3f67,01,0,7
SPIWrite 0013,00,0,7    //PAGE: cm4top_dram=0x0;(Meaning: );    Address(0x13[1:1],)
SPIWrite 0013,10,0,7    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,01,0,7
SPIWrite 0193,25,0,7

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,0,7    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,0,7    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

SPIWrite 0013,00,0,7    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
SPIWrite 0013,10,0,7    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7
SPIWrite 0193,12,0,7

SPIPoll 00f0,0,7,7

\\END: Done Loading PLL EFuse trims

\\START: Configuring TOP parameters for MCU

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,03,0,7
SPIWrite 00a1,1f,0,7
SPIWrite 00a0,1f,0,7
SPIWrite 0193,21,0,7

\\Read  macro_opcode_operand=0x21000000;    Address(0x190[31:0],)

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```
SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;    Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;    Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;    Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;    Address(0x20[7:7],)

SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 0193,25,0,7

\\Read  macro_opcode_operand=0x25000000;    Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;    Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;    Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;    Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;    Address(0x20[7:7],)

SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,01,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,00,0,7
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SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7
SPIWrite 0193,26,0,7

\\Read macro_opcode_operand=0x26000000; Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0; (Meaning:); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address(0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address(0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address(0x20[6:6],)

\\Read MACRO_ERROR_IN_EXECUTION=0x0; Address(0x20[7:7],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1; (Meaning:); Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7
SPIWrite 00ab,00,0,7
SPIWrite 00aa,00,0,7
SPIWrite 00a9,00,0,7
SPIWrite 00a8,00,0,7
SPIWrite 00af,00,0,7
SPIWrite 00ae,00,0,7
SPIWrite 00ad,00,0,7
SPIWrite 00ac,00,0,7
SPIWrite 00b3,00,0,7
SPIWrite 00b2,1c,0,7
SPIWrite 00b1,20,0,7
SPIWrite 00b0,00,0,7
SPIWrite 00b7,00,0,7
SPIWrite 00b6,20,0,7
SPIWrite 00b5,a6,0,7
SPIWrite 00b4,c0,0,7
SPIWrite 00bb,00,0,7
SPIWrite 00ba,20,0,7
SPIWrite 00b9,a6,0,7
SPIWrite 00b8,c0,0,7
SPIWrite 00bf,00,0,7
SPIWrite 00be,20,0,7
SPIWrite 00bd,a6,0,7
SPIWrite 00bc,c0,0,7

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SPIWrite 0193,24,0,7

\\Read  macro_opcode_operand=0x24000000;    Address(0x190[31:0],)

SIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0; (Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;    Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;    Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;    Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;    Address(0x20[7:7],)

\\END: Done configuring TOP parameters for MCU

SPIWrite 0013,00,0,7    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
\\ STEP: efusebyMCU/step0

\\START: Loading PLL EFuse trims

SPIWrite 0013,10,0,7    //PAGE: customer_macro=0x1; (Meaning: );    Address(0x13[4:4],)

SIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,1f,0,7
SPIWrite 00a1,7c,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,09,0,7
SPIWrite 00a6,08,0,7
SPIWrite 00a5,02,0,7
SPIWrite 00a4,00,0,7
SPIWrite 0193,12,0,7

SIPoll 00f0,0,7,7

\\END: Done Loading PLL EFuse trims

SPIWrite 0013,00,0,7    //PAGE: customer_macro=0x0; (Meaning: );    Address(0x13[4:4],)
SPIWrite 0014,04,0,7    //PAGE: TIMING_CON=0x1; (Meaning: );    Address(0x14[2:2],)
SPIWrite 0717,00,0,7    //spare_reg_port=0x101;    Address(0x714[31:0],)
SPIWrite 0716,00,0,7
SPIWrite 0715,01,0,7
SPIWrite 0714,01,0,7
SPIWrite 0014,00,0,7    //PAGE: TIMING_CON=0x0; (Meaning: );    Address(0x14[2:2],)
\\ STEP: pll0Config/step0

\\START: Requesting/releasing SPI Access to PLL Pages

SPIWrite 0015,80,0,7    //PAGE: DIGTOP_MISC=0x1; (Meaning: );    Address(0x15[7:7],)
SPIWrite 01d4,01,0,7    //pll_reg_spi_req_a=0x1;    Address(0x1d4[0:0],)
SPIWrite 0374,00,0,7    //pll_reg_spi_req_b1=0x0; (Meaning: );    Address(0x374[0:0],)

```

SPIPoll 01d5,0,0,1

\\Read pll_reg_spi_a_ack=0x1; Address(0x1d4[8:8],)

\\END: Done requesting/releasing SPI Access to PLL Pages

SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning:); Address(0x15[7:7],)

\\ STEP: pll0Config/step1

\\START: Configure PLL

SPIWrite 0014,08,0,7 //PAGE: pll=0x1; Address(0x14[7:3],)
SPIWrite 004b,33,0,7 //PLL_SPARE0=0xcc000; Address(0x48[31:10],)
SPIWrite 004a,00,0,7
SPIWrite 0049,00,0,7
SPIWrite 005e,97,0,7 //EN_VCO=0x1; Address(0x5c[23:23],)
SPIWrite 0028,0f,0,7 //N=0xf; Address(0x28[7:0],)
SPIWrite 0036,0f,0,7 //N_to_CAL=0xf; Address(0x34[23:16],)
SPIWrite 0035,08,0,7 //CAL_CONTROL=0x8; Address(0x34[12:8],)
SPIWrite 0051,06,0,7 //CTL_REF_DIV=0x1; (Meaning:); Address(0x50[8:6],)
SPIWrite 0050,40,0,7
SPIWrite 0051,06,0,7 //EN_DIV8_CLK_TO_PLLDIG=0x1; Address(0x50[9:9],)
SPIWrite 0051,06,0,7 //EN_CLK_TO_PLLDIG=0x1; Address(0x50[10:10],)
SPIWrite 0051,0e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 003c,40,0,7 //CTL_DIV_23_45=0x0; Address(0x3c[0:0],)
SPIWrite 005e,96,0,7 //EN_FRAC_N_MODE=0x0; Address(0x5c[16:16],)
SPIWrite 0033,02,0,7 //EN_FRAC_MODE=0x0; Address(0x30[24:24],)
SPIWrite 003f,0c,0,7 //CTL_TEST_MUX=0x0; Address(0x3c[31:30],)
SPIWrite 0049,01,0,7 //EN_VCO_PKDET=0x1; Address(0x48[8:8],)
SPIWrite 0048,2f,0,7 //VCO_PKDET_BIAS=0x7; Address(0x48[2:0],)
SPIWrite 0044,27,0,7 //CTL_VCO_IB_GM=0x7; Address(0x44[2:0],)
SPIWrite 0043,4c,0,7 //CTL_VCO_IB_TAIL=0xc; Address(0x40[27:24],)
SPIWrite 0043,0c,0,7 //CTL_VCO_IB_GM_SLOPE=0x0; Address(0x40[30:28],)
SPIWrite 0042,04,0,7 //CTL_VCO_IB_TAIL_SLOPE=0x0; Address(0x40[22:19],)
SPIWrite 0046,00,0,7 //CTL_VCO_IB_GM1=0x0; Address(0x44[22:21],)
SPIWrite 0040,28,0,7 //CTL_VB_VAR_SLOPE=0x0; Address(0x40[2:0],)
SPIWrite 002e,00,0,7 //F=0x0; Address(0x2c[19:0],)
SPIWrite 002d,00,0,7
SPIWrite 002c,00,0,7
SPIWrite 0032,03,0,7 //D=0x3c000; Address(0x30[19:0],)
SPIWrite 0031,c0,0,7
SPIWrite 0030,00,0,7
SPIWrite 0033,02,0,7 //F_order=0x1; Address(0x30[27:25],)
SPIWrite 0039,40,0,7 //EN_LOCK_DETECT=0x1; Address(0x38[14:14],)
SPIWrite 003f,14,0,7 //CTL_PFD_DEL=0x2; Address(0x3c[29:27],)
SPIWrite 003f,12,0,7 //CTL_OUT_DIV=0x2; Address(0x3c[26:24],)
SPIWrite 003c,40,0,7 //CTL_LDO_ANA=0x4; Address(0x3c[6:4],)
SPIWrite 003d,24,0,7 //CTL_LDO_RF=0x4; Address(0x3c[10:8],)
SPIWrite 0052,10,0,7 //CTL_FBDIV_LDO_PROG=0x8; Address(0x50[20:17],)
SPIWrite 003a,08,0,7 //CTL_CP_IREF=0x2; Address(0x38[20:18],)
SPIWrite 003b,30,0,7 //CTL_CP_IOUT=0x3; Address(0x38[29:28],)
SPIWrite 0034,03,0,7 //lock_acc=0x3; Address(0x34[1:0],)
SPIWrite 0034,0f,0,7 //lock_cnt=0x3; Address(0x34[3:2],)
SPIWrite 0034,1f,0,7 //lock_err=0x1; Address(0x34[5:4],)
SPIWrite 0066,04,0,7 //lock_rst=0x1; Address(0x64[18:18],)
SPIWrite 0066,00,0,7 //lock_rst=0x0; Address(0x64[18:18],)
SPIWrite 0066,08,0,7 //lock_lost_rst=0x1; Address(0x64[19:19],)
SPIWrite 0066,00,0,7 //lock_lost_rst=0x0; Address(0x64[19:19],)
SPIWrite 0034,1f,0,7 //EN_CAL=0x0; Address(0x34[6:6],)
SPIWrite 0034,5f,0,7 //EN_CAL=0x1; Address(0x34[6:6],)
SPIWrite 003c,44,0,7 //CTL_I_OFFSET=0x1; Address(0x3c[3:2],)

WAIT 0.01

SPIWrite 005e,96,0,7 //EN_I_OFFSET=0x0; Address(0x5c[19:19],)

SPIPoll 0062,7,7,1

SPIReadCheck 0062,7,7,80

\\Read lock=0x1; Address(0x60[23:23],)

\\END: Done configuring PLL

SPIWrite 0014,00,0,7 //PAGE: pll=0x0; Address(0x14[7:3],)

\\ STEP: pll1Config/step0

\\START: Configure PLL

SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)
SPIWrite 005e,97,0,7 //EN_VCO=0x1; Address(0x5c[23:23],)
SPIWrite 0028,12,0,7 //N=0x12; Address(0x28[7:0],)
SPIWrite 0036,12,0,7 //N_to_CAL=0x12; Address(0x34[23:16],)
SPIWrite 0035,08,0,7 //CAL_CONTROL=0x8; Address(0x34[12:8],)
SPIWrite 0051,06,0,7 //CTL_REF_DIV=0x1; (Meaning:); Address(0x50[8:6],)
SPIWrite 0050,40,0,7
SPIWrite 0051,06,0,7 //EN_DIV8_CLK_TO_PLLDIG=0x1; Address(0x50[9:9],)
SPIWrite 0051,06,0,7 //EN_CLK_TO_PLLDIG=0x1; Address(0x50[10:10],)
SPIWrite 0051,0e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 003c,40,0,7 //CTL_DIV_23_45=0x0; Address(0x3c[0:0],)
SPIWrite 005e,96,0,7 //EN_FRAC_N_MODE=0x0; Address(0x5c[16:16],)
SPIWrite 0033,02,0,7 //EN_FRAC_MODE=0x0; Address(0x30[24:24],)
SPIWrite 003f,0c,0,7 //CTL_TEST_MUX=0x0; Address(0x3c[31:30],)
SPIWrite 0049,01,0,7 //EN_VCO_PKDET=0x1; Address(0x48[8:8],)
SPIWrite 0048,2f,0,7 //VCO_PKDT_BIAS=0x7; Address(0x48[2:0],)
SPIWrite 0044,27,0,7 //CTL_VCO_IB_GM=0x7; Address(0x44[2:0],)
SPIWrite 0043,4c,0,7 //CTL_VCO_IB_TAIL=0xc; Address(0x40[27:24],)
SPIWrite 0043,0c,0,7 //CTL_VCO_IB_GM_SLOPE=0x0; Address(0x40[30:28],)
SPIWrite 0042,04,0,7 //CTL_VCO_IB_TAIL_SLOPE=0x0; Address(0x40[22:19],)
SPIWrite 0046,00,0,7 //CTL_VCO_IB_GM1=0x0; Address(0x44[22:21],)
SPIWrite 0040,28,0,7 //CTL_VB_VAR_SLOPE=0x0; Address(0x40[2:0],)
SPIWrite 002e,00,0,7 //F=0x0; Address(0x2c[19:0],)
SPIWrite 002d,00,0,7
SPIWrite 002c,00,0,7
SPIWrite 0032,02,0,7 //D=0x20000; Address(0x30[19:0],)
SPIWrite 0031,00,0,7
SPIWrite 0030,00,0,7
SPIWrite 0033,02,0,7 //F_order=0x1; Address(0x30[27:25],)
SPIWrite 0039,40,0,7 //EN_LOCK_DETECT=0x1; Address(0x38[14:14],)
SPIWrite 003f,14,0,7 //CTL_PFD_DEL=0x2; Address(0x3c[29:27],)
SPIWrite 003f,13,0,7 //CTL_OUT_DIV=0x3; Address(0x3c[26:24],)
SPIWrite 003c,40,0,7 //CTL_LDO_ANA=0x4; Address(0x3c[6:4],)
SPIWrite 003d,24,0,7 //CTL_LDO_RF=0x4; Address(0x3c[10:8],)
SPIWrite 0052,10,0,7 //CTL_FBDIV_LDO_PROG=0x8; Address(0x50[20:17],)
SPIWrite 003a,08,0,7 //CTL_CP_IREF=0x2; Address(0x38[20:18],)
SPIWrite 003b,30,0,7 //CTL_CP_IOUT=0x3; Address(0x38[29:28],)
SPIWrite 0034,03,0,7 //lock_acc=0x3; Address(0x34[1:0],)
SPIWrite 0034,0f,0,7 //lock_cnt=0x3; Address(0x34[3:2],)
SPIWrite 0034,1f,0,7 //lock_err=0x1; Address(0x34[5:4],)
SPIWrite 0066,04,0,7 //lock_rst=0x1; Address(0x64[18:18],)
SPIWrite 0066,00,0,7 //lock_rst=0x0; Address(0x64[18:18],)
SPIWrite 0066,08,0,7 //lock_lost_rst=0x1; Address(0x64[19:19],)
SPIWrite 0066,00,0,7 //lock_lost_rst=0x0; Address(0x64[19:19],)
SPIWrite 0034,1f,0,7 //EN_CAL=0x0; Address(0x34[6:6],)
SPIWrite 0034,5f,0,7 //EN_CAL=0x1; Address(0x34[6:6],)

WAIT 0.01

SPIWrite 005e,96,0,7 //EN_I_OFFSET=0x0; Address(0x5c[19:19],)

SPIPoll 0062,7,7,1

SPIReadCheck 0062,7,7,80

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\\Read  lock=0x1;  Address(0x60[23:23],)

SPIWrite 0051,0c,0,7  //EN_DIV8_CLK_TO_PLLDIG=0x0;  Address(0x50[9:9],)

\\END: Done configuring PLL

SPIWrite 0014,00,0,7  //PAGE: pll=0x0;  Address(0x14[7:3],)
\\ STEP: pll2Config/step0

\\START: Configure PLL

SPIWrite 0014,20,0,7  //PAGE: pll=0x4;  Address(0x14[7:3],)
SPIWrite 004b,33,0,7  //PLL_SPARE0=0xcc000;  Address(0x48[31:10],)
SPIWrite 004a,00,0,7
SPIWrite 0049,00,0,7
SPIWrite 005e,97,0,7  //EN_VCO=0x1;  Address(0x5c[23:23],)
SPIWrite 0028,11,0,7  //N=0x11;  Address(0x28[7:0],)
SPIWrite 0036,11,0,7  //N_to_CAL=0x11;  Address(0x34[23:16],)
SPIWrite 0035,08,0,7  //CAL_CONTROL=0x8;  Address(0x34[12:8],)
SPIWrite 0051,06,0,7  //CTL_REF_DIV=0x1; (Meaning: );  Address(0x50[8:6],)
SPIWrite 0050,40,0,7
SPIWrite 0051,06,0,7  //EN_DIV8_CLK_TO_PLLDIG=0x1;  Address(0x50[9:9],)
SPIWrite 0051,06,0,7  //EN_CLK_TO_PLLDIG=0x1;  Address(0x50[10:10],)
SPIWrite 0051,0e,0,7  //EN_SYNC_TO_PLLDIG_DIV=0x1;  Address(0x50[11:11],)
SPIWrite 003c,40,0,7  //CTL_DIV_23_45=0x0;  Address(0x3c[0:0],)
SPIWrite 005e,97,0,7  //EN_FRAC_N_MODE=0x1;  Address(0x5c[16:16],)
SPIWrite 0033,03,0,7  //EN_FRAC_MODE=0x1;  Address(0x30[24:24],)
SPIWrite 003f,0c,0,7  //CTL_TEST_MUX=0x0;  Address(0x3c[31:30],)
SPIWrite 0049,01,0,7  //EN_VCO_PKDET=0x1;  Address(0x48[8:8],)
SPIWrite 0048,2f,0,7  //VCO_PKDT_BIAS=0x7;  Address(0x48[2:0],)
SPIWrite 0044,27,0,7  //CTL_VCO_IB_GM=0x7;  Address(0x44[2:0],)
SPIWrite 0043,4c,0,7  //CTL_VCO_IB_TAIL=0xc;  Address(0x40[27:24],)
SPIWrite 0043,0c,0,7  //CTL_VCO_IB_GM_SLOPE=0x0;  Address(0x40[30:28],)
SPIWrite 0042,04,0,7  //CTL_VCO_IB_TAIL_SLOPE=0x0;  Address(0x40[22:19],)
SPIWrite 0046,00,0,7  //CTL_VCO_IB_GM1=0x0;  Address(0x44[22:21],)
SPIWrite 0040,28,0,7  //CTL_VB_VAR_SLOPE=0x0;  Address(0x40[2:0],)
SPIWrite 002e,01,0,7  //F=0x18d80;  Address(0x2c[19:0],)
SPIWrite 002d,8d,0,7
SPIWrite 002c,80,0,7
SPIWrite 0032,03,0,7  //D=0x3c000;  Address(0x30[19:0],)
SPIWrite 0031,c0,0,7
SPIWrite 0030,00,0,7
SPIWrite 0033,03,0,7  //F_order=0x1;  Address(0x30[27:25],)
SPIWrite 0039,40,0,7  //EN_LOCK_DETECT=0x1;  Address(0x38[14:14],)
SPIWrite 003f,14,0,7  //CTL_PFD_DEL=0x2;  Address(0x3c[29:27],)
SPIWrite 003f,12,0,7  //CTL_OUT_DIV=0x2;  Address(0x3c[26:24],)
SPIWrite 003c,40,0,7  //CTL_LDO_ANA=0x4;  Address(0x3c[6:4],)
SPIWrite 003d,24,0,7  //CTL_LDO_RF=0x4;  Address(0x3c[10:8],)
SPIWrite 0052,10,0,7  //CTL_FBDIV_LDO_PROG=0x8;  Address(0x50[20:17],)
SPIWrite 003a,08,0,7  //CTL_CP_IREF=0x2;  Address(0x38[20:18],)
SPIWrite 003b,30,0,7  //CTL_CP_IOUT=0x3;  Address(0x38[29:28],)
SPIWrite 0034,03,0,7  //lock_acc=0x3;  Address(0x34[1:0],)
SPIWrite 0034,0f,0,7  //lock_cnt=0x3;  Address(0x34[3:2],)
SPIWrite 0034,1f,0,7  //lock_err=0x1;  Address(0x34[5:4],)
SPIWrite 0066,04,0,7  //lock_rst=0x1;  Address(0x64[18:18],)
SPIWrite 0066,00,0,7  //lock_rst=0x0;  Address(0x64[18:18],)
SPIWrite 0066,08,0,7  //lock_lost_rst=0x1;  Address(0x64[19:19],)
SPIWrite 0066,00,0,7  //lock_lost_rst=0x0;  Address(0x64[19:19],)
SPIWrite 0034,1f,0,7  //EN_CAL=0x0;  Address(0x34[6:6],)
SPIWrite 0034,5f,0,7  //EN_CAL=0x1;  Address(0x34[6:6],)

WAIT 0.01

SPIWrite 005e,9f,0,7  //EN_I_OFFSET=0x1;  Address(0x5c[19:19],)
SPIWrite 003c,40,0,7  //CTL_I_OFST_UP_DN=0x0;  Address(0x3c[1:1],)
SPIWrite 0065,10,0,7  //CTL_I_OFST_REF=0x1;  Address(0x64[14:12],)
SPIWrite 003c,48,0,7  //CTL_I_OFFSET=0x2;  Address(0x3c[3:2],)

SPIPoll 0062,7,7,1

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SPIReadCheck 0062,7,7,80

\\Read lock=0x1; Address(0x60[23:23],)

\\END: Done configuring PLL

SPIWrite 0014,00,0,7 //PAGE: pll=0x0; Address(0x14[7:3],)
\\ STEP: pll3Config/step0

\\START: Configure PLL

SPIWrite 0014,40,0,7 //PAGE: pll=0x8; Address(0x14[7:3],)
SPIWrite 004b,33,0,7 //PLL_SPARE0=0xcc000; Address(0x48[31:10],)
SPIWrite 004a,00,0,7
SPIWrite 0049,00,0,7
SPIWrite 005e,97,0,7 //EN_VCO=0x1; Address(0x5c[23:23],)
SPIWrite 0028,0e,0,7 //N=0xe; Address(0x28[7:0],)
SPIWrite 0036,0e,0,7 //N_to_CAL=0xe; Address(0x34[23:16],)
SPIWrite 0035,08,0,7 //CAL_CONTROL=0x8; Address(0x34[12:8],)
SPIWrite 0051,06,0,7 //CTL_REF_DIV=0x1; (Meaning:); Address(0x50[8:6],)
SPIWrite 0050,40,0,7
SPIWrite 0051,06,0,7 //EN_DIV8_CLK_TO_PLLDIG=0x1; Address(0x50[9:9],)
SPIWrite 0051,06,0,7 //EN_CLK_TO_PLLDIG=0x1; Address(0x50[10:10],)
SPIWrite 0051,0e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 003c,41,0,7 //CTL_DIV_23_45=0x1; Address(0x3c[0:0],)
SPIWrite 005e,97,0,7 //EN_FRAC_N_MODE=0x1; Address(0x5c[16:16],)
SPIWrite 0033,03,0,7 //EN_FRAC_MODE=0x1; Address(0x30[24:24],)
SPIWrite 003f,0c,0,7 //CTL_TEST_MUX=0x0; Address(0x3c[31:30],)
SPIWrite 0049,01,0,7 //EN_VCO_PKDET=0x1; Address(0x48[8:8],)
SPIWrite 0048,2f,0,7 //VCO_PKDT_BIAS=0x7; Address(0x48[2:0],)
SPIWrite 0044,27,0,7 //CTL_VCO_IB_GM=0x7; Address(0x44[2:0],)
SPIWrite 0043,4c,0,7 //CTL_VCO_IB_TAIL=0xc; Address(0x40[27:24],)
SPIWrite 0043,0c,0,7 //CTL_VCO_IB_GM_SLOPE=0x0; Address(0x40[30:28],)
SPIWrite 0042,04,0,7 //CTL_VCO_IB_TAIL_SLOPE=0x0; Address(0x40[22:19],)
SPIWrite 0046,00,0,7 //CTL_VCO_IB_GM1=0x0; Address(0x44[22:21],)
SPIWrite 0040,28,0,7 //CTL_VB_VAR_SLOPE=0x0; Address(0x40[2:0],)
SPIWrite 002e,00,0,7 //F=0xd3e0; Address(0x2c[19:0],)
SPIWrite 002d,d3,0,7
SPIWrite 002c,e0,0,7
SPIWrite 0032,03,0,7 //D=0x3c000; Address(0x30[19:0],)
SPIWrite 0031,c0,0,7
SPIWrite 0030,00,0,7
SPIWrite 0033,03,0,7 //F_order=0x1; Address(0x30[27:25],)
SPIWrite 0039,40,0,7 //EN_LOCK_DETECT=0x1; Address(0x38[14:14],)
SPIWrite 003f,14,0,7 //CTL_PFD_DEL=0x2; Address(0x3c[29:27],)
SPIWrite 003f,12,0,7 //CTL_OUT_DIV=0x2; Address(0x3c[26:24],)
SPIWrite 003c,41,0,7 //CTL_LDO_ANA=0x4; Address(0x3c[6:4],)
SPIWrite 003d,24,0,7 //CTL_LDO_RF=0x4; Address(0x3c[10:8],)
SPIWrite 0052,10,0,7 //CTL_FBDIV_LDO_PROG=0x8; Address(0x50[20:17],)
SPIWrite 003a,08,0,7 //CTL_CP_IREF=0x2; Address(0x38[20:18],)
SPIWrite 003b,30,0,7 //CTL_CP_IOUT=0x3; Address(0x38[29:28],)
SPIWrite 0034,03,0,7 //lock_acc=0x3; Address(0x34[1:0],)
SPIWrite 0034,0f,0,7 //lock_cnt=0x3; Address(0x34[3:2],)
SPIWrite 0034,1f,0,7 //lock_err=0x1; Address(0x34[5:4],)
SPIWrite 0066,04,0,7 //lock_rst=0x1; Address(0x64[18:18],)
SPIWrite 0066,00,0,7 //lock_rst=0x0; Address(0x64[18:18],)
SPIWrite 0066,08,0,7 //lock_lost_rst=0x1; Address(0x64[19:19],)
SPIWrite 0066,00,0,7 //lock_lost_rst=0x0; Address(0x64[19:19],)
SPIWrite 0034,1f,0,7 //EN_CAL=0x0; Address(0x34[6:6],)
SPIWrite 0034,5f,0,7 //EN_CAL=0x1; Address(0x34[6:6],)
SPIWrite 003c,45,0,7 //CTL_I_OFFSET=0x1; Address(0x3c[3:2],)

WAIT 0.01

SPIWrite 005e,9f,0,7 //EN_I_OFFSET=0x1; Address(0x5c[19:19],)
SPIWrite 003c,45,0,7 //CTL_I_OFST_UP_DN=0x0; Address(0x3c[1:1],)
SPIWrite 0065,10,0,7 //CTL_I_OFST_REF=0x1; Address(0x64[14:12],)

SPIPoll 0062,7,7,1

SPIReadCheck 0062,7,7,80

\\Read lock=0x1; Address(0x60[23:23],)

\\END: Done configuring PLL

SPIWrite 0014,00,0,7 //PAGE: pll=0x0; Address(0x14[7:3],)
\\ STEP: pll4Config/step0

\\START: Configure PLL

SPIWrite 0014,80,0,7 //PAGE: pll=0x10; Address(0x14[7:3],)
SPIWrite 004b,33,0,7 //PLL_SPARE0=0xcc000; Address(0x48[31:10],)
SPIWrite 004a,00,0,7
SPIWrite 0049,00,0,7
SPIWrite 005e,97,0,7 //EN_VCO=0x1; Address(0x5c[23:23],)
SPIWrite 0028,0f,0,7 //N=0xf; Address(0x28[7:0],)
SPIWrite 0036,0f,0,7 //N_to_CAL=0xf; Address(0x34[23:16],)
SPIWrite 0035,08,0,7 //CAL_CONTROL=0x8; Address(0x34[12:8],)
SPIWrite 0051,06,0,7 //CTL_REF_DIV=0x1; (Meaning:); Address(0x50[8:6],)
SPIWrite 0050,40,0,7
SPIWrite 0051,06,0,7 //EN_DIV8_CLK_TO_PLLDIG=0x1; Address(0x50[9:9],)
SPIWrite 0051,06,0,7 //EN_CLK_TO_PLLDIG=0x1; Address(0x50[10:10],)
SPIWrite 0051,0e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 003c,40,0,7 //CTL_DIV_23_45=0x0; Address(0x3c[0:0],)
SPIWrite 005e,97,0,7 //EN_FRAC_N_MODE=0x1; Address(0x5c[16:16],)
SPIWrite 0033,03,0,7 //EN_FRAC_MODE=0x1; Address(0x30[24:24],)
SPIWrite 003f,0c,0,7 //CTL_TEST_MUX=0x0; Address(0x3c[31:30],)
SPIWrite 0049,01,0,7 //EN_VCO_PKDET=0x1; Address(0x48[8:8],)
SPIWrite 0048,2f,0,7 //VCO_PKDET_BIAS=0x7; Address(0x48[2:0],)
SPIWrite 0044,27,0,7 //CTL_VCO_IB_GM=0x7; Address(0x44[2:0],)
SPIWrite 0043,4c,0,7 //CTL_VCO_IB_TAIL=0xc; Address(0x40[27:24],)
SPIWrite 0043,0c,0,7 //CTL_VCO_IB_GM_SLOPE=0x0; Address(0x40[30:28],)
SPIWrite 0042,04,0,7 //CTL_VCO_IB_TAIL_SLOPE=0x0; Address(0x40[22:19],)
SPIWrite 0046,00,0,7 //CTL_VCO_IB_GM1=0x0; Address(0x44[22:21],)
SPIWrite 0040,28,0,7 //CTL_VB_VAR_SLOPE=0x0; Address(0x40[2:0],)
SPIWrite 002e,03,0,7 //F=0x34260; Address(0x2c[19:0],)
SPIWrite 002d,42,0,7
SPIWrite 002c,60,0,7
SPIWrite 0032,03,0,7 //D=0x3c000; Address(0x30[19:0],)
SPIWrite 0031,c0,0,7
SPIWrite 0030,00,0,7
SPIWrite 0033,03,0,7 //F_order=0x1; Address(0x30[27:25],)
SPIWrite 0039,40,0,7 //EN_LOCK_DETECT=0x1; Address(0x38[14:14],)
SPIWrite 003f,14,0,7 //CTL_PFD_DEL=0x2; Address(0x3c[29:27],)
SPIWrite 003f,12,0,7 //CTL_OUT_DIV=0x2; Address(0x3c[26:24],)
SPIWrite 003c,40,0,7 //CTL_LDO_ANA=0x4; Address(0x3c[6:4],)
SPIWrite 003d,24,0,7 //CTL_LDO_RF=0x4; Address(0x3c[10:8],)
SPIWrite 0052,10,0,7 //CTL_FBDIV_LDO_PROG=0x8; Address(0x50[20:17],)
SPIWrite 003a,08,0,7 //CTL_CP_IREF=0x2; Address(0x38[20:18],)
SPIWrite 003b,30,0,7 //CTL_CP_IOUT=0x3; Address(0x38[29:28],)
SPIWrite 0034,03,0,7 //lock_acc=0x3; Address(0x34[1:0],)
SPIWrite 0034,0f,0,7 //lock_cnt=0x3; Address(0x34[3:2],)
SPIWrite 0034,1f,0,7 //lock_err=0x1; Address(0x34[5:4],)
SPIWrite 0066,04,0,7 //lock_rst=0x1; Address(0x64[18:18],)
SPIWrite 0066,00,0,7 //lock_rst=0x0; Address(0x64[18:18],)
SPIWrite 0066,08,0,7 //lock_lost_rst=0x1; Address(0x64[19:19],)
SPIWrite 0066,00,0,7 //lock_lost_rst=0x0; Address(0x64[19:19],)
SPIWrite 0034,1f,0,7 //EN_CAL=0x0; Address(0x34[6:6],)
SPIWrite 0034,5f,0,7 //EN_CAL=0x1; Address(0x34[6:6],)
SPIWrite 003c,44,0,7 //CTL_I_OFFSET=0x1; Address(0x3c[3:2],)

WAIT 0.01

SPIWrite 005e,9f,0,7 //EN_I_OFFSET=0x1; Address(0x5c[19:19],)

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SPIWrite 003c,44,0,7    //CTL_I_OFST_UP_DN=0x0;    Address(0x3c[1:1],)
SPIWrite 0065,10,0,7    //CTL_I_OFST_REF=0x1;    Address(0x64[14:12],)

SIPoll 0062,7,7,1

SPIReadCheck 0062,7,7,80

\\Read  lock=0x1;    Address(0x60[23:23],)

\\END: Done configuring PLL

SPIWrite 0014,00,0,7    //PAGE: pll=0x0;    Address(0x14[7:3],)
\\ STEP: pll4Config/step1

\\START: Requesting/releasing SPI Access to PLL Pages

SPIWrite 0015,80,0,7    //PAGE: DIGTOP_MISC=0x1;(Meaning: );    Address(0x15[7:7],)
SPIWrite 01d4,00,0,7    //pll_reg_spi_req_a=0x0;    Address(0x1d4[0:0],)
SPIWrite 0374,00,0,7    //pll_reg_spi_req_b1=0x0;(Meaning: );    Address(0x374[0:0],)

WAIT 0.01

\\END: Done requesting/releasing SPI Access to PLL Pages

SPIWrite 0015,00,0,7    //PAGE: DIGTOP_MISC=0x0;(Meaning: );    Address(0x15[7:7],)
\\ STEP: efuseToAnalog/step0
SPIWrite 0013,20,0,7    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIWrite 0084,21,0,7
SPIWrite 0084,61,0,7
SPIWrite 0084,21,0,7
SPIWrite 0084,21,0,7
SPIWrite 0084,01,0,7
SPIWrite 0088,01,0,7
SPIWrite 0013,00,0,7    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
\\ STEP: efuseToAnalog/step1

\\START: Loading EFuse Packets

SPIWrite 0013,10,0,7    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)

SIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,04,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,07,0,7
SPIWrite 00a4,00,0,7
SPIWrite 00ab,11,0,7
SPIWrite 00aa,0b,0,7
SPIWrite 00a9,0a,0,7
SPIWrite 00a8,07,0,7
SPIWrite 00af,00,0,7
SPIWrite 00ae,00,0,7
SPIWrite 00ad,00,0,7
SPIWrite 00ac,14,0,7
SPIWrite 0193,12,0,7

SIPoll 00f0,0,7,7

\\END: Done Loading Efuse packets

SPIWrite 0013,00,0,7    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
\\ STEP: topConfig/step0
```


\\START: Doing Top & DSA initialization

SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning:); Address(0x15[7:7],)
SPIWrite 0649,80,0,7 //dsa_abcd_addr_range=0x1; Address(0x648[15:15],)

\\Disabling FB CD

SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning:); Address(0x15[7:7],)
SPIWrite 0018,01,0,7 //PAGE: ana_4t4r=0x1; (Meaning:); Address(0x18[0:0],)
SPIWrite 002c,02,0,7
SPIWrite 0018,00,0,7 //PAGE: ana_4t4r=0x0; (Meaning:); Address(0x18[0:0],)
SPIWrite 0014,01,0,7 //PAGE: clktop_2t=0x1; Address(0x14[1:0],)
SPIWrite 006c,08,0,7 //MASK_PDN_SYNC_TXCML_L=0x1; Address(0x6c[3:3],)
SPIWrite 006c,18,0,7 //MASK_PDN_SYNC_TXCML_R=0x1; Address(0x6c[4:4],)
SPIWrite 006c,1c,0,7 //MASK_PDN_FAST_TXCML_R=0x1; Address(0x6c[2:2],)
SPIWrite 006c,1e,0,7 //MASK_PDN_FAST_TXCML_L=0x1; Address(0x6c[1:1],)
SPIWrite 0014,00,0,7 //PAGE: clktop_2t=0x0; Address(0x14[1:0],)
SPIWrite 0018,01,0,7 //PAGE: ana_4t4r=0x1; (Meaning:); Address(0x18[0:0],)
SPIWrite 0050,00,0,7
SPIWrite 0054,00,0,7
SPIWrite 0050,00,0,7
SPIWrite 0054,00,0,7
SPIWrite 0018,00,0,7 //PAGE: ana_4t4r=0x0; (Meaning:); Address(0x18[0:0],)
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning:); Address(0x14[2:2],)
SPIWrite 0105,02,0,7 //map_invalid_settings=0x2; Address(0x104[9:8],)
SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning:); Address(0x14[2:2],)
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning:); Address(0x15[7:7],)
SPIWrite 039c,00,0,7 //sync_lvds_mode_ab=0x0; (Meaning:); Address(0x39c[0:0],)
SPIWrite 039c,00,0,7 //sync_lvds_mode_cd=0x0; (Meaning:); Address(0x39c[1:1],)
SPIWrite 0330,00,0,7 //pull_ctrl_gpio_76=0x0; Address(0x330[2:0],)
SPIWrite 02e4,00,0,7 //pull_ctrl_gpio_57=0x0; Address(0x2e4[2:0],)
SPIWrite 02f4,00,0,7 //pull_ctrl_gpio_61=0x0; Address(0x2f4[2:0],)
SPIWrite 02c0,00,0,7 //pull_ctrl_gpio_48=0x0; Address(0x2c0[2:0],)
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning:); Address(0x15[7:7],)
SPIWrite 0016,30,0,7 //PAGE: dsa=0x3; Address(0x16[5:4],)
SPIWrite 0180,01,0,7
SPIWrite 02fb,00,0,7
SPIWrite 065b,00,0,7
SPIWrite 02fd,00,0,7
SPIWrite 065d,00,0,7
SPIWrite 02fe,00,0,7
SPIWrite 0305,00,0,7
SPIWrite 030e,00,0,7
SPIWrite 065e,00,0,7
SPIWrite 0665,00,0,7
SPIWrite 066e,00,0,7
SPIWrite 0040,03,0,7
SPIWrite 0311,00,0,7
SPIWrite 0671,00,0,7
SPIWrite 02c8,01,0,7
SPIWrite 02d0,01,0,7
SPIWrite 0630,01,0,7
SPIWrite 0051,00,0,7
SPIWrite 00b1,00,0,7
SPIWrite 0677,01,0,7
SPIWrite 0676,f0,0,7
SPIWrite 0675,03,0,7
SPIWrite 0674,e0,0,7
SPIWrite 067b,01,0,7
SPIWrite 067a,f0,0,7
SPIWrite 0679,03,0,7
SPIWrite 0678,21,0,7
SPIWrite 067f,01,0,7
SPIWrite 067e,f0,0,7
SPIWrite 067d,02,0,7
SPIWrite 067c,82,0,7
SPIWrite 0683,01,0,7
SPIWrite 0682,f0,0,7

SPIWrite 0681,02,0,7
SPIWrite 0680,23,0,7
SPIWrite 0687,01,0,7
SPIWrite 0686,f0,0,7
SPIWrite 0685,01,0,7
SPIWrite 0684,c4,0,7
SPIWrite 068b,01,0,7
SPIWrite 068a,f0,0,7
SPIWrite 0689,01,0,7
SPIWrite 0688,85,0,7
SPIWrite 068f,01,0,7
SPIWrite 068e,f0,0,7
SPIWrite 068d,01,0,7
SPIWrite 068c,46,0,7
SPIWrite 0693,01,0,7
SPIWrite 0692,f0,0,7
SPIWrite 0691,01,0,7
SPIWrite 0690,27,0,7
SPIWrite 0697,01,0,7
SPIWrite 0696,e0,0,7
SPIWrite 0695,01,0,7
SPIWrite 0694,08,0,7
SPIWrite 069b,01,0,7
SPIWrite 069a,e0,0,7
SPIWrite 0699,00,0,7
SPIWrite 0698,e9,0,7
SPIWrite 069f,01,0,7
SPIWrite 069e,f0,0,7
SPIWrite 069d,00,0,7
SPIWrite 069c,ca,0,7
SPIWrite 06a3,01,0,7
SPIWrite 06a2,f0,0,7
SPIWrite 06a1,00,0,7
SPIWrite 06a0,ab,0,7
SPIWrite 06a7,01,0,7
SPIWrite 06a6,f0,0,7
SPIWrite 06a5,00,0,7
SPIWrite 06a4,8c,0,7
SPIWrite 06ab,01,0,7
SPIWrite 06aa,f0,0,7
SPIWrite 06a9,00,0,7
SPIWrite 06a8,8d,0,7
SPIWrite 06af,01,0,7
SPIWrite 06ae,f0,0,7
SPIWrite 06ad,00,0,7
SPIWrite 06ac,6e,0,7
SPIWrite 06b3,01,0,7
SPIWrite 06b2,f0,0,7
SPIWrite 06b1,00,0,7
SPIWrite 06b0,6f,0,7
SPIWrite 06b7,01,0,7
SPIWrite 06b6,d0,0,7
SPIWrite 06b5,00,0,7
SPIWrite 06b4,70,0,7
SPIWrite 06bb,01,0,7
SPIWrite 06ba,b0,0,7
SPIWrite 06b9,00,0,7
SPIWrite 06b8,71,0,7
SPIWrite 06bf,01,0,7
SPIWrite 06be,80,0,7
SPIWrite 06bd,00,0,7
SPIWrite 06bc,72,0,7
SPIWrite 06c3,01,0,7
SPIWrite 06c2,50,0,7
SPIWrite 06c1,00,0,7
SPIWrite 06c0,73,0,7
SPIWrite 06c7,01,0,7
SPIWrite 06c6,20,0,7
SPIWrite 06c5,00,0,7

SPIWrite 06c4,74,0,7
SPIWrite 06cb,00,0,7
SPIWrite 06ca,60,0,7
SPIWrite 06c9,00,0,7
SPIWrite 06c8,75,0,7
SPIWrite 06cf,00,0,7
SPIWrite 06ce,20,0,7
SPIWrite 06cd,00,0,7
SPIWrite 06cc,76,0,7
SPIWrite 06d3,00,0,7
SPIWrite 06d2,00,0,7
SPIWrite 06d1,00,0,7
SPIWrite 06d0,77,0,7
SPIWrite 06d7,00,0,7
SPIWrite 06d6,00,0,7
SPIWrite 06d5,00,0,7
SPIWrite 06d4,78,0,7
SPIWrite 06db,00,0,7
SPIWrite 06da,40,0,7
SPIWrite 06d9,00,0,7
SPIWrite 06d8,59,0,7
SPIWrite 06df,00,0,7
SPIWrite 06de,00,0,7
SPIWrite 06dd,00,0,7
SPIWrite 06dc,5a,0,7
SPIWrite 06e3,00,0,7
SPIWrite 06e2,00,0,7
SPIWrite 06e1,00,0,7
SPIWrite 06e0,5b,0,7
SPIWrite 06e7,00,0,7
SPIWrite 06e6,00,0,7
SPIWrite 06e5,00,0,7
SPIWrite 06e4,5c,0,7
SPIWrite 06eb,01,0,7
SPIWrite 06ea,30,0,7
SPIWrite 06e9,00,0,7
SPIWrite 06e8,3d,0,7
SPIWrite 06ef,00,0,7
SPIWrite 06ee,f0,0,7
SPIWrite 06ed,00,0,7
SPIWrite 06ec,3e,0,7
SPIWrite 06f3,00,0,7
SPIWrite 06f2,f0,0,7
SPIWrite 06f1,00,0,7
SPIWrite 06f0,3e,0,7
SPIWrite 06f7,00,0,7
SPIWrite 06f6,f0,0,7
SPIWrite 06f5,00,0,7
SPIWrite 06f4,3e,0,7
SPIWrite 06fb,00,0,7
SPIWrite 06fa,f0,0,7
SPIWrite 06f9,00,0,7
SPIWrite 06f8,3e,0,7
SPIWrite 06ff,00,0,7
SPIWrite 06fe,f0,0,7
SPIWrite 06fd,00,0,7
SPIWrite 06fc,3e,0,7
SPIWrite 0703,00,0,7
SPIWrite 0702,f0,0,7
SPIWrite 0701,00,0,7
SPIWrite 0700,3e,0,7
SPIWrite 0707,00,0,7
SPIWrite 0706,f0,0,7
SPIWrite 0705,00,0,7
SPIWrite 0704,3e,0,7
SPIWrite 0317,01,0,7
SPIWrite 0316,f0,0,7
SPIWrite 0315,03,0,7
SPIWrite 0314,e0,0,7

SPIWrite 031b,01,0,7
SPIWrite 031a,f0,0,7
SPIWrite 0319,03,0,7
SPIWrite 0318,21,0,7
SPIWrite 031f,01,0,7
SPIWrite 031e,f0,0,7
SPIWrite 031d,02,0,7
SPIWrite 031c,82,0,7
SPIWrite 0323,01,0,7
SPIWrite 0322,f0,0,7
SPIWrite 0321,02,0,7
SPIWrite 0320,23,0,7
SPIWrite 0327,01,0,7
SPIWrite 0326,f0,0,7
SPIWrite 0325,01,0,7
SPIWrite 0324,c4,0,7
SPIWrite 032b,01,0,7
SPIWrite 032a,f0,0,7
SPIWrite 0329,01,0,7
SPIWrite 0328,85,0,7
SPIWrite 032f,01,0,7
SPIWrite 032e,f0,0,7
SPIWrite 032d,01,0,7
SPIWrite 032c,46,0,7
SPIWrite 0333,01,0,7
SPIWrite 0332,f0,0,7
SPIWrite 0331,01,0,7
SPIWrite 0330,27,0,7
SPIWrite 0337,01,0,7
SPIWrite 0336,e0,0,7
SPIWrite 0335,01,0,7
SPIWrite 0334,08,0,7
SPIWrite 033b,01,0,7
SPIWrite 033a,e0,0,7
SPIWrite 0339,00,0,7
SPIWrite 0338,e9,0,7
SPIWrite 033f,01,0,7
SPIWrite 033e,f0,0,7
SPIWrite 033d,00,0,7
SPIWrite 033c,ca,0,7
SPIWrite 0343,01,0,7
SPIWrite 0342,f0,0,7
SPIWrite 0341,00,0,7
SPIWrite 0340,ab,0,7
SPIWrite 0347,01,0,7
SPIWrite 0346,f0,0,7
SPIWrite 0345,00,0,7
SPIWrite 0344,8c,0,7
SPIWrite 034b,01,0,7
SPIWrite 034a,f0,0,7
SPIWrite 0349,00,0,7
SPIWrite 0348,8d,0,7
SPIWrite 034f,01,0,7
SPIWrite 034e,f0,0,7
SPIWrite 034d,00,0,7
SPIWrite 034c,6e,0,7
SPIWrite 0353,01,0,7
SPIWrite 0352,f0,0,7
SPIWrite 0351,00,0,7
SPIWrite 0350,6f,0,7
SPIWrite 0357,01,0,7
SPIWrite 0356,d0,0,7
SPIWrite 0355,00,0,7
SPIWrite 0354,70,0,7
SPIWrite 035b,01,0,7
SPIWrite 035a,b0,0,7
SPIWrite 0359,00,0,7
SPIWrite 0358,71,0,7
SPIWrite 035f,01,0,7

SPIWrite 035e,80,0,7
SPIWrite 035d,00,0,7
SPIWrite 035c,72,0,7
SPIWrite 0363,01,0,7
SPIWrite 0362,50,0,7
SPIWrite 0361,00,0,7
SPIWrite 0360,73,0,7
SPIWrite 0367,01,0,7
SPIWrite 0366,20,0,7
SPIWrite 0365,00,0,7
SPIWrite 0364,74,0,7
SPIWrite 036b,00,0,7
SPIWrite 036a,60,0,7
SPIWrite 0369,00,0,7
SPIWrite 0368,75,0,7
SPIWrite 036f,00,0,7
SPIWrite 036e,20,0,7
SPIWrite 036d,00,0,7
SPIWrite 036c,76,0,7
SPIWrite 0373,00,0,7
SPIWrite 0372,00,0,7
SPIWrite 0371,00,0,7
SPIWrite 0370,77,0,7
SPIWrite 0377,00,0,7
SPIWrite 0376,00,0,7
SPIWrite 0375,00,0,7
SPIWrite 0374,78,0,7
SPIWrite 037b,00,0,7
SPIWrite 037a,40,0,7
SPIWrite 0379,00,0,7
SPIWrite 0378,59,0,7
SPIWrite 037f,00,0,7
SPIWrite 037e,00,0,7
SPIWrite 037d,00,0,7
SPIWrite 037c,5a,0,7
SPIWrite 0383,00,0,7
SPIWrite 0382,00,0,7
SPIWrite 0381,00,0,7
SPIWrite 0380,5b,0,7
SPIWrite 0387,00,0,7
SPIWrite 0386,00,0,7
SPIWrite 0385,00,0,7
SPIWrite 0384,5c,0,7
SPIWrite 038b,01,0,7
SPIWrite 038a,30,0,7
SPIWrite 0389,00,0,7
SPIWrite 0388,3d,0,7
SPIWrite 038f,00,0,7
SPIWrite 038e,f0,0,7
SPIWrite 038d,00,0,7
SPIWrite 038c,3e,0,7
SPIWrite 0393,00,0,7
SPIWrite 0392,f0,0,7
SPIWrite 0391,00,0,7
SPIWrite 0390,3e,0,7
SPIWrite 0397,00,0,7
SPIWrite 0396,f0,0,7
SPIWrite 0395,00,0,7
SPIWrite 0394,3e,0,7
SPIWrite 039b,00,0,7
SPIWrite 039a,f0,0,7
SPIWrite 0399,00,0,7
SPIWrite 0398,3e,0,7
SPIWrite 039f,00,0,7
SPIWrite 039e,f0,0,7
SPIWrite 039d,00,0,7
SPIWrite 039c,3e,0,7
SPIWrite 03a3,00,0,7
SPIWrite 03a2,f0,0,7

SPIWrite 03a1,00,0,7
SPIWrite 03a0,3e,0,7
SPIWrite 03a7,00,0,7
SPIWrite 03a6,f0,0,7
SPIWrite 03a5,00,0,7
SPIWrite 03a4,3e,0,7
SPIWrite 09a8,ff,0,7
SPIWrite 09b0,ff,0,7
SPIWrite 09b8,ff,0,7
SPIWrite 09c0,ff,0,7
SPIWrite 09c8,ff,0,7
SPIWrite 09d0,ff,0,7
SPIWrite 09d8,ff,0,7
SPIWrite 09e0,ff,0,7
SPIWrite 09e8,ff,0,7
SPIWrite 09f0,ff,0,7
SPIWrite 09f8,ff,0,7
SPIWrite 0a00,ff,0,7
SPIWrite 0a08,ff,0,7
SPIWrite 0a10,ff,0,7
SPIWrite 0a18,ff,0,7
SPIWrite 0a20,ff,0,7
SPIWrite 0a28,ff,0,7
SPIWrite 0a30,ff,0,7
SPIWrite 0a38,ff,0,7
SPIWrite 0a40,ff,0,7
SPIWrite 0a48,ff,0,7
SPIWrite 0a50,ff,0,7
SPIWrite 0a58,ff,0,7
SPIWrite 0a60,ff,0,7
SPIWrite 0a68,ff,0,7
SPIWrite 0a70,ff,0,7
SPIWrite 0a78,ff,0,7
SPIWrite 0a80,ff,0,7
SPIWrite 0a88,ff,0,7
SPIWrite 0a90,ff,0,7
SPIWrite 0a98,ff,0,7
SPIWrite 0aa0,ff,0,7
SPIWrite 0aa8,ff,0,7
SPIWrite 0ab0,fe,0,7
SPIWrite 0ab8,fc,0,7
SPIWrite 0ac0,f8,0,7
SPIWrite 0ac8,f0,0,7
SPIWrite 0ad0,e0,0,7
SPIWrite 0ad8,c0,0,7
SPIWrite 0ae0,80,0,7
SPIWrite 09af,ff,0,7
SPIWrite 09ae,ff,0,7
SPIWrite 09ad,ff,0,7
SPIWrite 09ac,ff,0,7
SPIWrite 09b7,ff,0,7
SPIWrite 09b6,ff,0,7
SPIWrite 09b5,ff,0,7
SPIWrite 09b4,fe,0,7
SPIWrite 09bf,ff,0,7
SPIWrite 09be,ff,0,7
SPIWrite 09bd,ff,0,7
SPIWrite 09bc,fc,0,7
SPIWrite 09c7,ff,0,7
SPIWrite 09c6,ff,0,7
SPIWrite 09c5,ff,0,7
SPIWrite 09c4,f8,0,7
SPIWrite 09cf,ff,0,7
SPIWrite 09ce,ff,0,7
SPIWrite 09cd,ff,0,7
SPIWrite 09cc,f0,0,7
SPIWrite 09d7,ff,0,7
SPIWrite 09d6,ff,0,7
SPIWrite 09d5,ff,0,7

SPIWrite 09d4,e0,0,7
SPIWrite 09df,ff,0,7
SPIWrite 09de,ff,0,7
SPIWrite 09dd,ff,0,7
SPIWrite 09dc,c0,0,7
SPIWrite 09e7,ff,0,7
SPIWrite 09e6,ff,0,7
SPIWrite 09e5,ff,0,7
SPIWrite 09e4,80,0,7
SPIWrite 09ef,ff,0,7
SPIWrite 09ee,ff,0,7
SPIWrite 09ed,ff,0,7
SPIWrite 09ec,00,0,7
SPIWrite 09f7,ff,0,7
SPIWrite 09f6,ff,0,7
SPIWrite 09f5,fe,0,7
SPIWrite 09f4,00,0,7
SPIWrite 09ff,ff,0,7
SPIWrite 09fe,ff,0,7
SPIWrite 09fd,fc,0,7
SPIWrite 09fc,00,0,7
SPIWrite 0a07,ff,0,7
SPIWrite 0a06,ff,0,7
SPIWrite 0a05,f8,0,7
SPIWrite 0a04,00,0,7
SPIWrite 0a0f,ff,0,7
SPIWrite 0a0e,ff,0,7
SPIWrite 0a0d,f0,0,7
SPIWrite 0a0c,00,0,7
SPIWrite 0a17,ff,0,7
SPIWrite 0a16,ff,0,7
SPIWrite 0a15,e0,0,7
SPIWrite 0a14,00,0,7
SPIWrite 0a1f,ff,0,7
SPIWrite 0ale,ff,0,7
SPIWrite 0ald,c0,0,7
SPIWrite 0alc,00,0,7
SPIWrite 0a27,ff,0,7
SPIWrite 0a26,ff,0,7
SPIWrite 0a25,80,0,7
SPIWrite 0a24,00,0,7
SPIWrite 0a2f,ff,0,7
SPIWrite 0a2e,ff,0,7
SPIWrite 0a2d,00,0,7
SPIWrite 0a2c,00,0,7
SPIWrite 0a37,ff,0,7
SPIWrite 0a36,fe,0,7
SPIWrite 0a35,00,0,7
SPIWrite 0a34,00,0,7
SPIWrite 0a3f,ff,0,7
SPIWrite 0a3e,fc,0,7
SPIWrite 0a3d,00,0,7
SPIWrite 0a3c,00,0,7
SPIWrite 0a47,ff,0,7
SPIWrite 0a46,f8,0,7
SPIWrite 0a45,00,0,7
SPIWrite 0a44,00,0,7
SPIWrite 0a4f,ff,0,7
SPIWrite 0a4e,f0,0,7
SPIWrite 0a4d,00,0,7
SPIWrite 0a4c,00,0,7
SPIWrite 0a57,ff,0,7
SPIWrite 0a56,e0,0,7
SPIWrite 0a55,00,0,7
SPIWrite 0a54,00,0,7
SPIWrite 0a5f,ff,0,7
SPIWrite 0a5e,c0,0,7
SPIWrite 0a5d,00,0,7
SPIWrite 0a5c,00,0,7

SPIWrite 0a67,ff,0,7
SPIWrite 0a66,80,0,7
SPIWrite 0a65,00,0,7
SPIWrite 0a64,00,0,7
SPIWrite 0a6f,ff,0,7
SPIWrite 0a6e,00,0,7
SPIWrite 0a6d,00,0,7
SPIWrite 0a6c,00,0,7
SPIWrite 0a77,fe,0,7
SPIWrite 0a76,00,0,7
SPIWrite 0a75,00,0,7
SPIWrite 0a74,00,0,7
SPIWrite 0a7f,fc,0,7
SPIWrite 0a7e,00,0,7
SPIWrite 0a7d,00,0,7
SPIWrite 0a7c,00,0,7
SPIWrite 0a87,f8,0,7
SPIWrite 0a86,00,0,7
SPIWrite 0a85,00,0,7
SPIWrite 0a84,00,0,7
SPIWrite 0a8f,f0,0,7
SPIWrite 0a8e,00,0,7
SPIWrite 0a8d,00,0,7
SPIWrite 0a8c,00,0,7
SPIWrite 0a97,e0,0,7
SPIWrite 0a96,00,0,7
SPIWrite 0a95,00,0,7
SPIWrite 0a94,00,0,7
SPIWrite 0a9f,c0,0,7
SPIWrite 0a9e,00,0,7
SPIWrite 0a9d,00,0,7
SPIWrite 0a9c,00,0,7
SPIWrite 0aa7,80,0,7
SPIWrite 0aa6,00,0,7
SPIWrite 0aa5,00,0,7
SPIWrite 0aa4,00,0,7
SPIWrite 0aaf,00,0,7
SPIWrite 0aae,00,0,7
SPIWrite 0aad,00,0,7
SPIWrite 0aac,00,0,7
SPIWrite 0ab7,00,0,7
SPIWrite 0ab6,00,0,7
SPIWrite 0ab5,00,0,7
SPIWrite 0ab4,00,0,7
SPIWrite 0abf,00,0,7
SPIWrite 0abe,00,0,7
SPIWrite 0abd,00,0,7
SPIWrite 0abc,00,0,7
SPIWrite 0ac7,00,0,7
SPIWrite 0ac6,00,0,7
SPIWrite 0ac5,00,0,7
SPIWrite 0ac4,00,0,7
SPIWrite 0acf,00,0,7
SPIWrite 0ace,00,0,7
SPIWrite 0acd,00,0,7
SPIWrite 0acc,00,0,7
SPIWrite 0ad7,00,0,7
SPIWrite 0ad6,00,0,7
SPIWrite 0ad5,00,0,7
SPIWrite 0ad4,00,0,7
SPIWrite 0adf,00,0,7
SPIWrite 0ade,00,0,7
SPIWrite 0add,00,0,7
SPIWrite 0adc,00,0,7
SPIWrite 0ae7,00,0,7
SPIWrite 0ae6,00,0,7
SPIWrite 0ae5,00,0,7
SPIWrite 0ae4,00,0,7
SPIWrite 0bc8,ff,0,7

SPIWrite 0bd0,ff,0,7
SPIWrite 0bd8,ff,0,7
SPIWrite 0be0,ff,0,7
SPIWrite 0be8,ff,0,7
SPIWrite 0bf0,ff,0,7
SPIWrite 0bf8,ff,0,7
SPIWrite 0c00,ff,0,7
SPIWrite 0c08,ff,0,7
SPIWrite 0c10,ff,0,7
SPIWrite 0c18,ff,0,7
SPIWrite 0c20,ff,0,7
SPIWrite 0c28,ff,0,7
SPIWrite 0c30,ff,0,7
SPIWrite 0c38,ff,0,7
SPIWrite 0c40,ff,0,7
SPIWrite 0c48,ff,0,7
SPIWrite 0c50,ff,0,7
SPIWrite 0c58,ff,0,7
SPIWrite 0c60,ff,0,7
SPIWrite 0c68,ff,0,7
SPIWrite 0c70,ff,0,7
SPIWrite 0c78,ff,0,7
SPIWrite 0c80,ff,0,7
SPIWrite 0c88,ff,0,7
SPIWrite 0c90,ff,0,7
SPIWrite 0c98,ff,0,7
SPIWrite 0ca0,ff,0,7
SPIWrite 0ca8,ff,0,7
SPIWrite 0cb0,ff,0,7
SPIWrite 0cb8,ff,0,7
SPIWrite 0cc0,ff,0,7
SPIWrite 0cc8,ff,0,7
SPIWrite 0cd0,fe,0,7
SPIWrite 0cd8,fc,0,7
SPIWrite 0ce0,f8,0,7
SPIWrite 0ce8,f0,0,7
SPIWrite 0cf0,e0,0,7
SPIWrite 0cf8,c0,0,7
SPIWrite 0d00,80,0,7
SPIWrite 0bcf,ff,0,7
SPIWrite 0bce,ff,0,7
SPIWrite 0bcd,ff,0,7
SPIWrite 0bcc,ff,0,7
SPIWrite 0bd7,ff,0,7
SPIWrite 0bd6,ff,0,7
SPIWrite 0bd5,ff,0,7
SPIWrite 0bd4,fe,0,7
SPIWrite 0bdf,ff,0,7
SPIWrite 0bde,ff,0,7
SPIWrite 0bdd,ff,0,7
SPIWrite 0bdc,fc,0,7
SPIWrite 0be7,ff,0,7
SPIWrite 0be6,ff,0,7
SPIWrite 0be5,ff,0,7
SPIWrite 0be4,f8,0,7
SPIWrite 0bef,ff,0,7
SPIWrite 0bee,ff,0,7
SPIWrite 0bed,ff,0,7
SPIWrite 0bec,f0,0,7
SPIWrite 0bf7,ff,0,7
SPIWrite 0bf6,ff,0,7
SPIWrite 0bf5,ff,0,7
SPIWrite 0bf4,e0,0,7
SPIWrite 0bff,ff,0,7
SPIWrite 0bfe,ff,0,7
SPIWrite 0bfd,ff,0,7
SPIWrite 0bfc,c0,0,7
SPIWrite 0c07,ff,0,7
SPIWrite 0c06,ff,0,7

SPIWrite 0c05,ff,0,7
SPIWrite 0c04,80,0,7
SPIWrite 0c0f,ff,0,7
SPIWrite 0c0e,ff,0,7
SPIWrite 0c0d,ff,0,7
SPIWrite 0c0c,00,0,7
SPIWrite 0c17,ff,0,7
SPIWrite 0c16,ff,0,7
SPIWrite 0c15,fe,0,7
SPIWrite 0c14,00,0,7
SPIWrite 0c1f,ff,0,7
SPIWrite 0c1e,ff,0,7
SPIWrite 0c1d,fc,0,7
SPIWrite 0c1c,00,0,7
SPIWrite 0c27,ff,0,7
SPIWrite 0c26,ff,0,7
SPIWrite 0c25,f8,0,7
SPIWrite 0c24,00,0,7
SPIWrite 0c2f,ff,0,7
SPIWrite 0c2e,ff,0,7
SPIWrite 0c2d,f0,0,7
SPIWrite 0c2c,00,0,7
SPIWrite 0c37,ff,0,7
SPIWrite 0c36,ff,0,7
SPIWrite 0c35,e0,0,7
SPIWrite 0c34,00,0,7
SPIWrite 0c3f,ff,0,7
SPIWrite 0c3e,ff,0,7
SPIWrite 0c3d,c0,0,7
SPIWrite 0c3c,00,0,7
SPIWrite 0c47,ff,0,7
SPIWrite 0c46,ff,0,7
SPIWrite 0c45,80,0,7
SPIWrite 0c44,00,0,7
SPIWrite 0c4f,ff,0,7
SPIWrite 0c4e,ff,0,7
SPIWrite 0c4d,00,0,7
SPIWrite 0c4c,00,0,7
SPIWrite 0c57,ff,0,7
SPIWrite 0c56,fe,0,7
SPIWrite 0c55,00,0,7
SPIWrite 0c54,00,0,7
SPIWrite 0c5f,ff,0,7
SPIWrite 0c5e,fc,0,7
SPIWrite 0c5d,00,0,7
SPIWrite 0c5c,00,0,7
SPIWrite 0c67,ff,0,7
SPIWrite 0c66,f8,0,7
SPIWrite 0c65,00,0,7
SPIWrite 0c64,00,0,7
SPIWrite 0c6f,ff,0,7
SPIWrite 0c6e,f0,0,7
SPIWrite 0c6d,00,0,7
SPIWrite 0c6c,00,0,7
SPIWrite 0c77,ff,0,7
SPIWrite 0c76,e0,0,7
SPIWrite 0c75,00,0,7
SPIWrite 0c74,00,0,7
SPIWrite 0c7f,ff,0,7
SPIWrite 0c7e,c0,0,7
SPIWrite 0c7d,00,0,7
SPIWrite 0c7c,00,0,7
SPIWrite 0c87,ff,0,7
SPIWrite 0c86,80,0,7
SPIWrite 0c85,00,0,7
SPIWrite 0c84,00,0,7
SPIWrite 0c8f,ff,0,7
SPIWrite 0c8e,00,0,7
SPIWrite 0c8d,00,0,7

SPIWrite 0c8c,00,0,7
SPIWrite 0c97,fe,0,7
SPIWrite 0c96,00,0,7
SPIWrite 0c95,00,0,7
SPIWrite 0c94,00,0,7
SPIWrite 0c9f,fc,0,7
SPIWrite 0c9e,00,0,7
SPIWrite 0c9d,00,0,7
SPIWrite 0c9c,00,0,7
SPIWrite 0ca7,f8,0,7
SPIWrite 0ca6,00,0,7
SPIWrite 0ca5,00,0,7
SPIWrite 0ca4,00,0,7
SPIWrite 0caf,f0,0,7
SPIWrite 0cae,00,0,7
SPIWrite 0cad,00,0,7
SPIWrite 0cac,00,0,7
SPIWrite 0cb7,e0,0,7
SPIWrite 0cb6,00,0,7
SPIWrite 0cb5,00,0,7
SPIWrite 0cb4,00,0,7
SPIWrite 0cbf,c0,0,7
SPIWrite 0cbe,00,0,7
SPIWrite 0cbd,00,0,7
SPIWrite 0cbc,00,0,7
SPIWrite 0cc7,80,0,7
SPIWrite 0cc6,00,0,7
SPIWrite 0cc5,00,0,7
SPIWrite 0cc4,00,0,7
SPIWrite 0ccf,00,0,7
SPIWrite 0cce,00,0,7
SPIWrite 0ccd,00,0,7
SPIWrite 0ccc,00,0,7
SPIWrite 0cd7,00,0,7
SPIWrite 0cd6,00,0,7
SPIWrite 0cd5,00,0,7
SPIWrite 0cd4,00,0,7
SPIWrite 0cdf,00,0,7
SPIWrite 0cde,00,0,7
SPIWrite 0cdd,00,0,7
SPIWrite 0cdc,00,0,7
SPIWrite 0ce7,00,0,7
SPIWrite 0ce6,00,0,7
SPIWrite 0ce5,00,0,7
SPIWrite 0ce4,00,0,7
SPIWrite 0cef,00,0,7
SPIWrite 0cee,00,0,7
SPIWrite 0ced,00,0,7
SPIWrite 0cec,00,0,7
SPIWrite 0cf7,00,0,7
SPIWrite 0cf6,00,0,7
SPIWrite 0cf5,00,0,7
SPIWrite 0cf4,00,0,7
SPIWrite 0cff,00,0,7
SPIWrite 0cfe,00,0,7
SPIWrite 0cfd,00,0,7
SPIWrite 0cfc,00,0,7
SPIWrite 0d07,00,0,7
SPIWrite 0d06,00,0,7
SPIWrite 0d05,00,0,7
SPIWrite 0d04,00,0,7
SPIWrite 09a2,00,0,7
SPIWrite 09a2,01,0,7
SPIWrite 09a2,00,0,7
SPIWrite 0bc2,00,0,7
SPIWrite 0bc2,01,0,7
SPIWrite 0bc2,00,0,7
SPIWrite 0101,18,0,7
SPIWrite 0121,18,0,7

SPIWrite 0103,01,0,7
SPIWrite 0123,01,0,7
SPIWrite 0100,01,0,7
SPIWrite 0102,00,0,7
SPIWrite 0102,01,0,7
SPIWrite 0102,00,0,7
SPIWrite 0100,00,0,7
SPIWrite 0102,00,0,7
SPIWrite 0102,01,0,7
SPIWrite 0102,00,0,7
SPIWrite 0120,01,0,7
SPIWrite 0122,00,0,7
SPIWrite 0122,01,0,7
SPIWrite 0122,00,0,7
SPIWrite 0120,00,0,7
SPIWrite 0122,00,0,7
SPIWrite 0122,01,0,7
SPIWrite 0122,00,0,7
SPIWrite 0103,00,0,7
SPIWrite 0123,00,0,7
SPIWrite 0155,0f,0,7
SPIWrite 0994,01,0,7
SPIWrite 0bb4,01,0,7
SPIWrite 0df2,00,0,7
SPIWrite 0df1,00,0,7
SPIWrite 0df0,00,0,7
SPIWrite 0df6,00,0,7
SPIWrite 0df5,84,0,7
SPIWrite 0df4,88,0,7
SPIWrite 0dfa,01,0,7
SPIWrite 0df9,10,0,7
SPIWrite 0df8,a0,0,7
SPIWrite 0dfe,02,0,7
SPIWrite 0dfd,98,0,7
SPIWrite 0dfc,b9,0,7
SPIWrite 0e02,04,0,7
SPIWrite 0e01,20,0,7
SPIWrite 0e00,df,0,7
SPIWrite 0e06,06,0,7
SPIWrite 0e05,25,0,7
SPIWrite 0e04,12,0,7
SPIWrite 0e0a,08,0,7
SPIWrite 0e09,a9,0,7
SPIWrite 0e08,41,0,7
SPIWrite 0e0e,0b,0,7
SPIWrite 0e0d,31,0,7
SPIWrite 0e0c,5e,0,7
SPIWrite 0e12,0f,0,7
SPIWrite 0e11,31,0,7
SPIWrite 0e10,92,0,7
SPIWrite 0e16,15,0,7
SPIWrite 0e15,b5,0,7
SPIWrite 0e14,98,0,7
SPIWrite 0e1a,20,0,7
SPIWrite 0e19,ba,0,7
SPIWrite 0e18,de,0,7
SPIWrite 0e1e,25,0,7
SPIWrite 0e1d,42,0,7
SPIWrite 0e1c,c0,0,7
SPIWrite 0e22,25,0,7
SPIWrite 0e21,4a,0,7
SPIWrite 0e20,d8,0,7
SPIWrite 0e26,25,0,7
SPIWrite 0e25,4b,0,7
SPIWrite 0e24,00,0,7
SPIWrite 0e2a,25,0,7
SPIWrite 0e29,53,0,7
SPIWrite 0e28,15,0,7
SPIWrite 0e2e,25,0,7

SPIWrite 0e2d,57,0,7
SPIWrite 0e2c,2b,0,7
SPIWrite 0e32,25,0,7
SPIWrite 0e31,5b,0,7
SPIWrite 0e30,40,0,7
SPIWrite 0e36,25,0,7
SPIWrite 0e35,5f,0,7
SPIWrite 0e34,53,0,7
SPIWrite 0e3a,25,0,7
SPIWrite 0e39,63,0,7
SPIWrite 0e38,64,0,7
SPIWrite 0e3e,25,0,7
SPIWrite 0e3d,67,0,7
SPIWrite 0e3c,74,0,7
SPIWrite 0e42,25,0,7
SPIWrite 0e41,6b,0,7
SPIWrite 0e40,81,0,7
SPIWrite 0e46,25,0,7
SPIWrite 0e45,6b,0,7
SPIWrite 0e44,81,0,7
SPIWrite 0e4a,25,0,7
SPIWrite 0e49,6b,0,7
SPIWrite 0e48,81,0,7
SPIWrite 0e4e,25,0,7
SPIWrite 0e4d,6b,0,7
SPIWrite 0e4c,81,0,7
SPIWrite 0e52,25,0,7
SPIWrite 0e51,6b,0,7
SPIWrite 0e50,81,0,7
SPIWrite 0e56,25,0,7
SPIWrite 0e55,6b,0,7
SPIWrite 0e54,81,0,7
SPIWrite 0e5a,25,0,7
SPIWrite 0e59,6b,0,7
SPIWrite 0e58,81,0,7
SPIWrite 0e5e,25,0,7
SPIWrite 0e5d,6b,0,7
SPIWrite 0e5c,81,0,7
SPIWrite 0e62,25,0,7
SPIWrite 0e61,6b,0,7
SPIWrite 0e60,81,0,7
SPIWrite 0e66,25,0,7
SPIWrite 0e65,6b,0,7
SPIWrite 0e64,81,0,7
SPIWrite 0e6a,25,0,7
SPIWrite 0e69,6b,0,7
SPIWrite 0e68,81,0,7
SPIWrite 0e6e,25,0,7
SPIWrite 0e6d,6b,0,7
SPIWrite 0e6c,81,0,7
SPIWrite 0dd1,01,0,7

\\END: Done Top & DSA initialization

SPIWrite 0016,00,0,7 //PAGE: dsa=0x0; Address(0x16[5:4],)
\\ STEP: clkMuxConfig/step0

\\START: Configuring PLL Mux

SPIWrite 0014,01,0,7 //PAGE: clktop_2t=0x1; Address(0x14[1:0],)
SPIWrite 0064,00,0,7 //CTL_SEL_CLK_MUX_CROSS_PLL_TOP_1P8_L=0x0; (Meaning:);
Address(0x64[0:0],)
SPIWrite 0064,02,0,7 //CTL_SEL_CLK_MUX_CROSS_PLL_TOP_1P8_R=0x1; (Meaning:);
Address(0x64[1:1],)
SPIWrite 0062,01,0,7 //CTL_SEL_CLK_MUX_CROSS_PLL_BOT_L=0x1; (Meaning:); Address(0x60[16:16],)
SPIWrite 0062,03,0,7 //CTL_SEL_CLK_MUX_CROSS_PLL_BOT_R=0x1; (Meaning:); Address(0x60[17:17],)
SPIWrite 0070,04,0,7 //FORCE_PDN_GBL_RXCML_L=0x1; Address(0x70[2:2],)
SPIWrite 0070,0c,0,7 //FORCE_PDN_GBL_RXCML_R=0x1; Address(0x70[3:3],)
SPIWrite 0071,00,0,7 //SEL_CLK_MUX_RX_1P8_L=0x0; (Meaning:); Address(0x70[8:8],)

```
SPIWrite 0071,02,0,7 //SEL_CLK_MUX_RX_1P8_R=0x1; (Meaning: ); Address(0x70[9:9],)
SPIWrite 0071,02,0,7 //SEL_CLK_MUX_TX_1P8_L=0x0; (Meaning: ); Address(0x70[10:10],)
SPIWrite 0071,0a,0,7 //SEL_CLK_MUX_TX_1P8_R=0x1; (Meaning: ); Address(0x70[11:11],)
SPIWrite 0071,2a,0,7 //SEL_PDN_FAST_MUX_31_CROSS_1P8=0x1; Address(0x70[13:13],)
SPIWrite 0060,01,0,7 //CTL_EN_CROSS_PLL_BLOCK_1P8=0x1; (Meaning: ); Address(0x60[0:0],)
SPIWrite 0060,03,0,7 //CTL_SEL_CROSS_PLL_DIR_1P8=0x1; (Meaning: ); Address(0x60[1:1],)
SPIWrite 0014,00,0,7 //PAGE: clktop_2t=0x0; Address(0x14[1:0],)
SPIWrite 0018,01,0,7 //PAGE: ana_4t4r=0x1; (Meaning: ); Address(0x18[0:0],)
SPIWrite 0050,00,0,7
SPIWrite 0054,00,0,7
SPIWrite 0018,00,0,7 //PAGE: ana_4t4r=0x0; (Meaning: ); Address(0x18[0:0],)
SPIWrite 0011,10,0,7 //PAGE: rx_ec_q=0x1; Address(0x11[7:4],)
SPIWrite 0f0d,20,0,7
SPIWrite 0011,20,0,7 //PAGE: rx_ec_q=0x2; Address(0x11[7:4],)
SPIWrite 0f0d,20,0,7
SPIWrite 0011,40,0,7 //PAGE: rx_ec_q=0x4; Address(0x11[7:4],)
SPIWrite 0f0d,20,0,7
SPIWrite 0011,80,0,7 //PAGE: rx_ec_q=0x8; Address(0x11[7:4],)
SPIWrite 0f0d,20,0,7
SPIWrite 0011,00,0,7 //PAGE: rx_ec_q=0x0; Address(0x11[7:4],)
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0a00,01,0,7 //use_register_values_for_refclk_trim=0x1; Address(0xa00[0:0],)
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0014,01,0,7 //PAGE: clktop_2t=0x1; Address(0x14[1:0],)
SPIWrite 0065,08,0,7 //CTL_LOWPOWER_MODE_REFINBUF_1P8=0x1; Address(0x64[11:11],)
SPIWrite 0065,18,0,7 //CTL_LOWPOWER_MODE_SYSREFBUF_1P8=0x1; Address(0x64[12:12],)
```

\\END: Done configuring PLL Mux

```
SPIWrite 0014,00,0,7 //PAGE: clktop_2t=0x0; Address(0x14[1:0],)
\\ STEP: serdesConfig/step0
```

\\START: Enabling access to SERDES

```
SPIWrite 0015,08,0,7 //PAGE: jesd_subchip=0x1; (Meaning: ); Address(0x15[3:3],)
SPIWrite 0020,07,0,7
SPIWrite 0020,07,0,7
SPIWrite 0021,12,0,7
SPIWrite 0022,12,0,7
SPIWrite 0021,12,0,7
SPIWrite 0022,12,0,7
SPIWrite 0021,12,0,7
SPIWrite 0022,12,0,7
SPIWrite 0015,00,0,7 //PAGE: jesd_subchip=0x0; (Meaning: ); Address(0x15[3:3],)
SPIWrite 0015,04,0,7 //PAGE: SERDES=0x1; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,04,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE3=0x0; Address(0x9803[3:3],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE2=0x0; Address(0x9803[2:2],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE1=0x0; Address(0x9803[1:1],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE0=0x0; Address(0x9803[0:0],)
SPIWrite 7006,00,0,7
SPIWrite 0015,00,0,7 //PAGE: SERDES=0x2; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,40,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE3=0x0; Address(0x9803[3:3],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE2=0x0; Address(0x9803[2:2],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE1=0x0; Address(0x9803[1:1],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE0=0x0; Address(0x9803[0:0],)
SPIWrite 7006,00,0,7
```

\\END: Done enabling access to SERDES

\\Forcing the sync pins to high to ensure random data for SERDES adaptation.

```

SPIWrite 0015,40,0,7 //PAGE: SERDES=0x0; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,00,0,7
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1;(Meaning: ); Address(0x15[7:7],)
SPIWrite 0520,02,0,7 //buf_dir_ctrl_gpio_72=0x2; Address(0x520[1:0],)
SPIWrite 0520,02,0,7 //preferred_input_sel_gpio_72=0x0; Address(0x520[4:2],)
SPIWrite 0520,22,0,7 //preferred_output_sel_gpio_72=0x1; Address(0x520[7:5],)
SPIWrite 0521,00,0,7 //crossbar_sel_input_gpio_72=0x0; Address(0x520[15:8],)
SPIWrite 0522,00,0,7 //crossbar_sel_output_gpio_72=0x0; Address(0x520[23:16],)
SPIWrite 0523,00,0,7 //activ_bir_dir_ctrl_gpio_72=0x0; Address(0x520[27:25],)
SPIWrite 0528,02,0,7 //buf_dir_ctrl_gpio_74=0x2; Address(0x528[1:0],)
SPIWrite 0528,02,0,7 //preferred_input_sel_gpio_74=0x0; Address(0x528[4:2],)
SPIWrite 0528,22,0,7 //preferred_output_sel_gpio_74=0x1; Address(0x528[7:5],)
SPIWrite 0529,00,0,7 //crossbar_sel_input_gpio_74=0x0; Address(0x528[15:8],)
SPIWrite 052a,00,0,7 //crossbar_sel_output_gpio_74=0x0; Address(0x528[23:16],)
SPIWrite 052b,00,0,7 //activ_bir_dir_ctrl_gpio_74=0x0; Address(0x528[27:25],)
SPIWrite 04bc,02,0,7 //buf_dir_ctrl_gpio_47=0x2; Address(0x4bc[1:0],)
SPIWrite 04bc,02,0,7 //preferred_input_sel_gpio_47=0x0; Address(0x4bc[4:2],)
SPIWrite 04bc,22,0,7 //preferred_output_sel_gpio_47=0x1; Address(0x4bc[7:5],)
SPIWrite 04bd,00,0,7 //crossbar_sel_input_gpio_47=0x0; Address(0x4bc[15:8],)
SPIWrite 04be,00,0,7 //crossbar_sel_output_gpio_47=0x0; Address(0x4bc[23:16],)
SPIWrite 04bf,00,0,7 //activ_bir_dir_ctrl_gpio_47=0x0; Address(0x4bc[27:25],)
SPIWrite 04c4,02,0,7 //buf_dir_ctrl_gpio_49=0x2; Address(0x4c4[1:0],)
SPIWrite 04c4,02,0,7 //preferred_input_sel_gpio_49=0x0; Address(0x4c4[4:2],)
SPIWrite 04c4,22,0,7 //preferred_output_sel_gpio_49=0x1; Address(0x4c4[7:5],)
SPIWrite 04c5,00,0,7 //crossbar_sel_input_gpio_49=0x0; Address(0x4c4[15:8],)
SPIWrite 04c6,00,0,7 //crossbar_sel_output_gpio_49=0x0; Address(0x4c4[23:16],)
SPIWrite 04c7,00,0,7 //activ_bir_dir_ctrl_gpio_49=0x0; Address(0x4c4[27:25],)
SPIWrite 039c,00,0,7 //sync_lvds_mode_ab=0x0;(Meaning: ); Address(0x39c[0:0],)
SPIWrite 039c,00,0,7 //sync_lvds_mode_cd=0x0;(Meaning: ); Address(0x39c[1:1],)
SPIWrite 0555,02,0,7 //ovr_sel_intpo_dac_sync_n_ab_0=0x1; Address(0x554[9:9],)
SPIWrite 0555,0a,0,7 //ovr_sel_intpo_dac_sync_n_ab_1=0x1; Address(0x554[11:11],)
SPIWrite 0555,2a,0,7 //ovr_sel_intpo_dac_sync_n_cd_0=0x1; Address(0x554[13:13],)
SPIWrite 0555,aa,0,7 //ovr_sel_intpo_dac_sync_n_cd_1=0x1; Address(0x554[15:15],)
SPIWrite 0555,ba,0,7 //ovr_intpo_dac_sync_n_cd_0=0x1; Address(0x554[12:12],)
SPIWrite 0555,bb,0,7 //ovr_intpo_dac_sync_n_ab_0=0x1; Address(0x554[8:8],)
SPIWrite 0555,bf,0,7 //ovr_intpo_dac_sync_n_ab_1=0x1; Address(0x554[10:10],)
SPIWrite 0555,ff,0,7 //ovr_intpo_dac_sync_n_cd_1=0x1; Address(0x554[14:14],)
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0;(Meaning: ); Address(0x15[7:7],)
\\ STEP: serdesConfig/step1

```

\\START: Resetting Serdes

```

SPIWrite 0015,04,0,7 //PAGE: SERDES=0x1; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,04,0,7
SPIWrite 701b,08,0,7 //DOMAIN_RESET=0x888; Address(0x980d[11:0],)
SPIWrite 701a,88,0,7
SPIWrite 701b,00,0,7 //DOMAIN_RESET=0x0; Address(0x980d[11:0],)
SPIWrite 701a,00,0,7

```

WAIT 0.1

```

SPIWrite 701b,07,0,7 //DOMAIN_RESET=0x777; Address(0x980d[11:0],)
SPIWrite 701a,77,0,7
SPIWrite 701b,00,0,7 //DOMAIN_RESET=0x0; Address(0x980d[11:0],)
SPIWrite 701a,00,0,7

```

WAIT 0.1

\\END: Done resetting Serdes

\\START: Resetting Serdes

```

SPIWrite 0015,00,0,7 //PAGE: SERDES=0x2; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,40,0,7
SPIWrite 701b,08,0,7 //DOMAIN_RESET=0x888; Address(0x980d[11:0],)
SPIWrite 701a,88,0,7

```

```
SPIWrite 701b,00,0,7    //DOMAIN_RESET=0x0;    Address(0x980d[11:0],)
SPIWrite 701a,00,0,7
```

WAIT 0.1

```
SPIWrite 701b,07,0,7    //DOMAIN_RESET=0x777;    Address(0x980d[11:0],)
SPIWrite 701a,77,0,7
SPIWrite 701b,00,0,7    //DOMAIN_RESET=0x0;    Address(0x980d[11:0],)
SPIWrite 701a,00,0,7
```

WAIT 0.1

\\END: Done resetting Serdes

```
SPIWrite 0015,40,0,7    //PAGE: SERDES=0x0;    Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,00,0,7
\\ STEP: serdesConfig/step2
```

\\START: Configuring the SERDES

```
SPIWrite 0015,08,0,7    //PAGE: jesd_subchip=0x1;(Meaning: );    Address(0x15[3:3],)
SPIWrite 0021,12,0,7
SPIWrite 0022,12,0,7
SPIWrite 0015,00,0,7    //PAGE: jesd_subchip=0x0;(Meaning: );    Address(0x15[3:3],)
SPIWrite 0015,04,0,7    //PAGE: SERDES=0x3;    Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,44,0,7
SPIWrite 702f,c0,0,7
SPIWrite 702e,00,0,7
```

WAIT 0.01

```
SPIWrite 7029,ff,0,7
SPIWrite 7028,f0,0,7
SPIWrite 701b,0a,0,7
SPIWrite 701a,aa,0,7
SPIWrite 701b,00,0,7
SPIWrite 701a,00,0,7
```

WAIT 0.01

```
SPIWrite 702f,00,0,7
SPIWrite 702e,00,0,7
SPIWrite 7007,00,0,7
SPIWrite 7006,00,0,7
SPIWrite 49f1,92,0,7
SPIWrite 49f0,40,0,7
SPIWrite 49f3,ea,0,7
SPIWrite 49f2,80,0,7
SPIWrite 49e3,f0,0,7
SPIWrite 49e2,00,0,7
SPIWrite 49b5,47,0,7
SPIWrite 49b4,47,0,7
SPIWrite 49ff,fd,0,7
SPIWrite 49fe,b0,0,7
SPIWrite 49ed,1d,0,7
SPIWrite 49ec,c0,0,7
SPIWrite 49e7,52,0,7
SPIWrite 49e6,36,0,7
SPIWrite 49e5,6e,0,7
SPIWrite 49e4,b6,0,7
SPIWrite 49df,a8,0,7
SPIWrite 49de,28,0,7
SPIWrite 49eb,0a,0,7
SPIWrite 49ea,9e,0,7
SPIWrite 49e9,72,0,7
SPIWrite 49e8,44,0,7
SPIWrite 49fd,0a,0,7
SPIWrite 49fc,9e,0,7
```


SPIWrite 49fb,72,0,7
SPIWrite 49fa,48,0,7
SPIWrite 49f9,4a,0,7
SPIWrite 49f8,66,0,7
SPIWrite 49f7,79,0,7
SPIWrite 49f6,b6,0,7
SPIWrite 49d9,6c,0,7
SPIWrite 49d8,06,0,7
SPIWrite 4601,10,0,7
SPIWrite 4600,6b,0,7
SPIWrite 4603,64,0,7
SPIWrite 4602,80,0,7
SPIWrite 4605,62,0,7
SPIWrite 4604,00,0,7
SPIWrite 4607,7b,0,7
SPIWrite 4606,33,0,7
SPIWrite 4609,70,0,7
SPIWrite 4608,0a,0,7
SPIWrite 460b,bd,0,7
SPIWrite 460a,68,0,7
SPIWrite 460d,76,0,7
SPIWrite 460c,2d,0,7
SPIWrite 460f,66,0,7
SPIWrite 460e,ab,0,7
SPIWrite 4611,d0,0,7
SPIWrite 4610,08,0,7
SPIWrite 4613,00,0,7
SPIWrite 4612,18,0,7
SPIWrite 4615,66,0,7
SPIWrite 4614,2c,0,7
SPIWrite 4617,3d,0,7
SPIWrite 4616,15,0,7
SPIWrite 4619,00,0,7
SPIWrite 4618,80,0,7
SPIWrite 461b,00,0,7
SPIWrite 461a,02,0,7
SPIWrite 461d,34,0,7
SPIWrite 461c,00,0,7
SPIWrite 461f,00,0,7
SPIWrite 461e,00,0,7
SPIWrite 4621,10,0,7
SPIWrite 4620,1a,0,7
SPIWrite 4639,03,0,7
SPIWrite 4638,40,0,7
SPIWrite 463b,00,0,7
SPIWrite 463a,60,0,7
SPIWrite 463d,00,0,7
SPIWrite 463c,00,0,7
SPIWrite 463f,00,0,7
SPIWrite 463e,00,0,7
SPIWrite 4677,00,0,7
SPIWrite 4676,00,0,7
SPIWrite 4679,00,0,7
SPIWrite 4678,00,0,7
SPIWrite 467b,00,0,7
SPIWrite 467a,00,0,7
SPIWrite 467d,00,0,7
SPIWrite 467c,00,0,7
SPIWrite 4683,9f,0,7
SPIWrite 4682,df,0,7
SPIWrite 4685,b3,0,7
SPIWrite 4684,c0,0,7
SPIWrite 468f,24,0,7
SPIWrite 468e,a2,0,7
SPIWrite 4691,cc,0,7
SPIWrite 4690,34,0,7
SPIWrite 4693,e3,0,7
SPIWrite 4692,d7,0,7
SPIWrite 4695,76,0,7

SPIWrite 4694,60,0,7
SPIWrite 4697,06,0,7
SPIWrite 4696,db,0,7
SPIWrite 4741,03,0,7
SPIWrite 4740,41,0,7
SPIWrite 47e7,00,0,7
SPIWrite 47e6,80,0,7
SPIWrite 47e9,fc,0,7
SPIWrite 47e8,00,0,7
SPIWrite 47eb,9f,0,7
SPIWrite 47ea,fe,0,7
SPIWrite 47ed,00,0,7
SPIWrite 47ec,60,0,7
SPIWrite 47ef,10,0,7
SPIWrite 47ee,00,0,7
SPIWrite 47f1,68,0,7
SPIWrite 47f0,64,0,7
SPIWrite 47f3,92,0,7
SPIWrite 47f2,30,0,7
SPIWrite 47f5,00,0,7
SPIWrite 47f4,00,0,7
SPIWrite 47f7,6d,0,7
SPIWrite 47f6,87,0,7
SPIWrite 47f9,6d,0,7
SPIWrite 47f8,b6,0,7
SPIWrite 47fb,42,0,7
SPIWrite 47fa,46,0,7
SPIWrite 47fd,62,0,7
SPIWrite 47fc,7c,0,7
SPIWrite 47ff,88,0,7
SPIWrite 47fe,c8,0,7
SPIWrite 4401,10,0,7
SPIWrite 4400,6b,0,7
SPIWrite 4403,64,0,7
SPIWrite 4402,80,0,7
SPIWrite 4405,62,0,7
SPIWrite 4404,00,0,7
SPIWrite 4407,7b,0,7
SPIWrite 4406,33,0,7
SPIWrite 4409,70,0,7
SPIWrite 4408,0a,0,7
SPIWrite 440b,bd,0,7
SPIWrite 440a,68,0,7
SPIWrite 440d,76,0,7
SPIWrite 440c,2d,0,7
SPIWrite 440f,66,0,7
SPIWrite 440e,ab,0,7
SPIWrite 4411,d0,0,7
SPIWrite 4410,08,0,7
SPIWrite 4413,00,0,7
SPIWrite 4412,18,0,7
SPIWrite 4415,66,0,7
SPIWrite 4414,2c,0,7
SPIWrite 4417,3d,0,7
SPIWrite 4416,15,0,7
SPIWrite 4419,00,0,7
SPIWrite 4418,80,0,7
SPIWrite 441b,00,0,7
SPIWrite 441a,02,0,7
SPIWrite 441d,34,0,7
SPIWrite 441c,00,0,7
SPIWrite 441f,00,0,7
SPIWrite 441e,00,0,7
SPIWrite 4421,10,0,7
SPIWrite 4420,1a,0,7
SPIWrite 4439,03,0,7
SPIWrite 4438,40,0,7
SPIWrite 443b,00,0,7
SPIWrite 443a,60,0,7

SPIWrite 443d,00,0,7
SPIWrite 443c,00,0,7
SPIWrite 443f,00,0,7
SPIWrite 443e,00,0,7
SPIWrite 4477,00,0,7
SPIWrite 4476,00,0,7
SPIWrite 4479,00,0,7
SPIWrite 4478,00,0,7
SPIWrite 447b,00,0,7
SPIWrite 447a,00,0,7
SPIWrite 447d,00,0,7
SPIWrite 447c,00,0,7
SPIWrite 4483,9f,0,7
SPIWrite 4482,df,0,7
SPIWrite 4485,b3,0,7
SPIWrite 4484,c0,0,7
SPIWrite 448f,24,0,7
SPIWrite 448e,a2,0,7
SPIWrite 4491,cc,0,7
SPIWrite 4490,34,0,7
SPIWrite 4493,e3,0,7
SPIWrite 4492,d7,0,7
SPIWrite 4495,76,0,7
SPIWrite 4494,60,0,7
SPIWrite 4497,06,0,7
SPIWrite 4496,db,0,7
SPIWrite 4541,03,0,7
SPIWrite 4540,41,0,7
SPIWrite 45e7,00,0,7
SPIWrite 45e6,80,0,7
SPIWrite 45e9,fc,0,7
SPIWrite 45e8,00,0,7
SPIWrite 45eb,9f,0,7
SPIWrite 45ea,fe,0,7
SPIWrite 45ed,00,0,7
SPIWrite 45ec,00,0,7
SPIWrite 45ef,10,0,7
SPIWrite 45ee,00,0,7
SPIWrite 45f1,68,0,7
SPIWrite 45f0,64,0,7
SPIWrite 45f3,92,0,7
SPIWrite 45f2,30,0,7
SPIWrite 45f5,00,0,7
SPIWrite 45f4,00,0,7
SPIWrite 45f7,6d,0,7
SPIWrite 45f6,87,0,7
SPIWrite 45f9,6d,0,7
SPIWrite 45f8,b6,0,7
SPIWrite 45fb,42,0,7
SPIWrite 45fa,46,0,7
SPIWrite 45fd,62,0,7
SPIWrite 45fc,7c,0,7
SPIWrite 45ff,88,0,7
SPIWrite 45fe,c8,0,7
SPIWrite 4001,10,0,7
SPIWrite 4000,6b,0,7
SPIWrite 4003,64,0,7
SPIWrite 4002,80,0,7
SPIWrite 4005,62,0,7
SPIWrite 4004,00,0,7
SPIWrite 4007,7b,0,7
SPIWrite 4006,33,0,7
SPIWrite 4009,70,0,7
SPIWrite 4008,0a,0,7
SPIWrite 400b,bd,0,7
SPIWrite 400a,68,0,7
SPIWrite 400d,76,0,7
SPIWrite 400c,2d,0,7
SPIWrite 400f,66,0,7

SPIWrite 400e,ab,0,7
SPIWrite 4011,d0,0,7
SPIWrite 4010,08,0,7
SPIWrite 4013,00,0,7
SPIWrite 4012,18,0,7
SPIWrite 4015,66,0,7
SPIWrite 4014,2c,0,7
SPIWrite 4017,3d,0,7
SPIWrite 4016,15,0,7
SPIWrite 4019,00,0,7
SPIWrite 4018,80,0,7
SPIWrite 401b,00,0,7
SPIWrite 401a,02,0,7
SPIWrite 401d,34,0,7
SPIWrite 401c,00,0,7
SPIWrite 401f,00,0,7
SPIWrite 401e,00,0,7
SPIWrite 4021,10,0,7
SPIWrite 4020,1a,0,7
SPIWrite 4039,03,0,7
SPIWrite 4038,40,0,7
SPIWrite 403b,00,0,7
SPIWrite 403a,60,0,7
SPIWrite 403d,00,0,7
SPIWrite 403c,00,0,7
SPIWrite 403f,00,0,7
SPIWrite 403e,00,0,7
SPIWrite 4077,00,0,7
SPIWrite 4076,00,0,7
SPIWrite 4079,00,0,7
SPIWrite 4078,00,0,7
SPIWrite 407b,00,0,7
SPIWrite 407a,00,0,7
SPIWrite 407d,00,0,7
SPIWrite 407c,00,0,7
SPIWrite 4083,9f,0,7
SPIWrite 4082,df,0,7
SPIWrite 4085,b3,0,7
SPIWrite 4084,c0,0,7
SPIWrite 408f,24,0,7
SPIWrite 408e,a2,0,7
SPIWrite 4091,cc,0,7
SPIWrite 4090,34,0,7
SPIWrite 4093,e3,0,7
SPIWrite 4092,d7,0,7
SPIWrite 4095,76,0,7
SPIWrite 4094,60,0,7
SPIWrite 4097,06,0,7
SPIWrite 4096,db,0,7
SPIWrite 4141,03,0,7
SPIWrite 4140,41,0,7
SPIWrite 41e7,00,0,7
SPIWrite 41e6,80,0,7
SPIWrite 41e9,fc,0,7
SPIWrite 41e8,00,0,7
SPIWrite 41eb,9f,0,7
SPIWrite 41ea,fe,0,7
SPIWrite 41ed,00,0,7
SPIWrite 41ec,00,0,7
SPIWrite 41ef,10,0,7
SPIWrite 41ee,00,0,7
SPIWrite 41f1,68,0,7
SPIWrite 41f0,64,0,7
SPIWrite 41f3,92,0,7
SPIWrite 41f2,30,0,7
SPIWrite 41f5,00,0,7
SPIWrite 41f4,00,0,7
SPIWrite 41f7,6d,0,7
SPIWrite 41f6,87,0,7

SPIWrite 41f9,6d,0,7
SPIWrite 41f8,b6,0,7
SPIWrite 41fb,42,0,7
SPIWrite 41fa,46,0,7
SPIWrite 41fd,62,0,7
SPIWrite 41fc,7c,0,7
SPIWrite 41ff,88,0,7
SPIWrite 41fe,c8,0,7
SPIWrite 4201,10,0,7
SPIWrite 4200,6b,0,7
SPIWrite 4203,64,0,7
SPIWrite 4202,80,0,7
SPIWrite 4205,62,0,7
SPIWrite 4204,00,0,7
SPIWrite 4207,7b,0,7
SPIWrite 4206,33,0,7
SPIWrite 4209,70,0,7
SPIWrite 4208,0a,0,7
SPIWrite 420b,bd,0,7
SPIWrite 420a,68,0,7
SPIWrite 420d,76,0,7
SPIWrite 420c,2d,0,7
SPIWrite 420f,66,0,7
SPIWrite 420e,ab,0,7
SPIWrite 4211,d0,0,7
SPIWrite 4210,08,0,7
SPIWrite 4213,00,0,7
SPIWrite 4212,18,0,7
SPIWrite 4215,66,0,7
SPIWrite 4214,2c,0,7
SPIWrite 4217,3d,0,7
SPIWrite 4216,15,0,7
SPIWrite 4219,00,0,7
SPIWrite 4218,80,0,7
SPIWrite 421b,00,0,7
SPIWrite 421a,02,0,7
SPIWrite 421d,34,0,7
SPIWrite 421c,00,0,7
SPIWrite 421f,00,0,7
SPIWrite 421e,00,0,7
SPIWrite 4221,10,0,7
SPIWrite 4220,1a,0,7
SPIWrite 4239,03,0,7
SPIWrite 4238,40,0,7
SPIWrite 423b,00,0,7
SPIWrite 423a,60,0,7
SPIWrite 423d,00,0,7
SPIWrite 423c,00,0,7
SPIWrite 423f,00,0,7
SPIWrite 423e,00,0,7
SPIWrite 4277,00,0,7
SPIWrite 4276,00,0,7
SPIWrite 4279,00,0,7
SPIWrite 4278,00,0,7
SPIWrite 427b,00,0,7
SPIWrite 427a,00,0,7
SPIWrite 427d,00,0,7
SPIWrite 427c,00,0,7
SPIWrite 4283,9f,0,7
SPIWrite 4282,df,0,7
SPIWrite 4285,b3,0,7
SPIWrite 4284,c0,0,7
SPIWrite 428f,24,0,7
SPIWrite 428e,a2,0,7
SPIWrite 4291,cc,0,7
SPIWrite 4290,34,0,7
SPIWrite 4293,e3,0,7
SPIWrite 4292,d7,0,7
SPIWrite 4295,76,0,7

```
SPIWrite 4294,60,0,7
SPIWrite 4297,06,0,7
SPIWrite 4296,db,0,7
SPIWrite 4341,03,0,7
SPIWrite 4340,41,0,7
SPIWrite 43e7,00,0,7
SPIWrite 43e6,80,0,7
SPIWrite 43e9,fc,0,7
SPIWrite 43e8,00,0,7
SPIWrite 43eb,9f,0,7
SPIWrite 43ea,fe,0,7
SPIWrite 43ed,00,0,7
SPIWrite 43ec,00,0,7
SPIWrite 43ef,10,0,7
SPIWrite 43ee,00,0,7
SPIWrite 43f1,68,0,7
SPIWrite 43f0,64,0,7
SPIWrite 43f3,92,0,7
SPIWrite 43f2,30,0,7
SPIWrite 43f5,00,0,7
SPIWrite 43f4,00,0,7
SPIWrite 43f7,6d,0,7
SPIWrite 43f6,87,0,7
SPIWrite 43f9,6d,0,7
SPIWrite 43f8,b6,0,7
SPIWrite 43fb,42,0,7
SPIWrite 43fa,46,0,7
SPIWrite 43fd,62,0,7
SPIWrite 43fc,7c,0,7
SPIWrite 43ff,88,0,7
SPIWrite 43fe,c8,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE0=0x0; Address(0x9803[0:0],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE1=0x0; Address(0x9803[1:1],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE2=0x0; Address(0x9803[2:2],)
SPIWrite 7006,00,0,7
SPIWrite 7007,00,0,7 //BUS_WIDTH_LANE3=0x0; Address(0x9803[3:3],)
SPIWrite 7006,00,0,7
SPIWrite 0015,40,0,7 //PAGE: SERDES=0x0; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,00,0,7
```

\\END: Done configuring the SERDES

\\ STEP: serdesConfig/step3

\\START: Loading Serdes Firmware.

```
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1; (Meaning: ); Address(0x13[4:4],)
```

```
SPIPoll 00f0,0,0,1
```

```
SPIWrite 00a3,00,0,7
SPIWrite 00a2,01,0,7
SPIWrite 00a1,03,0,7
SPIWrite 00a0,00,0,7
SPIWrite 0193,82,0,7
```

```
SPIPoll 00f0,0,7,7
```

```
SPIPoll 00f0,0,0,1
```

```
SPIWrite 00a3,00,0,7
SPIWrite 00a2,01,0,7
SPIWrite 00a1,03,0,7
SPIWrite 00a0,01,0,7
SPIWrite 0193,82,0,7
```

```
SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0; (Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;    Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;    Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;    Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;    Address(0x20[7:7],)

SPIWrite 0013,00,0,7    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
SPIWrite 0015,04,0,7    //PAGE: SERDES=0x3;    Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,44,0,7
SPIWrite 702d,00,0,7
SPIWrite 702c,05,0,7
SPIWrite 7025,00,0,7
SPIWrite 7024,08,0,7
SPIWrite 702b,e0,0,7
SPIWrite 702a,20,0,7

WAIT 0.01

SPIWrite 702d,00,0,7
SPIWrite 702c,02,0,7
SPIWrite 7025,00,0,7
SPIWrite 7024,50,0,7
SPIWrite 702b,e0,0,7
SPIWrite 702a,20,0,7

WAIT 0.01

SPIWrite 701b,07,0,7    //DOMAIN_RESET=0x777;    Address(0x980d[11:0],)
SPIWrite 701a,77,0,7
SPIWrite 701b,00,0,7    //DOMAIN_RESET=0x0;    Address(0x980d[11:0],)
SPIWrite 701a,00,0,7

WAIT 5

SPIWrite 0015,40,0,7    //PAGE: SERDES=0x0;    Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,00,0,7

\\END: Done loading Serdes Firmware.

\\ STEP: sigChainConfig/step0

\\START: Configuring JESD TX Lane Mux

SPIWrite 0015,08,0,7    //PAGE: jesd_subchip=0x1; (Meaning: );    Address(0x15[3:3],)
SPIWrite 004a,11,0,7
SPIWrite 004a,31,0,7
SPIWrite 004b,35,0,7
SPIWrite 004b,45,0,7
SPIWrite 004c,50,0,7
SPIWrite 004c,20,0,7
SPIWrite 004d,76,0,7
SPIWrite 004d,76,0,7
```

SPIWrite 004e,14,0,7
SPIWrite 004e,04,0,7
SPIWrite 004f,35,0,7
SPIWrite 004f,15,0,7
SPIWrite 0050,53,0,7
SPIWrite 0050,23,0,7
SPIWrite 0051,76,0,7
SPIWrite 0051,76,0,7

\\END: Done configuring JESD TX Lane Mux

\\START: Configuring JESD RX Lane Mux

SPIWrite 0068,14,0,7
SPIWrite 0068,24,0,7
SPIWrite 0069,30,0,7
SPIWrite 0069,10,0,7
SPIWrite 006a,55,0,7
SPIWrite 006a,35,0,7
SPIWrite 006b,76,0,7
SPIWrite 006b,76,0,7
SPIWrite 006c,14,0,7
SPIWrite 006c,24,0,7
SPIWrite 006d,30,0,7
SPIWrite 006d,10,0,7
SPIWrite 006e,55,0,7
SPIWrite 006e,35,0,7
SPIWrite 006f,76,0,7
SPIWrite 006f,76,0,7

\\END: Done configuring JESD RX Lane Mux

\\START: Configuring the Data Muxes

SPIWrite 0044,50,0,7
SPIWrite 0044,50,0,7
SPIWrite 0044,50,0,7
SPIWrite 0044,50,0,7
SPIWrite 0045,fa,0,7
SPIWrite 0045,fa,0,7
SPIWrite 0045,fa,0,7
SPIWrite 0045,fa,0,7
SPIWrite 0046,fa,0,7
SPIWrite 0046,fa,0,7
SPIWrite 0046,fa,0,7
SPIWrite 0046,fa,0,7
SPIWrite 0046,fa,0,7
SPIWrite 0047,50,0,7
SPIWrite 0047,50,0,7
SPIWrite 0047,50,0,7
SPIWrite 0047,50,0,7
SPIWrite 003a,e4,0,7
SPIWrite 003a,e4,0,7
SPIWrite 003a,e4,0,7
SPIWrite 003a,e4,0,7
SPIWrite 003b,e4,0,7
SPIWrite 003b,e4,0,7
SPIWrite 003b,e4,0,7
SPIWrite 003b,e4,0,7
SPIWrite 003c,00,0,7
SPIWrite 003c,00,0,7
SPIWrite 003d,11,0,7
SPIWrite 003d,11,0,7
SPIWrite 003e,44,0,7
SPIWrite 003e,44,0,7
SPIWrite 003f,55,0,7
SPIWrite 003f,55,0,7
SPIWrite 0036,d8,0,7

SPIWrite 0036,d8,0,7
SPIWrite 0036,d8,0,7
SPIWrite 0036,d8,0,7
SPIWrite 0037,d8,0,7
SPIWrite 0037,d8,0,7
SPIWrite 0037,d8,0,7
SPIWrite 0037,d8,0,7
SPIWrite 0040,44,0,7
SPIWrite 0040,44,0,7
SPIWrite 0041,55,0,7
SPIWrite 0041,55,0,7
SPIWrite 0042,00,0,7
SPIWrite 0042,00,0,7
SPIWrite 0043,11,0,7
SPIWrite 0043,11,0,7
SPIWrite 0038,d8,0,7
SPIWrite 0038,d8,0,7
SPIWrite 0038,d8,0,7
SPIWrite 0038,d8,0,7
SPIWrite 0039,d8,0,7
SPIWrite 0039,d8,0,7
SPIWrite 0039,d8,0,7
SPIWrite 0039,d8,0,7
SPIWrite 0039,d8,0,7
SPIWrite 0040,44,0,7
SPIWrite 0040,44,0,7
SPIWrite 0041,55,0,7
SPIWrite 0041,55,0,7
SPIWrite 0042,00,0,7
SPIWrite 0042,00,0,7
SPIWrite 0043,11,0,7
SPIWrite 0043,11,0,7

\\END: Done configuring the Data Muxes

\\START: Configuring JESD TX Sync Mux

SPIWrite 0054,a4,0,7
SPIWrite 0054,84,0,7
SPIWrite 0054,88,0,7
SPIWrite 0054,88,0,7

\\END: Done configuring JESD TX Sync Mux

\\START: Configuring JESD RX Sync Mux

SPIWrite 0061,00,0,7
SPIWrite 0061,00,0,7
SPIWrite 0061,00,0,7
SPIWrite 0061,00,0,7
SPIWrite 0055,e4,0,7
SPIWrite 0055,e0,0,7
SPIWrite 0055,e0,0,7
SPIWrite 0055,a0,0,7

\\END: Done configuring JESD RX Sync Mux

\\START: Setting FIFO and dither in JESD

SPIWrite 0032,10,0,7
SPIWrite 0035,10,0,7
SPIWrite 0030,10,0,7
SPIWrite 0031,10,0,7
SPIWrite 0033,10,0,7
SPIWrite 0034,10,0,7

\\END: Done Setting FIFO and dither in JESD

```

SPIWrite 0015,00,0,7 //PAGE: jesd_subchip=0x0;(Meaning: ); Address(0x15[3:3],)
\\ STEP: sigChainConfig/step1

\\START: Doing RX, TX and FB Digital Chain Config.

SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1;(Meaning: ); Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,01,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,01,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7
SPIWrite 0193,31,0,7

\\Read macro_opcode_operand=0x31000000; Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0;(Meaning: ); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address(0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address(0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address(0x20[6:6],)

\\Read MACRO_ERROR_IN_EXECUTION=0x0; Address(0x20[7:7],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1;(Meaning: ); Address(0x13[4:4],)
SPIWrite 0193,45,0,7

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0;(Meaning: ); Address(0x13[4:4],)
SPIWrite 0016,40,6,7 //PAGE: fb_top=0x1; Address(0x16[7:6],)
SPIWrite 015b,05,0,2 //FBAsyncFIFORdPtrVal=0x5; Address(0x158[26:24],)
SPIWrite 0016,80,6,7 //PAGE: fb_top=0x2; Address(0x16[7:6],)
SPIWrite 015b,05,0,2 //FBAsyncFIFORdPtrVal=0x5; Address(0x158[26:24],)
SPIWrite 0016,00,6,7 //PAGE: fb_top=0x0; Address(0x16[7:6],)
SPIWrite 0010,04,2,3 //PAGE: rx_top=0x1; Address(0x10[3:2],)
SPIWrite 0120,03,0,2 //RxDecAsyncFIFORdPtrLoad=0x3; Address(0x120[2:0],)
SPIWrite 0010,08,2,3 //PAGE: rx_top=0x2; Address(0x10[3:2],)
SPIWrite 0120,03,0,2 //RxDecAsyncFIFORdPtrLoad=0x3; Address(0x120[2:0],)
SPIWrite 0010,00,2,3 //PAGE: rx_top=0x0; Address(0x10[3:2],)
SPIWrite 0010,01,0,1 //PAGE: tx_top=0x1; Address(0x10[1:0],)
SPIWrite 010d,03,0,2 //RdPtrOffDigOutAsyncFIFO=0x3; Address(0x10c[10:8],)
SPIWrite 0010,02,0,1 //PAGE: tx_top=0x2; Address(0x10[1:0],)
SPIWrite 010d,03,0,2 //RdPtrOffDigOutAsyncFIFO=0x3; Address(0x10c[10:8],)
SPIWrite 0010,01,0,1 //PAGE: tx_top=0x1; Address(0x10[1:0],)
SPIWrite 010b,0d,0,3 //RdPtrOffDigInpAsyncFIFO=0xd; Address(0x108[27:24],)

```

```

SPIWrite 0138,01,0,0 //BlockEnable=0x1;(Meaning: ); Address(0x138[0:0],)
SPIWrite 0139,01,0,1 //LutUpdateMode=0x1;(Meaning: ); Address(0x138[9:8],)
SPIWrite 0148,01,0,0 //BlockEnable=0x1;(Meaning: ); Address(0x148[0:0],)
SPIWrite 0149,01,0,1 //LutUpdateMode=0x1;(Meaning: ); Address(0x148[9:8],)
SPIWrite 0010,02,0,1 //PAGE: tx_top=0x2; Address(0x10[1:0],)
SPIWrite 010b,0d,0,3 //RdPtrOffDigInpAsyncFIFO=0xd; Address(0x108[27:24],)
SPIWrite 0138,01,0,0 //BlockEnable=0x1;(Meaning: ); Address(0x138[0:0],)
SPIWrite 0139,01,0,1 //LutUpdateMode=0x1;(Meaning: ); Address(0x138[9:8],)
SPIWrite 0148,01,0,0 //BlockEnable=0x1;(Meaning: ); Address(0x148[0:0],)
SPIWrite 0149,01,0,1 //LutUpdateMode=0x1;(Meaning: ); Address(0x148[9:8],)
SPIWrite 0010,00,0,1 //PAGE: tx_top=0x0; Address(0x10[1:0],)
SPIWrite 0010,04,2,3 //PAGE: rx_top=0x1; Address(0x10[3:2],)
SPIWrite 0af9,01,0,0 //pdn_in=0x1;(Meaning: ); Address(0xaf8[8:8],)
SPIWrite 0afb,01,0,0 //pdn_in=0x1;(Meaning: ); Address(0xaf8[24:24],)
SPIWrite 0a50,33,0,7 //gain_component_n=0x33; Address(0xa50[7:0],)
SPIWrite 0a51,33,0,7 //gain_component_n=0x33; Address(0xa50[15:8],)
SPIWrite 0a52,33,0,7 //gain_component_n=0x33; Address(0xa50[23:16],)
SPIWrite 0a53,33,0,7 //gain_component_n=0x33; Address(0xa50[31:24],)
SPIWrite 0a56,0e,0,0 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[16:16],)
SPIWrite 0a56,0c,1,1 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[17:17],)
SPIWrite 0a56,08,2,2 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[18:18],)
SPIWrite 0a56,00,3,3 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[19:19],)
SPIWrite 0a54,01,0,0 //rx1_unit_gain_mode=0x1;(Meaning: ); Address(0xa54[0:0],)
SPIWrite 0a54,03,1,1 //rx2_unit_gain_mode=0x1;(Meaning: ); Address(0xa54[1:1],)
SPIWrite 0c6b,00,0,0 //coef_update_signal=0x0; Address(0xc68[24:24],)
SPIWrite 0c73,00,0,0 //coef_update_signal=0x0; Address(0xc70[24:24],)
SPIWrite 0e6b,00,0,0 //coef_update_signal=0x0; Address(0xe68[24:24],)
SPIWrite 0e73,00,0,0 //coef_update_signal=0x0; Address(0xe70[24:24],)
SPIWrite 0c6b,01,0,0 //coef_update_signal=0x1; Address(0xc68[24:24],)
SPIWrite 0c73,01,0,0 //coef_update_signal=0x1; Address(0xc70[24:24],)
SPIWrite 0e6b,01,0,0 //coef_update_signal=0x1; Address(0xe68[24:24],)
SPIWrite 0e73,01,0,0 //coef_update_signal=0x1; Address(0xe70[24:24],)
SPIWrite 0c6b,00,0,0 //coef_update_signal=0x0; Address(0xc68[24:24],)
SPIWrite 0c73,00,0,0 //coef_update_signal=0x0; Address(0xc70[24:24],)
SPIWrite 0e6b,00,0,0 //coef_update_signal=0x0; Address(0xe68[24:24],)
SPIWrite 0e73,00,0,0 //coef_update_signal=0x0; Address(0xe70[24:24],)
SPIWrite 0010,08,2,3 //PAGE: rx_top=0x2; Address(0x10[3:2],)
SPIWrite 0af9,01,0,0 //pdn_in=0x1;(Meaning: ); Address(0xaf8[8:8],)
SPIWrite 0afb,01,0,0 //pdn_in=0x1;(Meaning: ); Address(0xaf8[24:24],)
SPIWrite 0a50,33,0,7 //gain_component_n=0x33; Address(0xa50[7:0],)
SPIWrite 0a51,33,0,7 //gain_component_n=0x33; Address(0xa50[15:8],)
SPIWrite 0a52,33,0,7 //gain_component_n=0x33; Address(0xa50[23:16],)
SPIWrite 0a53,33,0,7 //gain_component_n=0x33; Address(0xa50[31:24],)
SPIWrite 0a56,0e,0,0 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[16:16],)
SPIWrite 0a56,0c,1,1 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[17:17],)
SPIWrite 0a56,08,2,2 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[18:18],)
SPIWrite 0a56,00,3,3 //gain_corrector_bypass=0x0;(Meaning: ); Address(0xa54[19:19],)
SPIWrite 0a54,01,0,0 //rx1_unit_gain_mode=0x1;(Meaning: ); Address(0xa54[0:0],)
SPIWrite 0a54,03,1,1 //rx2_unit_gain_mode=0x1;(Meaning: ); Address(0xa54[1:1],)
SPIWrite 0c6b,00,0,0 //coef_update_signal=0x0; Address(0xc68[24:24],)
SPIWrite 0c73,00,0,0 //coef_update_signal=0x0; Address(0xc70[24:24],)
SPIWrite 0e6b,00,0,0 //coef_update_signal=0x0; Address(0xe68[24:24],)
SPIWrite 0e73,00,0,0 //coef_update_signal=0x0; Address(0xe70[24:24],)
SPIWrite 0c6b,01,0,0 //coef_update_signal=0x1; Address(0xc68[24:24],)
SPIWrite 0c73,01,0,0 //coef_update_signal=0x1; Address(0xc70[24:24],)
SPIWrite 0e6b,01,0,0 //coef_update_signal=0x1; Address(0xe68[24:24],)
SPIWrite 0e73,01,0,0 //coef_update_signal=0x1; Address(0xe70[24:24],)
SPIWrite 0c6b,00,0,0 //coef_update_signal=0x0; Address(0xc68[24:24],)
SPIWrite 0c73,00,0,0 //coef_update_signal=0x0; Address(0xc70[24:24],)
SPIWrite 0e6b,00,0,0 //coef_update_signal=0x0; Address(0xe68[24:24],)
SPIWrite 0e73,00,0,0 //coef_update_signal=0x0; Address(0xe70[24:24],)

```

\\END: Completed RX, TX and FB Digital Chain Config.

```

SPIWrite 0010,00,0,7 //PAGE: rx_top=0x0; Address(0x10[3:2],)
\\ STEP: sigChainConfig/step2

```

\\START: Configuring SR Block

```
SPIWrite 0010,01,0,7 //PAGE: tx_top=0x1; Address(0x10[1:0],)
SPIWrite 0247,0f,0,7 //alarmMask=0xfc0; Address(0x244[28:16],)
SPIWrite 0246,c0,0,7
SPIWrite 022a,01,0,7 //AmplMode=0x1;(Meaning: ); Address(0x228[16:16],)
SPIWrite 023b,00,0,7 //GainStepSize=0x26; Address(0x238[25:16],)
SPIWrite 023a,26,0,7
SPIWrite 0233,00,0,7 //AttnStepSize=0x26; Address(0x230[25:16],)
SPIWrite 0232,26,0,7
SPIWrite 0228,01,0,7 //AmplUpdateCycles=0x1; Address(0x228[6:0],)
SPIWrite 0210,02,0,7 //MultDataMode=0x1;(Meaning: ); Address(0x210[2:1],)
SPIWrite 022f,00,0,7 //fsmWaitCntr=0x13; Address(0x22c[31:16],)
SPIWrite 022e,13,0,7
SPIWrite 0210,03,0,7 //BlkEnable=0x1;(Meaning: ); Address(0x210[0:0],)
SPIWrite 0447,0f,0,7 //alarmMask=0xfc0; Address(0x444[28:16],)
SPIWrite 0446,c0,0,7
SPIWrite 042a,01,0,7 //AmplMode=0x1;(Meaning: ); Address(0x428[16:16],)
SPIWrite 043b,00,0,7 //GainStepSize=0x26; Address(0x438[25:16],)
SPIWrite 043a,26,0,7
SPIWrite 0433,00,0,7 //AttnStepSize=0x26; Address(0x430[25:16],)
SPIWrite 0432,26,0,7
SPIWrite 0428,01,0,7 //AmplUpdateCycles=0x1; Address(0x428[6:0],)
SPIWrite 0410,02,0,7 //MultDataMode=0x1;(Meaning: ); Address(0x410[2:1],)
SPIWrite 042f,00,0,7 //fsmWaitCntr=0x13; Address(0x42c[31:16],)
SPIWrite 042e,13,0,7
SPIWrite 0410,03,0,7 //BlkEnable=0x1;(Meaning: ); Address(0x410[0:0],)
```

\\END: Done Configuring SR Block

\\START: Configuring SR Block

```
SPIWrite 0010,02,0,7 //PAGE: tx_top=0x2; Address(0x10[1:0],)
SPIWrite 0247,0f,0,7 //alarmMask=0xfc0; Address(0x244[28:16],)
SPIWrite 0246,c0,0,7
SPIWrite 022a,01,0,7 //AmplMode=0x1;(Meaning: ); Address(0x228[16:16],)
SPIWrite 023b,00,0,7 //GainStepSize=0x26; Address(0x238[25:16],)
SPIWrite 023a,26,0,7
SPIWrite 0233,00,0,7 //AttnStepSize=0x26; Address(0x230[25:16],)
SPIWrite 0232,26,0,7
SPIWrite 0228,01,0,7 //AmplUpdateCycles=0x1; Address(0x228[6:0],)
SPIWrite 0210,02,0,7 //MultDataMode=0x1;(Meaning: ); Address(0x210[2:1],)
SPIWrite 022f,00,0,7 //fsmWaitCntr=0x13; Address(0x22c[31:16],)
SPIWrite 022e,13,0,7
SPIWrite 0210,03,0,7 //BlkEnable=0x1;(Meaning: ); Address(0x210[0:0],)
SPIWrite 0447,0f,0,7 //alarmMask=0xfc0; Address(0x444[28:16],)
SPIWrite 0446,c0,0,7
SPIWrite 042a,01,0,7 //AmplMode=0x1;(Meaning: ); Address(0x428[16:16],)
SPIWrite 043b,00,0,7 //GainStepSize=0x26; Address(0x438[25:16],)
SPIWrite 043a,26,0,7
SPIWrite 0433,00,0,7 //AttnStepSize=0x26; Address(0x430[25:16],)
SPIWrite 0432,26,0,7
SPIWrite 0428,01,0,7 //AmplUpdateCycles=0x1; Address(0x428[6:0],)
SPIWrite 0410,02,0,7 //MultDataMode=0x1;(Meaning: ); Address(0x410[2:1],)
SPIWrite 042f,00,0,7 //fsmWaitCntr=0x13; Address(0x42c[31:16],)
SPIWrite 042e,13,0,7
SPIWrite 0410,03,0,7 //BlkEnable=0x1;(Meaning: ); Address(0x410[0:0],)
```

\\END: Done Configuring SR Block

```
SPIWrite 0010,00,0,7 //PAGE: tx_top=0x0; Address(0x10[1:0],)
\\ STEP: OvTDDpin/step0
```

\\START: Doing Internal TDD settings

```
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1;(Meaning: ); Address(0x14[2:2],)
SPIWrite 04e7,00,0,7 //enable_multi_sysref=0x0; Address(0x4e4[24:24],)
SPIWrite 04c4,01,0,7 //mode_2t2r=0x1; Address(0x4c4[0:0],)
SPIWrite 04c5,00,0,7 //fdd_mode=0x0; Address(0x4c4[8:8],)
SPIWrite 0206,06,0,7 //force_tg_txAB=0x0; Address(0x204[16:16],)
```

```

SPIWrite 0206,04,0,7 //force_tg_rxAB=0x0; Address(0x204[17:17],)
SPIWrite 0206,00,0,7 //force_tg_fbAB=0x0; Address(0x204[18:18],)
SPIWrite 036a,06,0,7 //force_tg_txCD=0x0; Address(0x368[16:16],)
SPIWrite 036a,04,0,7 //force_tg_rxCD=0x0; Address(0x368[17:17],)
SPIWrite 036a,00,0,7 //force_tg_fbCD=0x0; Address(0x368[18:18],)
SPIWrite 04dc,80,0,7 //force_pll_txAB=0x0; Address(0x4dc[6:6],)
SPIWrite 04dd,0d,0,7 //force_pll_txCD=0x0; Address(0x4dc[9:9],)
SPIWrite 04dc,00,0,7 //force_pll_rxAB=0x0; Address(0x4dc[7:7],)
SPIWrite 04dd,09,0,7 //force_pll_rxCD=0x0; Address(0x4dc[10:10],)
SPIWrite 04dd,08,0,7 //force_pll_fbAB=0x0; Address(0x4dc[8:8],)
SPIWrite 04dd,00,0,7 //force_pll_fbCD=0x0; Address(0x4dc[11:11],)
SPIWrite 0110,00,0,7 //fb_vswr_pin_valid_cha=0x0; Address(0x110[0:0],)
SPIWrite 0111,00,0,7 //fb_vswr_pin_valid_chc=0x0; Address(0x110[8:8],)
SPIWrite 0118,00,0,7 //use_reg_for_tx_gswap_rxtxlo=0x0; Address(0x118[0:0],)
SPIWrite 011c,01,0,7 //use_reg_for_txdsalatch=0x1; Address(0x11c[0:0],)
SPIWrite 0121,00,0,7 //en_reg_for_gswap=0x0; Address(0x120[8:8],)
SPIWrite 0106,0f,0,7 //chsel_gswap=0xf; Address(0x104[19:16],)
SPIWrite 0114,00,0,7 //fb_ncosel_pin_valid_cha=0x0; Address(0x114[0:0],)
SPIWrite 0115,00,0,7 //fb_ncosel_pin_valid_chc=0x0; Address(0x114[8:8],)
SPIWrite 071f,90,0,7 //override_val_fbmuxsel=0x24; Address(0x71c[31:26],)
SPIWrite 071f,93,0,7 //override_fb_muxsel=0x3; Address(0x71c[25:24],)
SPIWrite 04c6,01,0,7 //dc_pll_sync_mode=0x1; Address(0x4c4[16:16],)
SPIWrite 0526,00,0,7 //en_ovr_for_cond_freeze=0x0; Address(0x524[16:16],)
SPIWrite 0621,01,0,7 //config_rise_rxjesdon_chab=0x177; Address(0x620[15:0],)
SPIWrite 0620,77,0,7
SPIWrite 0661,01,0,7 //config_rise_rxjesdon_chcd=0x177; Address(0x660[15:0],)
SPIWrite 0660,77,0,7
SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0014,01,0,7 //PAGE: clktop_2t=0x1; Address(0x14[1:0],)
SPIWrite 005c,10,0,7 //CTL_SEL_CM_BIAS_R_SHRT_RXCML_1P8=0x1; Address(0x5c[4:4],)
SPIWrite 005c,18,0,7 //CTL_FORCE_LDO_FILT_RXCML_1P8=0x1; Address(0x5c[3:3],)
SPIWrite 005c,1c,0,7 //CTL_FORCE_CASCADEFILT_RXCML_1P8=0x1; Address(0x5c[2:2],)
SPIWrite 005c,1e,0,7 //CTL_FORCE_BIASFILT_RXCML_1P8=0x1; Address(0x5c[1:1],)

```

\\END: Completed Internal TDD settings

```

SPIWrite 0014,00,0,7 //PAGE: clktop_2t=0x0; Address(0x14[1:0],)

```

\\ STEP: OvTDDpin/step1

\\START: Overriding TDD Pins internally

```

SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0124,01,0,7 //use_reg_txon_AB=0x1; Address(0x124[0:0],)
SPIWrite 0126,01,0,7 //use_reg_rxon_AB=0x1; Address(0x124[16:16],)
SPIWrite 0128,01,0,7 //use_reg_fbon_AB=0x1; Address(0x128[0:0],)
SPIWrite 012a,01,0,7 //use_reg_txon_CD=0x1; Address(0x128[16:16],)
SPIWrite 012c,01,0,7 //use_reg_rxon_CD=0x1; Address(0x12c[0:0],)
SPIWrite 012e,01,0,7 //use_reg_fbon_CD=0x1; Address(0x12c[16:16],)
SPIWrite 0125,00,0,7 //reg_for_txon_AB=0x0; Address(0x124[8:8],)
SPIWrite 0127,00,0,7 //reg_for_rxon_AB=0x0; Address(0x124[24:24],)
SPIWrite 0129,00,0,7 //reg_for_fbon_AB=0x0; Address(0x128[8:8],)
SPIWrite 012b,00,0,7 //reg_for_txon_CD=0x0; Address(0x128[24:24],)
SPIWrite 012d,00,0,7 //reg_for_rxon_CD=0x0; Address(0x12c[8:8],)
SPIWrite 012f,00,0,7 //reg_for_fbon_CD=0x0; Address(0x12c[24:24],)

```

\\END: Done overriding TDD Pins internally

```

SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)

```

\\ STEP: OvTDDpin/step2

\\START: Overriding TDD Pins internally

```

SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0124,01,0,7 //use_reg_txon_AB=0x1; Address(0x124[0:0],)
SPIWrite 0126,01,0,7 //use_reg_rxon_AB=0x1; Address(0x124[16:16],)
SPIWrite 0128,01,0,7 //use_reg_fbon_AB=0x1; Address(0x128[0:0],)
SPIWrite 012a,01,0,7 //use_reg_txon_CD=0x1; Address(0x128[16:16],)
SPIWrite 012c,01,0,7 //use_reg_rxon_CD=0x1; Address(0x12c[0:0],)
SPIWrite 012e,01,0,7 //use_reg_fbon_CD=0x1; Address(0x12c[16:16],)

```

```
SPIWrite 0125,01,0,7    //reg_for_txon_AB=0x1;  Address(0x124[8:8],)
SPIWrite 0127,00,0,7    //reg_for_rxon_AB=0x0;  Address(0x124[24:24],)
SPIWrite 0129,00,0,7    //reg_for_fbon_AB=0x0;  Address(0x128[8:8],)
SPIWrite 012b,01,0,7    //reg_for_txon_CD=0x1;  Address(0x128[24:24],)
SPIWrite 012d,00,0,7    //reg_for_rxon_CD=0x0;  Address(0x12c[8:8],)
SPIWrite 012f,00,0,7    //reg_for_fbon_CD=0x0;  Address(0x12c[24:24],)
```

\\END: Done overriding TDD Pins internally

```
SPIWrite 0014,00,0,7    //PAGE: TIMING_CON=0x0; (Meaning: );      Address(0x14[2:2],)
\\ STEP: analogWrite/step0
```

\\START: Doing DAC Analog Writes

```
SPIWrite 0017,0f,0,3    //PAGE: dac_1t=0xf;      Address(0x17[3:0],)
SPIWrite 00bc,55,0,2
SPIWrite 00bc,55,3,6
SPIWrite 00bd,aa,0,3
SPIWrite 00bd,aa,4,7
SPIWrite 00be,14,0,4
SPIWrite 00bf,19,0,4
SPIWrite 00c0,19,0,4
SPIWrite 0052,b4,2,4
SPIWrite 0052,b4,5,7
SPIWrite 0054,0a,0,3
SPIWrite 0055,14,0,4
SPIWrite 0056,19,0,4
SPIWrite 0057,19,0,4
SPIWrite 0040,18,3,3
SPIWrite 0040,18,4,4
SPIWrite 003f,00,0,7
SPIWrite 003e,00,0,7
SPIWrite 003d,00,0,7
SPIWrite 003c,03,0,7
SPIWrite 007b,03,0,7
SPIWrite 007a,ff,0,7
SPIWrite 007e,03,0,7
SPIWrite 007d,ff,0,7
SPIWrite 0081,03,0,7
SPIWrite 0080,ff,0,7
SPIWrite 0085,03,0,7
SPIWrite 0084,ff,0,7
SPIWrite 0089,00,0,7
SPIWrite 0088,03,0,7
SPIWrite 008d,00,0,7
SPIWrite 008c,03,0,7
SPIWrite 0091,01,0,7
SPIWrite 0090,ff,0,7
SPIWrite 0095,00,0,7
SPIWrite 0094,1f,0,7
SPIWrite 00c1,1e,0,7
SPIWrite 00c2,32,0,7
SPIWrite 006e,aa,0,3
SPIWrite 006e,aa,4,7
SPIWrite 0041,28,2,5
SPIWrite 0042,aa,0,3
SPIWrite 0042,aa,4,7
SPIWrite 0043,aa,0,3
SPIWrite 0043,aa,4,7
SPIWrite 0044,0a,0,3
SPIWrite 006f,aa,0,3
SPIWrite 0045,03,0,7
SPIWrite 0046,03,0,7
SPIWrite 0062,88,0,3
SPIWrite 0062,88,4,7
SPIWrite 0063,88,0,3
SPIWrite 0063,88,4,7
SPIWrite 0065,40,0,7
SPIWrite 0066,40,0,7
```

```

SPIWrite 0067,40,0,7
SPIWrite 0068,40,0,7
SPIWrite 0069,40,0,7
SPIWrite 006a,40,0,7
SPIWrite 006b,40,0,7
SPIWrite 006c,40,0,7
SPIWrite 0064,44,0,3
SPIWrite 0064,44,4,7
SPIWrite 00c8,55,0,3
SPIWrite 00c8,55,4,7
SPIWrite 00c9,82,0,3
SPIWrite 00c9,22,4,7
SPIWrite 00d5,e4,0,7
SPIWrite 00d8,e4,0,7
SPIWrite 00d9,a0,0,7
SPIWrite 00da,a0,0,7
SPIWrite 00af,92,0,7
SPIWrite 00ad,0f,0,3
SPIWrite 00b0,1f,0,7
SPIWrite 00a7,ff,0,7
SPIWrite 00a6,ff,0,7
SPIWrite 00a5,ff,0,7
SPIWrite 00a4,ff,0,7
SPIWrite 00a8,ff,0,7
SPIWrite 00ae,06,0,3
SPIWrite 00cb,ff,0,7
SPIWrite 00cc,ff,0,7
SPIWrite 00d5,b8,0,7
SPIWrite 00d8,b8,0,7
SPIWrite 006f,aa,0,3
SPIWrite 006f,aa,4,7
SPIWrite 006d,06,1,3
SPIWrite 006d,46,4,6
SPIWrite 0047,03,0,7
SPIWrite 009c,02,1,1
SPIWrite 009f,78,0,7
SPIWrite 009e,00,0,7
SPIWrite 0017,00,0,3    //PAGE: dac_1t=0x0;      Address(0x17[3:0],)
SPIWrite 0010,03,0,7    //PAGE: tx_top=0x3;      Address(0x10[1:0],)
SPIWrite 0830,00,0,7    //IndepCorrByp=0x0; (Meaning: );      Address(0x830[0:0],)
SPIWrite 0830,02,0,7    //IndepCorrEn=0x1; (Meaning: );      Address(0x830[1:1],)
SPIWrite 0890,00,0,7    //IndepCorrByp=0x0; (Meaning: );      Address(0x890[0:0],)
SPIWrite 0890,02,0,7    //IndepCorrEn=0x1; (Meaning: );      Address(0x890[1:1],)
SPIWrite 0650,02,0,7    //txIQMCLFSREn=0x1; (Meaning: );      Address(0x650[1:1],)
SPIWrite 06e4,02,0,7    //txIQMCLFSREn=0x1; (Meaning: );      Address(0x6e4[1:1],)
SPIWrite 0831,28,0,7    //dsaMinIndex=0x28;      Address(0x830[13:8],)
SPIWrite 0891,28,0,7    //dsaMinIndex=0x28;      Address(0x890[13:8],)
SPIWrite 0832,01,0,7    //IndepCorrUpdt=0x1;      Address(0x830[16:16],)
SPIWrite 0892,01,0,7    //IndepCorrUpdt=0x1;      Address(0x890[16:16],)
SPIWrite 0833,00,0,7    //CoeffScaling=0x0; (Meaning: );      Address(0x830[24:24],)
SPIWrite 0893,00,0,7    //CoeffScaling=0x0; (Meaning: );      Address(0x890[24:24],)
SPIWrite 0832,00,0,7    //IndepCorrUpdt=0x0;      Address(0x830[16:16],)
SPIWrite 0892,00,0,7    //IndepCorrUpdt=0x0;      Address(0x890[16:16],)
SPIWrite 0010,00,0,7    //PAGE: tx_top=0x0;      Address(0x10[1:0],)

```

\\END: Completed DAC Analog Writes

\\ STEP: analogWrite/step1

\\START: Overriding TDD Pins internally

```

SPIWrite 0014,04,0,7    //PAGE: TIMING_CON=0x1; (Meaning: );      Address(0x14[2:2],)
SPIWrite 0124,01,0,7    //use_reg_txon_AB=0x1;      Address(0x124[0:0],)
SPIWrite 0126,01,0,7    //use_reg_rxon_AB=0x1;      Address(0x124[16:16],)
SPIWrite 0128,01,0,7    //use_reg_fbon_AB=0x1;      Address(0x128[0:0],)
SPIWrite 012a,01,0,7    //use_reg_txon_CD=0x1;      Address(0x128[16:16],)
SPIWrite 012c,01,0,7    //use_reg_rxon_CD=0x1;      Address(0x12c[0:0],)
SPIWrite 012e,01,0,7    //use_reg_fbon_CD=0x1;      Address(0x12c[16:16],)
SPIWrite 0125,00,0,7    //reg_for_txon_AB=0x0;      Address(0x124[8:8],)

```

```
SPIWrite 0127,01,0,7    //reg_for_rxon_AB=0x1; Address(0x124[24:24],)
SPIWrite 0129,00,0,7    //reg_for_fbon_AB=0x0; Address(0x128[8:8],)
SPIWrite 012b,00,0,7    //reg_for_txon_CD=0x0; Address(0x128[24:24],)
SPIWrite 012d,01,0,7    //reg_for_rxon_CD=0x1; Address(0x12c[8:8],)
SPIWrite 012f,00,0,7    //reg_for_fbon_CD=0x0; Address(0x12c[24:24],)
```

\\END: Done overriding TDD Pins internally

\\START: Doing RX ADC Analog Writes

```
SPIWrite 0014,00,2,2    //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0011,01,0,3    //PAGE: rx_ec_i=0x1; Address(0x11[3:0],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0; Address(0x11[3:0],)
SPIWrite 0011,10,4,7    //PAGE: rx_ec_q=0x1; Address(0x11[7:4],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,4,7    //PAGE: rx_ec_q=0x0; Address(0x11[7:4],)
SPIWrite 0011,02,0,3    //PAGE: rx_ec_i=0x2; Address(0x11[3:0],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0; Address(0x11[3:0],)
SPIWrite 0011,20,4,7    //PAGE: rx_ec_q=0x2; Address(0x11[7:4],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,4,7    //PAGE: rx_ec_q=0x0; Address(0x11[7:4],)
SPIWrite 0011,04,0,3    //PAGE: rx_ec_i=0x4; Address(0x11[3:0],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0; Address(0x11[3:0],)
SPIWrite 0011,40,4,7    //PAGE: rx_ec_q=0x4; Address(0x11[7:4],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,4,7    //PAGE: rx_ec_q=0x0; Address(0x11[7:4],)
SPIWrite 0011,08,0,3    //PAGE: rx_ec_i=0x8; Address(0x11[3:0],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0; Address(0x11[3:0],)
SPIWrite 0011,80,4,7    //PAGE: rx_ec_q=0x8; Address(0x11[7:4],)
SPIWrite 04a0,0c,0,0
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0; Address(0x11[7:4],)
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf; Address(0x11[3:0],)
SPIWrite 0011,ff,0,7
SPIWrite 029d,03,0,7
SPIWrite 029d,1b,0,7
SPIWrite 029e,00,0,7
SPIWrite 029d,db,0,7
SPIWrite 0299,03,0,7
SPIWrite 029e,30,0,7
SPIWrite 029f,0c,0,7
SPIWrite 029f,6c,0,7
SPIWrite 02a0,03,0,7
SPIWrite 029e,30,0,7
SPIWrite 029f,6d,0,7
SPIWrite 02a0,13,0,7
SPIWrite 0284,00,0,7
SPIWrite 0281,00,0,7
SPIWrite 0284,40,0,7
SPIWrite 0338,2b,0,7
SPIWrite 0111,09,0,7
SPIWrite 0011,00,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0; Address(0x11[3:0],)
```

\\START: Doing RX IMD vs DSA improvement Analog Writes

```
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf; Address(0x11[3:0],)
SPIWrite 0011,0f,0,7
SPIWrite 0b33,00,0,7
SPIWrite 0b32,00,0,7
SPIWrite 0b33,08,0,7
SPIWrite 0c91,20,0,7
SPIWrite 0011,00,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0; Address(0x11[3:0],)
```


\\END: Completed RX IMD vs DSA improvement Analog Writes

```
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf;    Address(0x11[3:0],)
SPIWrite 0500,03,0,7
SPIWrite 0500,03,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,f0,0,7    //PAGE: rx_ec_q=0xf;    Address(0x11[7:4],)
SPIWrite 0500,03,0,7
SPIWrite 0500,03,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf;    Address(0x11[3:0],)
SPIWrite 0500,03,0,7
SPIWrite 0500,03,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,f0,0,7    //PAGE: rx_ec_q=0xf;    Address(0x11[7:4],)
SPIWrite 0500,03,0,7
SPIWrite 0500,03,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf;    Address(0x11[3:0],)
SPIWrite 0c70,08,0,7
SPIWrite 0c70,18,0,7
SPIWrite 0103,80,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,10,0,7    //PAGE: rx_ec_q=0x1;    Address(0x11[7:4],)
SPIWrite 0c74,02,0,7
SPIWrite 0103,80,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,01,0,7    //PAGE: rx_ec_i=0x1;    Address(0x11[3:0],)
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,10,0,7    //PAGE: rx_ec_q=0x1;    Address(0x11[7:4],)
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,02,0,7    //PAGE: rx_ec_i=0x2;    Address(0x11[3:0],)
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,20,0,7    //PAGE: rx_ec_q=0x2;    Address(0x11[7:4],)
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,04,0,7    //PAGE: rx_ec_i=0x4;    Address(0x11[3:0],)
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,40,0,7    //PAGE: rx_ec_q=0x4;    Address(0x11[7:4],)
```

```
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,08,0,7    //PAGE: rx_ec_i=0x8;    Address(0x11[3:0],)
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,80,0,7    //PAGE: rx_ec_q=0x8;    Address(0x11[7:4],)
SPIWrite 049f,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,04,0,7
SPIWrite 049e,01,0,7
SPIWrite 049d,00,0,7
SPIWrite 049c,01,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a2,01,0,7
SPIWrite 04a2,00,0,7
SPIWrite 049c,01,0,7
```

WAIT 0.01

```
SPIWrite 049c,00,0,7
```

WAIT 0.05

```
SPIWrite 049d,02,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf;    Address(0x11[3:0],)
SPIWrite 0500,02,0,7
SPIWrite 0500,00,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
```

```

SPIWrite 0011,f0,0,7    //PAGE: rx_ec_q=0xf;    Address(0x11[7:4],)
SPIWrite 0500,02,0,7
SPIWrite 0500,00,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf;    Address(0x11[3:0],)
SPIWrite 0500,00,0,7
SPIWrite 0500,00,0,7
SPIWrite 050a,01,0,7
SPIWrite 0502,01,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,f0,0,7    //PAGE: rx_ec_q=0xf;    Address(0x11[7:4],)
SPIWrite 0500,00,0,7
SPIWrite 0500,00,0,7
SPIWrite 050a,01,0,7
SPIWrite 0502,01,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,0f,0,7    //PAGE: rx_ec_i=0xf;    Address(0x11[3:0],)
SPIWrite 0c70,10,0,7
SPIWrite 0c70,00,0,7
SPIWrite 0103,00,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,10,0,7    //PAGE: rx_ec_q=0x1;    Address(0x11[7:4],)
SPIWrite 0c74,00,0,7
SPIWrite 0103,00,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,01,0,7    //PAGE: rx_ec_i=0x1;    Address(0x11[3:0],)
SPIWrite 0103,70,0,7
SPIWrite 01a1,04,0,7
SPIWrite 0112,05,0,7
SPIWrite 0bf3,03,0,7
SPIWrite 0101,02,0,7
SPIWrite 0100,28,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,10,0,7    //PAGE: rx_ec_q=0x1;    Address(0x11[7:4],)
SPIWrite 0103,70,0,7
SPIWrite 01a1,04,0,7
SPIWrite 0112,05,0,7
SPIWrite 0101,02,0,7
SPIWrite 0100,28,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,02,0,7    //PAGE: rx_ec_i=0x2;    Address(0x11[3:0],)
SPIWrite 0103,70,0,7
SPIWrite 01a1,04,0,7
SPIWrite 0112,05,0,7
SPIWrite 0bf3,03,0,7
SPIWrite 0101,02,0,7
SPIWrite 0100,28,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0010,04,0,7    //PAGE: rx_top=0x1;    Address(0x10[3:2],)
SPIWrite 0c10,05,0,7    //nl_corr_complete_byp=0x1; (Meaning: );    Address(0xc10[0:0],)
SPIWrite 0c11,01,0,7    //coef_update_pulse_nl=0x1;    Address(0xc10[8:8],)

```

WAIT 0.1

```

SPIWrite 0c11,00,0,7    //coef_update_pulse_nl=0x0;    Address(0xc10[8:8],)
SPIWrite 0e10,05,0,7    //nl_corr_complete_byp=0x1; (Meaning: );    Address(0xe10[0:0],)
SPIWrite 0e11,01,0,7    //coef_update_pulse_nl=0x1;    Address(0xe10[8:8],)

```

WAIT 0.1

```

SPIWrite 0e11,00,0,7    //coef_update_pulse_nl=0x0;    Address(0xe10[8:8],)
SPIWrite 0010,00,0,7    //PAGE: rx_top=0x0;    Address(0x10[3:2],)

```

```

SPIWrite 0011,04,0,7    //PAGE: rx_ec_i=0x4;    Address(0x11[3:0],)
SPIWrite 0103,70,0,7
SPIWrite 01a1,04,0,7
SPIWrite 0112,05,0,7
SPIWrite 0bf3,03,0,7
SPIWrite 0101,02,0,7
SPIWrite 0100,28,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,40,0,7    //PAGE: rx_ec_q=0x4;    Address(0x11[7:4],)
SPIWrite 0103,70,0,7
SPIWrite 01a1,04,0,7
SPIWrite 0112,05,0,7
SPIWrite 0101,02,0,7
SPIWrite 0100,28,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0011,08,0,7    //PAGE: rx_ec_i=0x8;    Address(0x11[3:0],)
SPIWrite 0103,70,0,7
SPIWrite 01a1,04,0,7
SPIWrite 0112,05,0,7
SPIWrite 0bf3,03,0,7
SPIWrite 0101,02,0,7
SPIWrite 0100,28,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_i=0x0;    Address(0x11[3:0],)
SPIWrite 0011,80,0,7    //PAGE: rx_ec_q=0x8;    Address(0x11[7:4],)
SPIWrite 0103,70,0,7
SPIWrite 01a1,04,0,7
SPIWrite 0112,05,0,7
SPIWrite 0101,02,0,7
SPIWrite 0100,28,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)
SPIWrite 0010,08,0,7    //PAGE: rx_top=0x2;    Address(0x10[3:2],)
SPIWrite 0c10,05,0,7    //nl_corr_complete_byp=0x1; (Meaning: );    Address(0xc10[0:0],)
SPIWrite 0c11,01,0,7    //coef_update_pulse_nl=0x1;    Address(0xc10[8:8],)

```

WAIT 0.1

```

SPIWrite 0c11,00,0,7    //coef_update_pulse_nl=0x0;    Address(0xc10[8:8],)
SPIWrite 0e10,05,0,7    //nl_corr_complete_byp=0x1; (Meaning: );    Address(0xe10[0:0],)
SPIWrite 0e11,01,0,7    //coef_update_pulse_nl=0x1;    Address(0xe10[8:8],)

```

WAIT 0.1

```

SPIWrite 0e11,00,0,7    //coef_update_pulse_nl=0x0;    Address(0xe10[8:8],)
SPIWrite 0010,00,0,7    //PAGE: rx_top=0x0;    Address(0x10[3:2],)
SPIWrite 0012,0f,0,7    //PAGE: rx_ec_common=0xf;    Address(0x12[3:0],)
SPIWrite 04c4,20,0,7
SPIWrite 0012,00,0,7    //PAGE: rx_ec_common=0x0;    Address(0x12[3:0],)
SPIWrite 0014,01,0,7    //PAGE: clktop_2t=0x1;    Address(0x14[1:0],)
SPIWrite 006b,10,0,7    //MASK_PDN_SYNC_RXCML_R=0x1;    Address(0x68[28:28],)
SPIWrite 006b,18,0,7    //MASK_PDN_SYNC_RXCML_L=0x1;    Address(0x68[27:27],)
SPIWrite 0014,00,0,7    //PAGE: clktop_2t=0x0;    Address(0x14[1:0],)
SPIWrite 0011,f0,0,7    //PAGE: rx_ec_q=0xf;    Address(0x11[7:4],)
SPIWrite 0293,00,0,7
SPIWrite 0b93,ec,0,7
SPIWrite 0b85,8d,0,7
SPIWrite 0b93,ec,0,7
SPIWrite 0b92,65,0,7
SPIWrite 0b91,cd,0,7
SPIWrite 0b95,1c,0,7
SPIWrite 0b94,e0,0,7
SPIWrite 0011,00,0,7    //PAGE: rx_ec_q=0x0;    Address(0x11[7:4],)

```

\\END: Completed RX ADC Analog Writes

```

SPIWrite 0015,04,0,7    //PAGE: SERDES=0x1;    Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,04,0,7
SPIWrite 47ed,00,0,7    //MAIN_CURSOR=0x7;    Address(0x80f6[7:5],)
SPIWrite 47ec,e0,0,7
SPIWrite 45ed,00,0,7    //MAIN_CURSOR=0x7;    Address(0x80f6[7:5],)

```

```
SPIWrite 45ec,e0,0,7
SPIWrite 41ed,00,0,7 //MAIN_CURSOR=0x7; Address(0x80f6[7:5],)
SPIWrite 41ec,e0,0,7
SPIWrite 43ed,00,0,7 //MAIN_CURSOR=0x7; Address(0x80f6[7:5],)
SPIWrite 43ec,e0,0,7
SPIWrite 0015,00,0,7 //PAGE: SERDES=0x2; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,40,0,7
SPIWrite 43ed,08,0,7 //MAIN_CURSOR=0x7; Address(0x80f6[7:5],)
SPIWrite 43ec,00,0,7
SPIWrite 41ed,08,0,7 //MAIN_CURSOR=0x7; Address(0x80f6[7:5],)
SPIWrite 41ec,00,0,7
SPIWrite 45ed,00,0,7 //MAIN_CURSOR=0x7; Address(0x80f6[7:5],)
SPIWrite 45ec,e0,0,7
SPIWrite 47ed,00,0,7 //MAIN_CURSOR=0x7; Address(0x80f6[7:5],)
SPIWrite 47ec,e0,0,7
SPIWrite 0015,40,0,7 //PAGE: SERDES=0x0; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,00,0,7
\\ STEP: analogWrite/step2
```

\\START: Overriding TDD Pins internally

```
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0124,01,0,7 //use_reg_txon_AB=0x1; Address(0x124[0:0],)
SPIWrite 0126,01,0,7 //use_reg_rxon_AB=0x1; Address(0x124[16:16],)
SPIWrite 0128,01,0,7 //use_reg_fbon_AB=0x1; Address(0x128[0:0],)
SPIWrite 012a,01,0,7 //use_reg_txon_CD=0x1; Address(0x128[16:16],)
SPIWrite 012c,01,0,7 //use_reg_rxon_CD=0x1; Address(0x12c[0:0],)
SPIWrite 012e,01,0,7 //use_reg_fbon_CD=0x1; Address(0x12c[16:16],)
SPIWrite 0125,00,0,7 //reg_for_txon_AB=0x0; Address(0x124[8:8],)
SPIWrite 0127,00,0,7 //reg_for_rxon_AB=0x0; Address(0x124[24:24],)
SPIWrite 0129,01,0,7 //reg_for_fbon_AB=0x1; Address(0x128[8:8],)
SPIWrite 012b,00,0,7 //reg_for_txon_CD=0x0; Address(0x128[24:24],)
SPIWrite 012d,00,0,7 //reg_for_rxon_CD=0x0; Address(0x12c[8:8],)
SPIWrite 012f,01,0,7 //reg_for_fbon_CD=0x1; Address(0x12c[24:24],)
```

\\END: Done overriding TDD Pins internally

\\START: Doing FB ADC Analog Writes

```
SPIWrite 0014,00,2,2 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0010,10,4,4 //PAGE: fb_ec=0x1; Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,10,6,6
SPIWrite 00b0,03,1,1
SPIWrite 0188,09,3,3
SPIWrite 0140,28,3,3
SPIWrite 0138,28,3,3
SPIWrite 0190,09,3,3
SPIWrite 0168,39,3,3
SPIWrite 0160,39,3,3
SPIWrite 0119,32,0,3
SPIWrite 0121,32,0,3
SPIWrite 0129,32,0,3
SPIWrite 0131,32,0,3
SPIWrite 0119,32,0,3
SPIWrite 0121,32,0,3
SPIWrite 0129,32,0,3
SPIWrite 0131,32,0,3
SPIWrite 0010,00,4,4 //PAGE: fb_ec=0x0; Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,00,6,6
SPIWrite 0010,20,5,5 //PAGE: fb_ana=0x1; Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,20,7,7
SPIWrite 0085,08,2,4
SPIWrite 0102,08,3,3
SPIWrite 0010,00,5,5 //PAGE: fb_ana=0x0; Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,00,7,7
SPIWrite 0010,00,4,4 //PAGE: fb_ec=0x2; Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,40,6,6
SPIWrite 00b0,03,1,1
```

```

SPIWrite 0188,09,3,3
SPIWrite 0140,28,3,3
SPIWrite 0138,28,3,3
SPIWrite 0190,09,3,3
SPIWrite 0168,39,3,3
SPIWrite 0160,39,3,3
SPIWrite 0119,32,0,3
SPIWrite 0121,32,0,3
SPIWrite 0129,32,0,3
SPIWrite 0131,32,0,3
SPIWrite 0119,32,0,3
SPIWrite 0121,32,0,3
SPIWrite 0129,32,0,3
SPIWrite 0131,32,0,3
SPIWrite 0010,40,4,4 //PAGE: fb_ec=0x0; Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,00,6,6
SPIWrite 0010,00,5,5 //PAGE: fb_ana=0x2; Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,80,7,7
SPIWrite 0085,08,2,4
SPIWrite 0102,08,3,3
SPIWrite 0010,80,5,5 //PAGE: fb_ana=0x0; Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,00,7,7
SPIWrite 0017,10,4,5 //PAGE: dac_2t=0x1; Address(0x17[5:4],)
SPIWrite 002e,04,2,2
SPIWrite 0017,20,4,5 //PAGE: dac_2t=0x2; Address(0x17[5:4],)
SPIWrite 002e,04,2,2
SPIWrite 0017,00,4,5 //PAGE: dac_2t=0x0; Address(0x17[5:4],)
SPIWrite 0017,40,6,7 //PAGE: ana_2r=0x1; Address(0x17[7:6],)
SPIWrite 003f,20,3,5
SPIWrite 003f,26,1,2
SPIWrite 0017,80,6,7 //PAGE: ana_2r=0x2; Address(0x17[7:6],)
SPIWrite 003f,20,3,5
SPIWrite 003f,26,1,2
SPIWrite 0017,00,6,7 //PAGE: ana_2r=0x0; Address(0x17[7:6],)
SPIWrite 0010,20,5,5 //PAGE: fb_ana=0x1; Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,20,7,7
SPIWrite 0080,10,4,4
SPIWrite 0080,18,3,3
SPIWrite 009a,01,0,0
SPIWrite 0095,01,0,0
SPIWrite 0094,c0,6,7
SPIWrite 0010,00,5,5 //PAGE: fb_ana=0x0; Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,00,7,7
SPIWrite 0010,10,4,4 //PAGE: fb_ec=0x1; Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,10,6,6
SPIWrite 0150,31,0,0
SPIWrite 0150,31,4,4
SPIWrite 0150,31,5,5
SPIWrite 0108,22,0,0
SPIWrite 0108,22,5,5
SPIWrite 0150,b1,7,7
SPIWrite 025e,40,0,7
SPIWrite 025d,00,7,7
SPIWrite 0028,a1,0,0
SPIWrite 0028,a0,0,0
SPIWrite 00a2,00,0,5
SPIWrite 00a1,00,0,7
SPIWrite 00a0,01,0,7
SPIWrite 00a2,00,0,5
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 0010,00,4,4 //PAGE: fb_ec=0x0; Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,00,6,6
SPIWrite 0010,00,5,5 //PAGE: fb_ana=0x2; Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,80,7,7
SPIWrite 0080,10,4,4
SPIWrite 0080,18,3,3
SPIWrite 009a,01,0,0
SPIWrite 0095,01,0,0

```

```

SPIWrite 0094,c0,6,7
SPIWrite 0010,80,5,5    //PAGE: fb_ana=0x0;      Address(0x10[5:5],0x10[7:7],)
SPIWrite 0010,00,7,7
SPIWrite 0010,00,4,4    //PAGE: fb_ec=0x2;   Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,40,6,6
SPIWrite 0150,31,0,0
SPIWrite 0150,31,4,4
SPIWrite 0150,31,5,5
SPIWrite 0108,22,0,0
SPIWrite 0108,22,5,5
SPIWrite 0150,b1,7,7
SPIWrite 025e,40,0,7
SPIWrite 025d,00,7,7
SPIWrite 0028,a1,0,0
SPIWrite 0028,a0,0,0
SPIWrite 00a2,00,0,5
SPIWrite 00a1,00,0,7
SPIWrite 00a0,01,0,7
SPIWrite 00a2,00,0,5
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 0010,40,4,4    //PAGE: fb_ec=0x0;   Address(0x10[4:4],0x10[6:6],)
SPIWrite 0010,00,6,6
SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1;(Meaning: );      Address(0x13[4:4],)
SPIWrite 00a3,00,0,7
SPIWrite 00a2,01,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,01,0,7
SPIWrite 0193,14,0,7

\\START: Enabling/Disabling Temp Sensor

SPIWrite 0013,00,0,7    //PAGE: customer_macro=0x0;(Meaning: );      Address(0x13[4:4],)
SPIWrite 0015,80,0,7    //PAGE: DIGTOP_MISC=0x1;(Meaning: );      Address(0x15[7:7],)
SPIWrite 03a3,08,0,7    //temp_sensel_sel_input=0x1;(Meaning: );      Address(0x3a0[27:27],)
SPIWrite 03a0,02,0,7    //temp_sensel_en=0x1;   Address(0x3a0[1:1],)
SPIWrite 03a0,02,0,7    //temp_sensel_soft_reset=0x0;   Address(0x3a0[0:0],)
SPIWrite 03a0,0e,0,7    //temp_sensel_div1=0x3;(Meaning: );      Address(0x3a0[3:2],)
SPIWrite 03a0,3e,0,7    //temp_sensel_div2=0x3;(Meaning: );      Address(0x3a0[5:4],)
SPIWrite 03a0,7e,0,7    //temp_sensel_analog_powerup=0x1;(Meaning: );      Address(0x3a0[6:6],)
SPIWrite 0722,02,0,7    //temp_sense_25m_clock_ungate=0x1;(Meaning: );      Address(0x720[17:17],)

\\END: Done enabling/Disabling Temp Sensor

\\END: Completed FB ADC Analog Writes

SPIWrite 0015,00,0,7    //PAGE: DIGTOP_MISC=0x0;(Meaning: );      Address(0x15[7:7],)
\\ STEP: analogWrite/step3
SPIWrite 0013,10,0,7    //PAGE: customer_macro=0x1;(Meaning: );      Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,18,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,07,0,7
SPIWrite 00a6,04,0,7
SPIWrite 00a5,02,0,7
SPIWrite 00a4,00,0,7
SPIWrite 00ab,00,0,7
SPIWrite 00aa,00,0,7
SPIWrite 00a9,00,0,7
SPIWrite 00a8,00,0,7
SPIWrite 0193,12,0,7

SPIPoll 00f0,0,7,7

```



```

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0; (Meaning: );      Address(0x13[4:4],)
SPIWrite 0011,10,4,7    //PAGE: rx_ec_q=0x1;      Address(0x11[7:4],)
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,31,6,7
SPIWrite 0335,3c,0,2
SPIWrite 0011,00,4,7    //PAGE: rx_ec_q=0x0;      Address(0x11[7:4],)
SPIWrite 0011,01,0,3    //PAGE: rx_ec_i=0x1;      Address(0x11[3:0],)
SPIWrite 0335,3c,0,2
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,32,6,7
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0;      Address(0x11[3:0],)
SPIWrite 0012,01,0,3    //PAGE: rx_ec_common=0x1;  Address(0x12[3:0],)
SPIWrite 028a,80,7,7
SPIWrite 0012,00,0,3    //PAGE: rx_ec_common=0x0;  Address(0x12[3:0],)
SPIWrite 0011,20,4,7    //PAGE: rx_ec_q=0x2;      Address(0x11[7:4],)
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,32,6,7
SPIWrite 0335,3c,0,2
SPIWrite 0011,00,4,7    //PAGE: rx_ec_q=0x0;      Address(0x11[7:4],)
SPIWrite 0011,02,0,3    //PAGE: rx_ec_i=0x2;      Address(0x11[3:0],)
SPIWrite 0335,3c,0,2
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,31,6,7
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0;      Address(0x11[3:0],)
SPIWrite 0012,02,0,3    //PAGE: rx_ec_common=0x2;  Address(0x12[3:0],)
SPIWrite 028a,80,7,7
SPIWrite 0012,00,0,3    //PAGE: rx_ec_common=0x0;  Address(0x12[3:0],)
SPIWrite 0011,40,4,7    //PAGE: rx_ec_q=0x4;      Address(0x11[7:4],)
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,32,6,7
SPIWrite 0335,3c,0,2
SPIWrite 0011,00,4,7    //PAGE: rx_ec_q=0x0;      Address(0x11[7:4],)
SPIWrite 0011,04,0,3    //PAGE: rx_ec_i=0x4;      Address(0x11[3:0],)
SPIWrite 0335,3c,0,2
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,32,6,7
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0;      Address(0x11[3:0],)
SPIWrite 0012,04,0,3    //PAGE: rx_ec_common=0x4;  Address(0x12[3:0],)
SPIWrite 028a,80,7,7
SPIWrite 0012,00,0,3    //PAGE: rx_ec_common=0x0;  Address(0x12[3:0],)
SPIWrite 0011,80,4,7    //PAGE: rx_ec_q=0x8;      Address(0x11[7:4],)
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,32,6,7
SPIWrite 0335,3c,0,2
SPIWrite 0011,00,4,7    //PAGE: rx_ec_q=0x0;      Address(0x11[7:4],)
SPIWrite 0011,08,0,3    //PAGE: rx_ec_i=0x8;      Address(0x11[3:0],)
SPIWrite 0335,3c,0,2
SPIWrite 0281,80,7,7
SPIWrite 0284,40,6,6
SPIWrite 0284,c0,7,7
SPIWrite 0334,32,6,7
SPIWrite 0011,00,0,3    //PAGE: rx_ec_i=0x0;      Address(0x11[3:0],)
SPIWrite 0012,08,0,3    //PAGE: rx_ec_common=0x8;  Address(0x12[3:0],)
SPIWrite 028a,80,7,7
SPIWrite 0012,00,0,7    //PAGE: rx_ec_common=0x0;  Address(0x12[3:0],)

```

\\ STEP: jesdConfig/step0

\\START: Configuring Device JESD TX

```
SPIWrite 0015,01,0,7 //PAGE: rx_jesd=0x1; Address(0x15[0:0],0x15[4:4],)
SPIWrite 0015,01,0,7
SPIWrite 00b0,04,0,7 //hd_sts_pll_lock_override=0x1; Address(0xb0[2:2],)
SPIWrite 00b0,0c,0,7 //hd_sts_pll_lock_reg=0x1; Address(0xb0[3:3],)
SPIWrite 0052,05,0,7 //ddc_bypass_8lanemode_en=0x0; Address(0x50[20:20],)
SPIWrite 0045,e0,0,7 //init_state=0x1;(Meaning: ); Address(0x44[15:15],)
SPIWrite 0045,e0,0,7 //fifo_init_state=0x1; Address(0x44[13:13],)
SPIWrite 0045,e0,0,7 //fb_init_state=0x1;(Meaning: ); Address(0x44[14:14],)
SPIWrite 0026,0f,0,7 //jesd_clear_data=0xf; Address(0x24[19:16],)
SPIWrite 0020,37,0,7 //sysref_jesd_mode=0x1; Address(0x20[7:5],)
SPIWrite 0020,3f,0,7 //k_m1=0x1f; Address(0x20[4:0],)
SPIWrite 0021,1f,0,7 //fb_k_m1=0x1f; Address(0x20[12:8],)
SPIWrite 0043,14,0,7 //jesd_mode=0x14; Address(0x40[30:24],)
SPIWrite 0044,01,0,7 //fb_jesd_mode=0x1; Address(0x44[6:0],)
SPIWrite 0076,02,0,7 //adc_jesd_root_clk_div=0x2; Address(0x74[23:16],)
SPIWrite 00c0,01,0,7 //ddc_rd_clk_ratio_rx1=0x1; Address(0xc0[4:0],)
SPIWrite 00c0,21,0,7 //ddc_rd_clk_ratio_rx1_override=0x1; Address(0xc0[5:5],)
SPIWrite 00c1,01,0,7 //ddc_rd_clk_ratio_rx2=0x1; Address(0xc0[12:8],)
SPIWrite 00c1,21,0,7 //ddc_rd_clk_ratio_rx2_override=0x1; Address(0xc0[13:13],)
SPIWrite 00c4,00,0,7 //jesd_tx_clk_ratio_rx1=0x0; Address(0xc4[4:0],)
SPIWrite 00c4,20,0,7 //jesd_tx_clk_ratio_rx1_override=0x1; Address(0xc4[5:5],)
SPIWrite 00c5,00,0,7 //jesd_tx_clk_ratio_rx2=0x0; Address(0xc4[12:8],)
SPIWrite 00c5,20,0,7 //jesd_tx_clk_ratio_rx2_override=0x1; Address(0xc4[13:13],)
SPIWrite 0060,04,0,7 //jesd_system_mode=0x1; Address(0x60[3:2],)
SPIWrite 0080,01,0,7 //scr_64b_initval=0x1;
Address(0x80[7:0],0x80[15:8],0x80[23:16],0x80[31:24],0x84[7:0],0x84[15:8],0x84[23:16],0x84[25:24],)
SPIWrite 0081,00,0,7
SPIWrite 0082,00,0,7
SPIWrite 0083,00,0,7
SPIWrite 0084,00,0,7
SPIWrite 0085,00,0,7
SPIWrite 0086,00,0,7
SPIWrite 0087,00,0,7
SPIWrite 0088,00,0,7 //scr_64b_en=0x0; Address(0x88[0:0],)
SPIWrite 004f,00,0,7 //jesd_std_sel=0x0;(Meaning: ); Address(0x4c[25:24],)
SPIWrite 005f,08,0,7 //dedicated_rx_fb_lane_mode=0x1; Address(0x5c[27:27],)
SPIWrite 00ca,01,0,7 //sample_drop_mode_rx1=0x1; Address(0xc8[19:16],)
SPIWrite 00ca,11,0,7 //sample_drop_mode_rx2=0x1; Address(0xc8[23:20],)
SPIWrite 0022,00,0,7 //scr=0x0;(Meaning: ); Address(0x20[16:16],)
SPIWrite 0023,00,0,7 //fb_scr=0x0;(Meaning: ); Address(0x20[24:24],)
SPIWrite 0025,05,0,7 //lane_ena=0x5; Address(0x24[11:8],)
SPIWrite 0077,04,0,7 //sysref_to_ddc_jesd_clk_div_override=0x1; Address(0x74[26:26],)
SPIWrite 0045,e0,0,7 //init_state=0x1;(Meaning: ); Address(0x44[15:15],)
SPIWrite 0045,e0,0,7 //fifo_init_state=0x1; Address(0x44[13:13],)
SPIWrite 0045,e0,0,7 //fb_init_state=0x1;(Meaning: ); Address(0x44[14:14],)
```

\\END: Done configuring Device JESD TX

```
SPIWrite 0015,00,0,7 //PAGE: rx_jesd=0x0; Address(0x15[0:0],0x15[4:4],)
SPIWrite 0015,00,0,7
\\ STEP: jesdConfig/step1
```

\\START: Configuring Device JESD RX

```
SPIWrite 0015,02,0,7 //PAGE: tx_jesd=0x1; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,02,0,7
SPIWrite 007f,1f,0,7 //k_m1=0x1f; Address(0x7c[31:24],)
SPIWrite 007e,0f,0,7 //rbd_m1=0xf; Address(0x7c[23:16],)
SPIWrite 0020,01,0,7 //init_state_tx0=0x1;(Meaning: ); Address(0x20[0:0],)
SPIWrite 0081,ff,0,7 //jesd_clear_data=0xff; Address(0x80[15:8],)
SPIWrite 0021,02,0,7 //dac_jesd_root_clk_div=0x2;(Meaning: ); Address(0x20[15:8],)
SPIWrite 013d,01,0,7 //duc_wr_clk_ratio_tx0=0x1;(Meaning: ); Address(0x13c[12:8],)
SPIWrite 013f,00,0,7 //jesd_rx_clk_ratio_tx0=0x0;(Meaning: ); Address(0x13c[28:24],)
SPIWrite 0024,01,0,7 //jesd_mode_tx0=0x1;(Meaning: ); Address(0x24[4:0],)
```

```

SPIWrite 013d,21,0,7 //duc_wr_clk_ratio_tx0_override=0x1; Address(0x13c[13:13],)
SPIWrite 013f,20,0,7 //jesd_rx_clk_ratio_tx0_override=0x1; Address(0x13c[29:29],)
SPIWrite 002d,9b,0,7 //spi_txenable=0x1; Address(0x2c[8:8],)
SPIWrite 0093,ff,0,7 //spidac=0x7fff; Address(0x90[31:24],0x94[7:0],)
SPIWrite 0094,7f,0,7
SPIWrite 0093,ff,0,7 //spidac=0x7fff; Address(0x90[31:24],0x94[7:0],)
SPIWrite 0094,7f,0,7
SPIWrite 0092,00,0,7 //spidac_ena=0x0; Address(0x90[23:23],)
SPIWrite 0092,00,0,7 //spidac_ena=0x0; Address(0x90[23:23],)
SPIWrite 008b,df,0,7 //sync_request_ena=0xdf; Address(0x88[31:24],)
SPIWrite 008c,00,0,7 //error_ena=0x0; Address(0x8c[7:0],)
SPIWrite 0123,7f,0,7 //comma_align_valid_thresh_tx0=0x7f; Address(0x120[30:24],)
SPIWrite 0148,00,0,7 //jesd_h_ena=0x0; Address(0x148[1:1],)
SPIWrite 0148,00,0,7 //jesd_c_ena=0x0; Address(0x148[0:0],)
SPIWrite 007d,12,0,7 //scr=0x0; Address(0x7c[15:15],)
SPIWrite 007c,01,0,7 //lane_ena=0x1; Address(0x7c[7:0],)
SPIWrite 0134,04,0,7 //sysref_to_dac_jesd_clk_div_override=0x1; Address(0x134[2:2],)
SPIWrite 0020,01,0,7 //init_state_tx0=0x1;(Meaning: ); Address(0x20[0:0],)

```

\\END: Done configuring Device JESD RX

```

SPIWrite 0015,00,0,7 //PAGE: tx_jesd=0x0; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,00,0,7
SPIWrite 0015,00,0,7 //PAGE: rx_jesd=0x2; Address(0x15[0:0],0x15[4:4],)
SPIWrite 0015,10,0,7
SPIWrite 0025,00,0,7 //lane_ena=0x0; Address(0x24[11:8],)
SPIWrite 0015,10,0,7 //PAGE: rx_jesd=0x0; Address(0x15[0:0],0x15[4:4],)
SPIWrite 0015,00,0,7
\\ STEP: jesdConfig/step2

```

\\START: Configuring Device JESD RX

```

SPIWrite 0015,00,0,7 //PAGE: tx_jesd=0x2; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,20,0,7
SPIWrite 007f,1f,0,7 //k_m1=0x1f; Address(0x7c[31:24],)
SPIWrite 007e,0f,0,7 //rbd_m1=0xf; Address(0x7c[23:16],)
SPIWrite 0020,01,0,7 //init_state_tx0=0x1;(Meaning: ); Address(0x20[0:0],)
SPIWrite 0081,ff,0,7 //jesd_clear_data=0xff; Address(0x80[15:8],)
SPIWrite 0021,02,0,7 //dac_jesd_root_clk_div=0x2;(Meaning: ); Address(0x20[15:8],)
SPIWrite 013d,01,0,7 //duc_wr_clk_ratio_tx0=0x1;(Meaning: ); Address(0x13c[12:8],)
SPIWrite 013f,00,0,7 //jesd_rx_clk_ratio_tx0=0x0;(Meaning: ); Address(0x13c[28:24],)
SPIWrite 0024,01,0,7 //jesd_mode_tx0=0x1;(Meaning: ); Address(0x24[4:0],)
SPIWrite 013d,21,0,7 //duc_wr_clk_ratio_tx0_override=0x1; Address(0x13c[13:13],)
SPIWrite 013f,20,0,7 //jesd_rx_clk_ratio_tx0_override=0x1; Address(0x13c[29:29],)
SPIWrite 002d,9b,0,7 //spi_txenable=0x1; Address(0x2c[8:8],)
SPIWrite 0093,ff,0,7 //spidac=0x7fff; Address(0x90[31:24],0x94[7:0],)
SPIWrite 0094,7f,0,7
SPIWrite 0093,ff,0,7 //spidac=0x7fff; Address(0x90[31:24],0x94[7:0],)
SPIWrite 0094,7f,0,7
SPIWrite 0092,00,0,7 //spidac_ena=0x0; Address(0x90[23:23],)
SPIWrite 0092,00,0,7 //spidac_ena=0x0; Address(0x90[23:23],)
SPIWrite 008b,df,0,7 //sync_request_ena=0xdf; Address(0x88[31:24],)
SPIWrite 008c,00,0,7 //error_ena=0x0; Address(0x8c[7:0],)
SPIWrite 0123,7f,0,7 //comma_align_valid_thresh_tx0=0x7f; Address(0x120[30:24],)
SPIWrite 0148,00,0,7 //jesd_h_ena=0x0; Address(0x148[1:1],)
SPIWrite 0148,00,0,7 //jesd_c_ena=0x0; Address(0x148[0:0],)
SPIWrite 007d,12,0,7 //scr=0x0; Address(0x7c[15:15],)
SPIWrite 007c,01,0,7 //lane_ena=0x1; Address(0x7c[7:0],)
SPIWrite 0134,04,0,7 //sysref_to_dac_jesd_clk_div_override=0x1; Address(0x134[2:2],)
SPIWrite 0020,01,0,7 //init_state_tx0=0x1;(Meaning: ); Address(0x20[0:0],)

```

\\END: Done configuring Device JESD RX

```

SPIWrite 0015,20,0,7 //PAGE: tx_jesd=0x0; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,00,0,7

```

\\EXTERNAL-ACTION: If you have a RX or TX DSA packet to be applied, apply it here.

\\ STEP: calibration/step0

\\START: Initializing TXIQMC

SPIWrite 0016,08,3,3 //PAGE: tx_iqmc=0x1; Address(0x16[3:3],)
SPIWrite 02ac,01,0,0
SPIWrite 02ac,03,1,1
SPIWrite 0016,00,3,3 //PAGE: tx_iqmc=0x0; Address(0x16[3:3],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1; (Meaning:); Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,80,0,7
SPIWrite 0193,27,0,7

\\Read macro_opcode_operand=0x27000000; Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0; (Meaning:); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address(0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address(0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address(0x20[6:6],)

\\Read MACRO_ERROR_IN_EXECUTION=0x0; Address(0x20[7:7],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)
SPIWrite 0016,08,3,3 //PAGE: tx_iqmc=0x1; Address(0x16[3:3],)
SPIWrite 0275,80,7,7
SPIWrite 0275,82,0,6
SPIWrite 0274,45,0,7
SPIWrite 0277,80,7,7
SPIWrite 0277,82,0,6
SPIWrite 0276,45,0,7
SPIWrite 0279,80,7,7
SPIWrite 0279,80,0,6
SPIWrite 0278,00,0,7
SPIWrite 027b,80,7,7
SPIWrite 027b,80,0,6
SPIWrite 027a,00,0,7
SPIWrite 0270,01,0,6
SPIWrite 0270,81,7,7
SPIWrite 0249,80,7,7
SPIWrite 0249,94,0,6
SPIWrite 0315,80,7,7
SPIWrite 0317,80,7,7
SPIWrite 0319,80,7,7
SPIWrite 031b,80,7,7
SPIWrite 0315,80,0,6
SPIWrite 0314,a2,0,7
SPIWrite 0317,80,0,6
SPIWrite 0316,a2,0,7

SPIWrite 0319,80,0,6
SPIWrite 0318,a2,0,7
SPIWrite 031b,80,0,6
SPIWrite 031a,a2,0,7
SPIWrite 024b,00,2,3
SPIWrite 024b,10,4,4
SPIWrite 02d5,02,1,1
SPIWrite 02d5,03,0,0
SPIWrite 030c,02,1,1
SPIWrite 030c,03,0,0
SPIWrite 02c5,01,0,0
SPIWrite 02c5,03,1,1
SPIWrite 0310,08,0,6
SPIWrite 0310,88,7,7
SPIWrite 0311,08,0,6
SPIWrite 0311,88,7,7
SPIWrite 0298,80,7,7
SPIWrite 0298,8f,0,6
SPIWrite 0289,80,7,7
SPIWrite 0289,94,0,6
SPIWrite 02a7,80,7,7
SPIWrite 02a7,93,0,6

\\START: Downloading TX IQMC Steady State Patch

SPIWrite 0016,00,3,3 //PAGE: tx_iqmc=0x0; Address(0x16[3:3],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1; (Meaning:); Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,80,0,7
SPIWrite 0193,31,0,7

\\Read macro_opcode_operand=0x31000000; Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0; (Meaning:); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address(0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address(0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address(0x20[6:6],)

\\Read MACRO_ERROR_IN_EXECUTION=0x0; Address(0x20[7:7],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)
SPIWrite 0012,10,4,4 //PAGE: sys_calib_macro_memory=0x1; (Meaning:); Address(0x12[4:4],)
SPIWrite 0040,00,0,7

SPIWrite 0020,00,0,7
SPIWrite 0021,00,0,7
SPIWrite 0022,00,0,7
SPIWrite 0023,00,0,7
SPIWrite 0024,21,0,7
SPIWrite 0025,c8,0,7
SPIWrite 0026,01,0,7
SPIWrite 0027,00,0,7
SPIWrite 0028,01,0,7
SPIWrite 0029,20,0,7
SPIWrite 002a,03,0,7
SPIWrite 002b,14,0,7
SPIWrite 002c,80,0,7
SPIWrite 002d,01,0,7
SPIWrite 002e,81,0,7
SPIWrite 002f,02,0,7
SPIWrite 0030,01,0,7
SPIWrite 0031,00,0,7
SPIWrite 0032,98,0,7
SPIWrite 0033,00,0,7
SPIWrite 0034,04,0,7
SPIWrite 0035,06,0,7
SPIWrite 0036,18,0,7
SPIWrite 0037,00,0,7
SPIWrite 0038,01,0,7
SPIWrite 0039,00,0,7
SPIWrite 003a,97,0,7
SPIWrite 003b,00,0,7
SPIWrite 003c,02,0,7
SPIWrite 003d,06,0,7
SPIWrite 003e,18,0,7
SPIWrite 003f,00,0,7
SPIWrite 0040,01,0,7
SPIWrite 0041,00,0,7
SPIWrite 0042,99,0,7
SPIWrite 0043,10,0,7
SPIWrite 0044,05,0,7
SPIWrite 0045,06,0,7
SPIWrite 0046,18,0,7
SPIWrite 0047,00,0,7
SPIWrite 0048,00,0,7
SPIWrite 0049,00,0,7
SPIWrite 004a,00,0,7
SPIWrite 004b,00,0,7
SPIWrite 004c,00,0,7
SPIWrite 004d,00,0,7
SPIWrite 004e,00,0,7
SPIWrite 004f,00,0,7
SPIWrite 0050,01,0,7
SPIWrite 0051,23,0,7
SPIWrite 0052,0b,0,7
SPIWrite 0053,31,0,7
SPIWrite 0054,40,0,7
SPIWrite 0055,00,0,7
SPIWrite 0056,8b,0,7
SPIWrite 0057,40,0,7
SPIWrite 0058,b3,0,7
SPIWrite 0059,fb,0,7
SPIWrite 005a,f2,0,7
SPIWrite 005b,f2,0,7
SPIWrite 005c,00,0,7
SPIWrite 005d,21,0,7
SPIWrite 005e,93,0,7
SPIWrite 005f,b2,0,7
SPIWrite 0060,db,0,7
SPIWrite 0061,04,0,7
SPIWrite 0062,0a,0,7
SPIWrite 0063,46,0,7
SPIWrite 0064,e0,0,7

SPIWrite 0065,fb,0,7
SPIWrite 0066,63,0,7
SPIWrite 0067,12,0,7
SPIWrite 0068,50,0,7
SPIWrite 0069,00,0,7
SPIWrite 006a,70,0,7
SPIWrite 006b,47,0,7
SPIWrite 006c,2d,0,7
SPIWrite 006d,e9,0,7
SPIWrite 006e,f0,0,7
SPIWrite 006f,47,0,7
SPIWrite 0070,df,0,7
SPIWrite 0071,f8,0,7
SPIWrite 0072,f8,0,7
SPIWrite 0073,92,0,7
SPIWrite 0074,8c,0,7
SPIWrite 0075,46,0,7
SPIWrite 0076,00,0,7
SPIWrite 0077,27,0,7
SPIWrite 0078,9a,0,7
SPIWrite 0079,46,0,7
SPIWrite 007a,05,0,7
SPIWrite 007b,46,0,7
SPIWrite 007c,4f,0,7
SPIWrite 007d,f0,0,7
SPIWrite 007e,ff,0,7
SPIWrite 007f,38,0,7
SPIWrite 0080,a2,0,7
SPIWrite 0081,f1,0,7
SPIWrite 0082,10,0,7
SPIWrite 0083,04,0,7
SPIWrite 0084,3e,0,7
SPIWrite 0085,46,0,7
SPIWrite 0086,10,0,7
SPIWrite 0087,34,0,7
SPIWrite 0088,20,0,7
SPIWrite 0089,7b,0,7
SPIWrite 008a,01,0,7
SPIWrite 008b,28,0,7
SPIWrite 008c,18,0,7
SPIWrite 008d,bf,0,7
SPIWrite 008e,bc,0,7
SPIWrite 008f,f1,0,7
SPIWrite 0090,00,0,7
SPIWrite 0091,0f,0,7
SPIWrite 0092,10,0,7
SPIWrite 0093,d1,0,7
SPIWrite 0094,b9,0,7
SPIWrite 0095,f8,0,7
SPIWrite 0096,00,0,7
SPIWrite 0097,30,0,7
SPIWrite 0098,f0,0,7
SPIWrite 0099,00,0,7
SPIWrite 009a,00,0,7
SPIWrite 009b,eb,0,7
SPIWrite 009c,46,0,7
SPIWrite 009d,10,0,7
SPIWrite 009e,29,0,7
SPIWrite 009f,18,0,7
SPIWrite 00a0,8a,0,7
SPIWrite 00a1,8c,0,7
SPIWrite 00a2,93,0,7
SPIWrite 00a3,42,0,7
SPIWrite 00a4,07,0,7
SPIWrite 00a5,dc,0,7
SPIWrite 00a6,08,0,7
SPIWrite 00a7,68,0,7
SPIWrite 00a8,c9,0,7
SPIWrite 00a9,8c,0,7

SPIWrite 00aa,ff,0,7
SPIWrite 00ab,f7,0,7
SPIWrite 00ac,d1,0,7
SPIWrite 00ad,ff,0,7
SPIWrite 00ae,87,0,7
SPIWrite 00af,42,0,7
SPIWrite 00b0,9c,0,7
SPIWrite 00b1,bf,0,7
SPIWrite 00b2,07,0,7
SPIWrite 00b3,1c,0,7
SPIWrite 00b4,b0,0,7
SPIWrite 00b5,46,0,7
SPIWrite 00b6,76,0,7
SPIWrite 00b7,1c,0,7
SPIWrite 00b8,40,0,7
SPIWrite 00b9,2e,0,7
SPIWrite 00ba,e4,0,7
SPIWrite 00bb,db,0,7
SPIWrite 00bc,ca,0,7
SPIWrite 00bd,f8,0,7
SPIWrite 00be,00,0,7
SPIWrite 00bf,70,0,7
SPIWrite 00c0,40,0,7
SPIWrite 00c1,46,0,7
SPIWrite 00c2,bd,0,7
SPIWrite 00c3,e8,0,7
SPIWrite 00c4,f0,0,7
SPIWrite 00c5,87,0,7
SPIWrite 00c6,70,0,7
SPIWrite 00c7,b5,0,7
SPIWrite 00c8,06,0,7
SPIWrite 00c9,46,0,7
SPIWrite 00ca,0c,0,7
SPIWrite 00cb,46,0,7
SPIWrite 00cc,30,0,7
SPIWrite 00cd,68,0,7
SPIWrite 00ce,71,0,7
SPIWrite 00cf,68,0,7
SPIWrite 00d0,f1,0,7
SPIWrite 00d1,f7,0,7
SPIWrite 00d2,e3,0,7
SPIWrite 00d3,f8,0,7
SPIWrite 00d4,05,0,7
SPIWrite 00d5,46,0,7
SPIWrite 00d6,b0,0,7
SPIWrite 00d7,68,0,7
SPIWrite 00d8,1f,0,7
SPIWrite 00d9,21,0,7
SPIWrite 00da,0f,0,7
SPIWrite 00db,22,0,7
SPIWrite 00dc,f1,0,7
SPIWrite 00dd,f7,0,7
SPIWrite 00de,21,0,7
SPIWrite 00df,fa,0,7
SPIWrite 00e0,00,0,7
SPIWrite 00e1,eb,0,7
SPIWrite 00e2,40,0,7
SPIWrite 00e3,00,0,7
SPIWrite 00e4,22,0,7
SPIWrite 00e5,46,0,7
SPIWrite 00e6,29,0,7
SPIWrite 00e7,1a,0,7
SPIWrite 00e8,00,0,7
SPIWrite 00e9,29,0,7
SPIWrite 00ea,28,0,7
SPIWrite 00eb,44,0,7
SPIWrite 00ec,d8,0,7
SPIWrite 00ed,bf,0,7
SPIWrite 00ee,00,0,7

SPIWrite 00ef,21,0,7
SPIWrite 00f0,10,0,7
SPIWrite 00f1,60,0,7
SPIWrite 00f2,51,0,7
SPIWrite 00f3,60,0,7
SPIWrite 00f4,70,0,7
SPIWrite 00f5,bd,0,7
SPIWrite 00f6,2d,0,7
SPIWrite 00f7,e9,0,7
SPIWrite 00f8,f0,0,7
SPIWrite 00f9,4f,0,7
SPIWrite 00fa,04,0,7
SPIWrite 00fb,46,0,7
SPIWrite 00fc,0d,0,7
SPIWrite 00fd,46,0,7
SPIWrite 00fe,ad,0,7
SPIWrite 00ff,f1,0,7
SPIWrite 0100,2c,0,7
SPIWrite 0101,0d,0,7
SPIWrite 0102,ea,0,7
SPIWrite 0103,f7,0,7
SPIWrite 0104,77,0,7
SPIWrite 0105,fc,0,7
SPIWrite 0106,c4,0,7
SPIWrite 0107,4e,0,7
SPIWrite 0108,30,0,7
SPIWrite 0109,78,0,7
SPIWrite 010a,00,0,7
SPIWrite 010b,28,0,7
SPIWrite 010c,00,0,7
SPIWrite 010d,f0,0,7
SPIWrite 010e,14,0,7
SPIWrite 010f,81,0,7
SPIWrite 0110,00,0,7
SPIWrite 0111,27,0,7
SPIWrite 0112,20,0,7
SPIWrite 0113,46,0,7
SPIWrite 0114,01,0,7
SPIWrite 0115,21,0,7
SPIWrite 0116,2a,0,7
SPIWrite 0117,46,0,7
SPIWrite 0118,06,0,7
SPIWrite 0119,ab,0,7
SPIWrite 011a,06,0,7
SPIWrite 011b,97,0,7
SPIWrite 011c,ff,0,7
SPIWrite 011d,f7,0,7
SPIWrite 011e,a6,0,7
SPIWrite 011f,ff,0,7
SPIWrite 0120,be,0,7
SPIWrite 0121,4c,0,7
SPIWrite 0122,bf,0,7
SPIWrite 0123,49,0,7
SPIWrite 0124,22,0,7
SPIWrite 0125,78,0,7
SPIWrite 0126,b0,0,7
SPIWrite 0127,f1,0,7
SPIWrite 0128,ff,0,7
SPIWrite 0129,3f,0,7
SPIWrite 012a,21,0,7
SPIWrite 012b,f8,0,7
SPIWrite 012c,12,0,7
SPIWrite 012d,00,0,7
SPIWrite 012e,00,0,7
SPIWrite 012f,f0,0,7
SPIWrite 0130,03,0,7
SPIWrite 0131,81,0,7
SPIWrite 0132,c0,0,7
SPIWrite 0133,b2,0,7

SPIWrite 0134,02,0,7
SPIWrite 0135,97,0,7
SPIWrite 0136,bb,0,7
SPIWrite 0137,49,0,7
SPIWrite 0138,03,0,7
SPIWrite 0139,97,0,7
SPIWrite 013a,05,0,7
SPIWrite 013b,eb,0,7
SPIWrite 013c,00,0,7
SPIWrite 013d,10,0,7
SPIWrite 013e,01,0,7
SPIWrite 013f,eb,0,7
SPIWrite 0140,02,0,7
SPIWrite 0141,11,0,7
SPIWrite 0142,90,0,7
SPIWrite 0143,e8,0,7
SPIWrite 0144,0c,0,7
SPIWrite 0145,50,0,7
SPIWrite 0146,81,0,7
SPIWrite 0147,e8,0,7
SPIWrite 0148,0c,0,7
SPIWrite 0149,50,0,7
SPIWrite 014a,02,0,7
SPIWrite 014b,a9,0,7
SPIWrite 014c,ff,0,7
SPIWrite 014d,f7,0,7
SPIWrite 014e,bb,0,7
SPIWrite 014f,ff,0,7
SPIWrite 0150,30,0,7
SPIWrite 0151,46,0,7
SPIWrite 0152,03,0,7
SPIWrite 0153,99,0,7
SPIWrite 0154,02,0,7
SPIWrite 0155,9a,0,7
SPIWrite 0156,40,0,7
SPIWrite 0157,88,0,7
SPIWrite 0158,df,0,7
SPIWrite 0159,f8,0,7
SPIWrite 015a,e0,0,7
SPIWrite 015b,82,0,7
SPIWrite 015c,df,0,7
SPIWrite 015d,f8,0,7
SPIWrite 015e,c8,0,7
SPIWrite 015f,a2,0,7
SPIWrite 0160,a5,0,7
SPIWrite 0161,f1,0,7
SPIWrite 0162,10,0,7
SPIWrite 0163,09,0,7
SPIWrite 0164,c9,0,7
SPIWrite 0165,00,0,7
SPIWrite 0166,42,0,7
SPIWrite 0167,43,0,7
SPIWrite 0168,b1,0,7
SPIWrite 0169,fb,0,7
SPIWrite 016a,f0,0,7
SPIWrite 016b,f0,0,7
SPIWrite 016c,09,0,7
SPIWrite 016d,90,0,7
SPIWrite 016e,d0,0,7
SPIWrite 016f,08,0,7
SPIWrite 0170,08,0,7
SPIWrite 0171,90,0,7
SPIWrite 0172,09,0,7
SPIWrite 0173,f1,0,7
SPIWrite 0174,10,0,7
SPIWrite 0175,09,0,7
SPIWrite 0176,99,0,7
SPIWrite 0177,f8,0,7
SPIWrite 0178,0c,0,7

SPIWrite 0179,00,0,7
SPIWrite 017a,01,0,7
SPIWrite 017b,28,0,7
SPIWrite 017c,18,0,7
SPIWrite 017d,bf,0,7
SPIWrite 017e,04,0,7
SPIWrite 017f,28,0,7
SPIWrite 0180,40,0,7
SPIWrite 0181,f0,0,7
SPIWrite 0182,d6,0,7
SPIWrite 0183,80,0,7
SPIWrite 0184,d9,0,7
SPIWrite 0185,f8,0,7
SPIWrite 0186,00,0,7
SPIWrite 0187,00,0,7
SPIWrite 0188,d9,0,7
SPIWrite 0189,f8,0,7
SPIWrite 018a,04,0,7
SPIWrite 018b,10,0,7
SPIWrite 018c,f1,0,7
SPIWrite 018d,f7,0,7
SPIWrite 018e,85,0,7
SPIWrite 018f,f8,0,7
SPIWrite 0190,08,0,7
SPIWrite 0191,99,0,7
SPIWrite 0192,81,0,7
SPIWrite 0193,42,0,7
SPIWrite 0194,03,0,7
SPIWrite 0195,d3,0,7
SPIWrite 0196,09,0,7
SPIWrite 0197,99,0,7
SPIWrite 0198,81,0,7
SPIWrite 0199,42,0,7
SPIWrite 019a,40,0,7
SPIWrite 019b,f2,0,7
SPIWrite 019c,c9,0,7
SPIWrite 019d,80,0,7
SPIWrite 019e,20,0,7
SPIWrite 019f,20,0,7
SPIWrite 01a0,21,0,7
SPIWrite 01a1,78,0,7
SPIWrite 01a2,89,0,7
SPIWrite 01a3,f8,0,7
SPIWrite 01a4,0c,0,7
SPIWrite 01a5,00,0,7
SPIWrite 01a6,0a,0,7
SPIWrite 01a7,eb,0,7
SPIWrite 01a8,01,0,7
SPIWrite 01a9,05,0,7
SPIWrite 01aa,2b,0,7
SPIWrite 01ab,78,0,7
SPIWrite 01ac,fa,0,7
SPIWrite 01ad,10,0,7
SPIWrite 01ae,90,0,7
SPIWrite 01af,08,0,7
SPIWrite 01b0,5b,0,7
SPIWrite 01b1,1c,0,7
SPIWrite 01b2,2b,0,7
SPIWrite 01b3,70,0,7
SPIWrite 01b4,98,0,7
SPIWrite 01b5,4b,0,7
SPIWrite 01b6,07,0,7
SPIWrite 01b7,90,0,7
SPIWrite 01b8,00,0,7
SPIWrite 01b9,eb,0,7
SPIWrite 01ba,41,0,7
SPIWrite 01bb,00,0,7
SPIWrite 01bc,0a,0,7
SPIWrite 01bd,eb,0,7

SPIWrite 01be,80,0,7
SPIWrite 01bf,00,0,7
SPIWrite 01c0,5e,0,7
SPIWrite 01c1,78,0,7
SPIWrite 01c2,43,0,7
SPIWrite 01c3,68,0,7
SPIWrite 01c4,4f,0,7
SPIWrite 01c5,f0,0,7
SPIWrite 01c6,01,0,7
SPIWrite 01c7,0b,0,7
SPIWrite 01c8,07,0,7
SPIWrite 01c9,f0,0,7
SPIWrite 01ca,1f,0,7
SPIWrite 01cb,05,0,7
SPIWrite 01cc,00,0,7
SPIWrite 01cd,2e,0,7
SPIWrite 01ce,0b,0,7
SPIWrite 01cf,fa,0,7
SPIWrite 01d0,05,0,7
SPIWrite 01d1,fb,0,7
SPIWrite 01d2,4b,0,7
SPIWrite 01d3,ea,0,7
SPIWrite 01d4,03,0,7
SPIWrite 01d5,03,0,7
SPIWrite 01d6,43,0,7
SPIWrite 01d7,60,0,7
SPIWrite 01d8,00,0,7
SPIWrite 01d9,f0,0,7
SPIWrite 01da,aa,0,7
SPIWrite 01db,80,0,7
SPIWrite 01dc,90,0,7
SPIWrite 01dd,10,0,7
SPIWrite 01de,93,0,7
SPIWrite 01df,4a,0,7
SPIWrite 01e0,80,0,7
SPIWrite 01e1,00,0,7
SPIWrite 01e2,00,0,7
SPIWrite 01e3,eb,0,7
SPIWrite 01e4,c1,0,7
SPIWrite 01e5,00,0,7
SPIWrite 01e6,10,0,7
SPIWrite 01e7,58,0,7
SPIWrite 01e8,1b,0,7
SPIWrite 01e9,ea,0,7
SPIWrite 01ea,00,0,7
SPIWrite 01eb,0f,0,7
SPIWrite 01ec,40,0,7
SPIWrite 01ed,f0,0,7
SPIWrite 01ee,82,0,7
SPIWrite 01ef,80,0,7
SPIWrite 01f0,91,0,7
SPIWrite 01f1,4e,0,7
SPIWrite 01f2,10,0,7
SPIWrite 01f3,46,0,7
SPIWrite 01f4,00,0,7
SPIWrite 01f5,eb,0,7
SPIWrite 01f6,c1,0,7
SPIWrite 01f7,00,0,7
SPIWrite 01f8,8d,0,7
SPIWrite 01f9,4d,0,7
SPIWrite 01fa,00,0,7
SPIWrite 01fb,90,0,7
SPIWrite 01fc,b3,0,7
SPIWrite 01fd,6a,0,7
SPIWrite 01fe,8d,0,7
SPIWrite 01ff,4e,0,7
SPIWrite 0200,0a,0,7
SPIWrite 0201,02,0,7
SPIWrite 0202,05,0,7

SPIWrite 0203,eb,0,7
SPIWrite 0204,81,0,7
SPIWrite 0205,25,0,7
SPIWrite 0206,02,0,7
SPIWrite 0207,eb,0,7
SPIWrite 0208,41,0,7
SPIWrite 0209,20,0,7
SPIWrite 020a,3a,0,7
SPIWrite 020b,46,0,7
SPIWrite 020c,31,0,7
SPIWrite 020d,18,0,7
SPIWrite 020e,28,0,7
SPIWrite 020f,46,0,7
SPIWrite 0210,00,0,7
SPIWrite 0211,f0,0,7
SPIWrite 0212,e7,0,7
SPIWrite 0213,fc,0,7
SPIWrite 0214,20,0,7
SPIWrite 0215,78,0,7
SPIWrite 0216,4f,0,7
SPIWrite 0217,f4,0,7
SPIWrite 0218,42,0,7
SPIWrite 0219,71,0,7
SPIWrite 021a,10,0,7
SPIWrite 021b,fb,0,7
SPIWrite 021c,01,0,7
SPIWrite 021d,80,0,7
SPIWrite 021e,39,0,7
SPIWrite 021f,46,0,7
SPIWrite 0220,00,0,7
SPIWrite 0221,1d,0,7
SPIWrite 0222,00,0,7
SPIWrite 0223,f0,0,7
SPIWrite 0224,a7,0,7
SPIWrite 0225,fb,0,7
SPIWrite 0226,84,0,7
SPIWrite 0227,49,0,7
SPIWrite 0228,20,0,7
SPIWrite 0229,78,0,7
SPIWrite 022a,86,0,7
SPIWrite 022b,4d,0,7
SPIWrite 022c,84,0,7
SPIWrite 022d,4e,0,7
SPIWrite 022e,0a,0,7
SPIWrite 022f,69,0,7
SPIWrite 0230,03,0,7
SPIWrite 0231,02,0,7
SPIWrite 0232,03,0,7
SPIWrite 0233,eb,0,7
SPIWrite 0234,40,0,7
SPIWrite 0235,23,0,7
SPIWrite 0236,e9,0,7
SPIWrite 0237,18,0,7
SPIWrite 0238,f0,0,7
SPIWrite 0239,18,0,7
SPIWrite 023a,3b,0,7
SPIWrite 023b,46,0,7
SPIWrite 023c,00,0,7
SPIWrite 023d,f0,0,7
SPIWrite 023e,c1,0,7
SPIWrite 023f,fc,0,7
SPIWrite 0240,21,0,7
SPIWrite 0241,78,0,7
SPIWrite 0242,00,0,7
SPIWrite 0243,25,0,7
SPIWrite 0244,2e,0,7
SPIWrite 0245,46,0,7
SPIWrite 0246,4f,0,7
SPIWrite 0247,f4,0,7

SPIWrite 0248,42,0,7
SPIWrite 0249,70,0,7
SPIWrite 024a,48,0,7
SPIWrite 024b,43,0,7
SPIWrite 024c,2a,0,7
SPIWrite 024d,18,0,7
SPIWrite 024e,42,0,7
SPIWrite 024f,44,0,7
SPIWrite 0250,12,0,7
SPIWrite 0251,7b,0,7
SPIWrite 0252,01,0,7
SPIWrite 0253,2a,0,7
SPIWrite 0254,18,0,7
SPIWrite 0255,d1,0,7
SPIWrite 0256,c3,0,7
SPIWrite 0257,4b,0,7
SPIWrite 0258,06,0,7
SPIWrite 0259,eb,0,7
SPIWrite 025a,81,0,7
SPIWrite 025b,12,0,7
SPIWrite 025c,9a,0,7
SPIWrite 025d,5c,0,7
SPIWrite 025e,97,0,7
SPIWrite 025f,42,0,7
SPIWrite 0260,12,0,7
SPIWrite 0261,d1,0,7
SPIWrite 0262,40,0,7
SPIWrite 0263,44,0,7
SPIWrite 0264,31,0,7
SPIWrite 0265,46,0,7
SPIWrite 0266,00,0,7
SPIWrite 0267,1d,0,7
SPIWrite 0268,00,0,7
SPIWrite 0269,f0,0,7
SPIWrite 026a,84,0,7
SPIWrite 026b,fb,0,7
SPIWrite 026c,72,0,7
SPIWrite 026d,49,0,7
SPIWrite 026e,20,0,7
SPIWrite 026f,78,0,7
SPIWrite 0270,0a,0,7
SPIWrite 0271,69,0,7
SPIWrite 0272,03,0,7
SPIWrite 0273,02,0,7
SPIWrite 0274,03,0,7
SPIWrite 0275,eb,0,7
SPIWrite 0276,40,0,7
SPIWrite 0277,23,0,7
SPIWrite 0278,72,0,7
SPIWrite 0279,49,0,7
SPIWrite 027a,71,0,7
SPIWrite 027b,48,0,7
SPIWrite 027c,c9,0,7
SPIWrite 027d,18,0,7
SPIWrite 027e,c0,0,7
SPIWrite 027f,18,0,7
SPIWrite 0280,33,0,7
SPIWrite 0281,46,0,7
SPIWrite 0282,00,0,7
SPIWrite 0283,f0,0,7
SPIWrite 0284,9e,0,7
SPIWrite 0285,fc,0,7
SPIWrite 0286,21,0,7
SPIWrite 0287,78,0,7
SPIWrite 0288,76,0,7
SPIWrite 0289,1c,0,7
SPIWrite 028a,0c,0,7
SPIWrite 028b,35,0,7
SPIWrite 028c,40,0,7

SPIWrite 028d,2e,0,7
SPIWrite 028e,da,0,7
SPIWrite 028f,db,0,7
SPIWrite 0290,b5,0,7
SPIWrite 0291,4a,0,7
SPIWrite 0292,c8,0,7
SPIWrite 0293,00,0,7
SPIWrite 0294,00,0,7
SPIWrite 0295,eb,0,7
SPIWrite 0296,01,0,7
SPIWrite 0297,10,0,7
SPIWrite 0298,12,0,7
SPIWrite 0299,18,0,7
SPIWrite 029a,12,0,7
SPIWrite 029b,79,0,7
SPIWrite 029c,97,0,7
SPIWrite 029d,42,0,7
SPIWrite 029e,19,0,7
SPIWrite 029f,d1,0,7
SPIWrite 02a0,b2,0,7
SPIWrite 02a1,49,0,7
SPIWrite 02a2,b1,0,7
SPIWrite 02a3,4a,0,7
SPIWrite 02a4,d1,0,7
SPIWrite 02a5,f8,0,7
SPIWrite 02a6,f0,0,7
SPIWrite 02a7,11,0,7
SPIWrite 02a8,80,0,7
SPIWrite 02a9,18,0,7
SPIWrite 02aa,08,0,7
SPIWrite 02ab,30,0,7
SPIWrite 02ac,88,0,7
SPIWrite 02ad,47,0,7
SPIWrite 02ae,af,0,7
SPIWrite 02af,49,0,7
SPIWrite 02b0,00,0,7
SPIWrite 02b1,23,0,7
SPIWrite 02b2,1e,0,7
SPIWrite 02b3,46,0,7
SPIWrite 02b4,ad,0,7
SPIWrite 02b5,f8,0,7
SPIWrite 02b6,10,0,7
SPIWrite 02b7,30,0,7
SPIWrite 02b8,22,0,7
SPIWrite 02b9,78,0,7
SPIWrite 02ba,c1,0,7
SPIWrite 02bb,4d,0,7
SPIWrite 02bc,d1,0,7
SPIWrite 02bd,f8,0,7
SPIWrite 02be,30,0,7
SPIWrite 02bf,12,0,7
SPIWrite 02c0,ad,0,7
SPIWrite 02c1,f8,0,7
SPIWrite 02c2,12,0,7
SPIWrite 02c3,60,0,7
SPIWrite 02c4,0b,0,7
SPIWrite 02c5,46,0,7
SPIWrite 02c6,04,0,7
SPIWrite 02c7,99,0,7
SPIWrite 02c8,90,0,7
SPIWrite 02c9,00,0,7
SPIWrite 02ca,00,0,7
SPIWrite 02cb,eb,0,7
SPIWrite 02cc,c2,0,7
SPIWrite 02cd,00,0,7
SPIWrite 02ce,28,0,7
SPIWrite 02cf,18,0,7
SPIWrite 02d0,98,0,7
SPIWrite 02d1,47,0,7

SPIWrite 02d2,21,0,7
SPIWrite 02d3,78,0,7
SPIWrite 02d4,0a,0,7
SPIWrite 02d5,eb,0,7
SPIWrite 02d6,01,0,7
SPIWrite 02d7,03,0,7
SPIWrite 02d8,07,0,7
SPIWrite 02d9,9a,0,7
SPIWrite 02da,93,0,7
SPIWrite 02db,f8,0,7
SPIWrite 02dc,24,0,7
SPIWrite 02dd,00,0,7
SPIWrite 02de,02,0,7
SPIWrite 02df,eb,0,7
SPIWrite 02e0,41,0,7
SPIWrite 02e1,02,0,7
SPIWrite 02e2,40,0,7
SPIWrite 02e3,1c,0,7
SPIWrite 02e4,0a,0,7
SPIWrite 02e5,eb,0,7
SPIWrite 02e6,82,0,7
SPIWrite 02e7,02,0,7
SPIWrite 02e8,83,0,7
SPIWrite 02e9,f8,0,7
SPIWrite 02ea,24,0,7
SPIWrite 02eb,00,0,7
SPIWrite 02ec,90,0,7
SPIWrite 02ed,6a,0,7
SPIWrite 02ee,4b,0,7
SPIWrite 02ef,ea,0,7
SPIWrite 02f0,00,0,7
SPIWrite 02f1,00,0,7
SPIWrite 02f2,90,0,7
SPIWrite 02f3,62,0,7
SPIWrite 02f4,e7,0,7
SPIWrite 02f5,b9,0,7
SPIWrite 02f6,ba,0,7
SPIWrite 02f7,48,0,7
SPIWrite 02f8,46,0,7
SPIWrite 02f9,5c,0,7
SPIWrite 02fa,ce,0,7
SPIWrite 02fb,b9,0,7
SPIWrite 02fc,9b,0,7
SPIWrite 02fd,48,0,7
SPIWrite 02fe,b9,0,7
SPIWrite 02ff,4e,0,7
SPIWrite 0300,d0,0,7
SPIWrite 0301,f8,0,7
SPIWrite 0302,2c,0,7
SPIWrite 0303,22,0,7
SPIWrite 0304,06,0,7
SPIWrite 0305,eb,0,7
SPIWrite 0306,01,0,7
SPIWrite 0307,10,0,7
SPIWrite 0308,90,0,7
SPIWrite 0309,47,0,7
SPIWrite 030a,98,0,7
SPIWrite 030b,48,0,7
SPIWrite 030c,22,0,7
SPIWrite 030d,78,0,7
SPIWrite 030e,96,0,7
SPIWrite 030f,4d,0,7
SPIWrite 0310,d0,0,7
SPIWrite 0311,f8,0,7
SPIWrite 0312,f0,0,7
SPIWrite 0313,31,0,7
SPIWrite 0314,d1,0,7
SPIWrite 0315,00,0,7
SPIWrite 0316,01,0,7

SPIWrite 0317,eb,0,7
SPIWrite 0318,02,0,7
SPIWrite 0319,11,0,7
SPIWrite 031a,05,0,7
SPIWrite 031b,f1,0,7
SPIWrite 031c,08,0,7
SPIWrite 031d,00,0,7
SPIWrite 031e,40,0,7
SPIWrite 031f,18,0,7
SPIWrite 0320,98,0,7
SPIWrite 0321,47,0,7
SPIWrite 0322,20,0,7
SPIWrite 0323,78,0,7
SPIWrite 0324,50,0,7
SPIWrite 0325,44,0,7
SPIWrite 0326,90,0,7
SPIWrite 0327,f8,0,7
SPIWrite 0328,48,0,7
SPIWrite 0329,10,0,7
SPIWrite 032a,49,0,7
SPIWrite 032b,1c,0,7
SPIWrite 032c,80,0,7
SPIWrite 032d,f8,0,7
SPIWrite 032e,48,0,7
SPIWrite 032f,10,0,7
SPIWrite 0330,7f,0,7
SPIWrite 0331,1c,0,7
SPIWrite 0332,40,0,7
SPIWrite 0333,2f,0,7
SPIWrite 0334,ff,0,7
SPIWrite 0335,f6,0,7
SPIWrite 0336,1d,0,7
SPIWrite 0337,af,0,7
SPIWrite 0338,0b,0,7
SPIWrite 0339,b0,0,7
SPIWrite 033a,bd,0,7
SPIWrite 033b,e8,0,7
SPIWrite 033c,f0,0,7
SPIWrite 033d,8f,0,7
SPIWrite 033e,98,0,7
SPIWrite 033f,b5,0,7
SPIWrite 0340,20,0,7
SPIWrite 0341,22,0,7
SPIWrite 0342,00,0,7
SPIWrite 0343,27,0,7
SPIWrite 0344,00,0,7
SPIWrite 0345,2f,0,7
SPIWrite 0346,14,0,7
SPIWrite 0347,bf,0,7
SPIWrite 0348,c7,0,7
SPIWrite 0349,f1,0,7
SPIWrite 034a,40,0,7
SPIWrite 034b,03,0,7
SPIWrite 034c,00,0,7
SPIWrite 034d,23,0,7
SPIWrite 034e,04,0,7
SPIWrite 034f,7b,0,7
SPIWrite 0350,01,0,7
SPIWrite 0351,2c,0,7
SPIWrite 0352,05,0,7
SPIWrite 0353,d1,0,7
SPIWrite 0354,01,0,7
SPIWrite 0355,eb,0,7
SPIWrite 0356,03,0,7
SPIWrite 0357,13,0,7
SPIWrite 0358,1b,0,7
SPIWrite 0359,7b,0,7
SPIWrite 035a,20,0,7
SPIWrite 035b,2b,0,7

SPIWrite 035c,08,0,7
SPIWrite 035d,bf,0,7
SPIWrite 035e,02,0,7
SPIWrite 035f,73,0,7
SPIWrite 0360,7f,0,7
SPIWrite 0361,1c,0,7
SPIWrite 0362,10,0,7
SPIWrite 0363,30,0,7
SPIWrite 0364,40,0,7
SPIWrite 0365,2f,0,7
SPIWrite 0366,ed,0,7
SPIWrite 0367,db,0,7
SPIWrite 0368,98,0,7
SPIWrite 0369,bd,0,7
SPIWrite 036a,c0,0,7
SPIWrite 036b,46,0,7
SPIWrite 036c,bc,0,7
SPIWrite 036d,19,0,7
SPIWrite 036e,01,0,7
SPIWrite 036f,20,0,7
SPIWrite 0370,80,0,7
SPIWrite 0371,b5,0,7
SPIWrite 0372,a0,0,7
SPIWrite 0373,fb,0,7
SPIWrite 0374,00,0,7
SPIWrite 0375,30,0,7
SPIWrite 0376,07,0,7
SPIWrite 0377,1c,0,7
SPIWrite 0378,07,0,7
SPIWrite 0379,bf,0,7
SPIWrite 037a,b3,0,7
SPIWrite 037b,fa,0,7
SPIWrite 037c,83,0,7
SPIWrite 037d,f0,0,7
SPIWrite 037e,20,0,7
SPIWrite 037f,30,0,7
SPIWrite 0380,00,0,7
SPIWrite 0381,1c,0,7
SPIWrite 0382,b0,0,7
SPIWrite 0383,fa,0,7
SPIWrite 0384,80,0,7
SPIWrite 0385,f0,0,7
SPIWrite 0386,c0,0,7
SPIWrite 0387,f1,0,7
SPIWrite 0388,3f,0,7
SPIWrite 0389,00,0,7
SPIWrite 038a,a0,0,7
SPIWrite 038b,eb,0,7
SPIWrite 038c,41,0,7
SPIWrite 038d,00,0,7
SPIWrite 038e,40,0,7
SPIWrite 038f,b2,0,7
SPIWrite 0390,02,0,7
SPIWrite 0391,eb,0,7
SPIWrite 0392,a0,0,7
SPIWrite 0393,02,0,7
SPIWrite 0394,00,0,7
SPIWrite 0395,f0,0,7
SPIWrite 0396,03,0,7
SPIWrite 0397,01,0,7
SPIWrite 0398,52,0,7
SPIWrite 0399,b2,0,7
SPIWrite 039a,10,0,7
SPIWrite 039b,f0,0,7
SPIWrite 039c,03,0,7
SPIWrite 039d,0f,0,7
SPIWrite 039e,07,0,7
SPIWrite 039f,d1,0,7
SPIWrite 03a0,20,0,7

SPIWrite 03a1,2a,0,7
SPIWrite 03a2,17,0,7
SPIWrite 03a3,da,0,7
SPIWrite 03a4,00,0,7
SPIWrite 03a5,2a,0,7
SPIWrite 03a6,4e,0,7
SPIWrite 03a7,bf,0,7
SPIWrite 03a8,00,0,7
SPIWrite 03a9,20,0,7
SPIWrite 03aa,01,0,7
SPIWrite 03ab,20,0,7
SPIWrite 03ac,90,0,7
SPIWrite 03ad,40,0,7
SPIWrite 03ae,80,0,7
SPIWrite 03af,bd,0,7
SPIWrite 03b0,8d,0,7
SPIWrite 03b1,48,0,7
SPIWrite 03b2,1f,0,7
SPIWrite 03b3,2a,0,7
SPIWrite 03b4,11,0,7
SPIWrite 03b5,d0,0,7
SPIWrite 03b6,20,0,7
SPIWrite 03b7,2a,0,7
SPIWrite 03b8,0c,0,7
SPIWrite 03b9,da,0,7
SPIWrite 03ba,50,0,7
SPIWrite 03bb,f8,0,7
SPIWrite 03bc,21,0,7
SPIWrite 03bd,10,0,7
SPIWrite 03be,c2,0,7
SPIWrite 03bf,f1,0,7
SPIWrite 03c0,1e,0,7
SPIWrite 03c1,00,0,7
SPIWrite 03c2,c2,0,7
SPIWrite 03c3,f1,0,7
SPIWrite 03c4,1f,0,7
SPIWrite 03c5,02,0,7
SPIWrite 03c6,21,0,7
SPIWrite 03c7,fa,0,7
SPIWrite 03c8,00,0,7
SPIWrite 03c9,f0,0,7
SPIWrite 03ca,00,0,7
SPIWrite 03cb,f0,0,7
SPIWrite 03cc,01,0,7
SPIWrite 03cd,00,0,7
SPIWrite 03ce,d1,0,7
SPIWrite 03cf,40,0,7
SPIWrite 03d0,40,0,7
SPIWrite 03d1,18,0,7
SPIWrite 03d2,80,0,7
SPIWrite 03d3,bd,0,7
SPIWrite 03d4,4f,0,7
SPIWrite 03d5,f0,0,7
SPIWrite 03d6,ff,0,7
SPIWrite 03d7,30,0,7
SPIWrite 03d8,80,0,7
SPIWrite 03d9,bd,0,7
SPIWrite 03da,50,0,7
SPIWrite 03db,f8,0,7
SPIWrite 03dc,21,0,7
SPIWrite 03dd,00,0,7
SPIWrite 03de,80,0,7
SPIWrite 03df,bd,0,7
SPIWrite 03e0,f8,0,7
SPIWrite 03e1,b5,0,7
SPIWrite 03e2,06,0,7
SPIWrite 03e3,af,0,7
SPIWrite 03e4,3d,0,7
SPIWrite 03e5,88,0,7

SPIWrite 03e6,b0,0,7
SPIWrite 03e7,f1,0,7
SPIWrite 03e8,ff,0,7
SPIWrite 03e9,3f,0,7
SPIWrite 03ea,1e,0,7
SPIWrite 03eb,46,0,7
SPIWrite 03ec,14,0,7
SPIWrite 03ed,46,0,7
SPIWrite 03ee,10,0,7
SPIWrite 03ef,d0,0,7
SPIWrite 03f0,08,0,7
SPIWrite 03f1,46,0,7
SPIWrite 03f2,0c,0,7
SPIWrite 03f3,22,0,7
SPIWrite 03f4,1e,0,7
SPIWrite 03f5,21,0,7
SPIWrite 03f6,ff,0,7
SPIWrite 03f7,f7,0,7
SPIWrite 03f8,bb,0,7
SPIWrite 03f9,ff,0,7
SPIWrite 03fa,80,0,7
SPIWrite 03fb,b2,0,7
SPIWrite 03fc,44,0,7
SPIWrite 03fd,43,0,7
SPIWrite 03fe,20,0,7
SPIWrite 03ff,0c,0,7
SPIWrite 0400,85,0,7
SPIWrite 0401,42,0,7
SPIWrite 0402,d4,0,7
SPIWrite 0403,bf,0,7
SPIWrite 0404,29,0,7
SPIWrite 0405,1c,0,7
SPIWrite 0406,01,0,7
SPIWrite 0407,1c,0,7
SPIWrite 0408,8e,0,7
SPIWrite 0409,42,0,7
SPIWrite 040a,02,0,7
SPIWrite 040b,da,0,7
SPIWrite 040c,85,0,7
SPIWrite 040d,42,0,7
SPIWrite 040e,01,0,7
SPIWrite 040f,dc,0,7
SPIWrite 0410,2e,0,7
SPIWrite 0411,46,0,7
SPIWrite 0412,30,0,7
SPIWrite 0413,46,0,7
SPIWrite 0414,f8,0,7
SPIWrite 0415,bd,0,7
SPIWrite 0416,c0,0,7
SPIWrite 0417,46,0,7
SPIWrite 0418,84,0,7
SPIWrite 0419,21,0,7
SPIWrite 041a,01,0,7
SPIWrite 041b,20,0,7
SPIWrite 041c,99,0,7
SPIWrite 041d,21,0,7
SPIWrite 041e,01,0,7
SPIWrite 041f,20,0,7
SPIWrite 0420,7c,0,7
SPIWrite 0421,21,0,7
SPIWrite 0422,01,0,7
SPIWrite 0423,20,0,7
SPIWrite 0424,00,0,7
SPIWrite 0425,21,0,7
SPIWrite 0426,01,0,7
SPIWrite 0427,20,0,7
SPIWrite 0428,00,0,7
SPIWrite 0429,7e,0,7
SPIWrite 042a,01,0,7

SPIWrite 042b,20,0,7
SPIWrite 042c,40,0,7
SPIWrite 042d,21,0,7
SPIWrite 042e,01,0,7
SPIWrite 042f,20,0,7
SPIWrite 0430,70,0,7
SPIWrite 0431,8f,0,7
SPIWrite 0432,00,0,7
SPIWrite 0433,20,0,7
SPIWrite 0434,90,0,7
SPIWrite 0435,bb,0,7
SPIWrite 0436,00,0,7
SPIWrite 0437,20,0,7
SPIWrite 0438,8c,0,7
SPIWrite 0439,1b,0,7
SPIWrite 043a,01,0,7
SPIWrite 043b,20,0,7
SPIWrite 043c,70,0,7
SPIWrite 043d,af,0,7
SPIWrite 043e,00,0,7
SPIWrite 043f,20,0,7
SPIWrite 0440,90,0,7
SPIWrite 0441,d3,0,7
SPIWrite 0442,00,0,7
SPIWrite 0443,20,0,7
SPIWrite 0444,90,0,7
SPIWrite 0445,df,0,7
SPIWrite 0446,00,0,7
SPIWrite 0447,20,0,7
SPIWrite 0448,2d,0,7
SPIWrite 0449,e9,0,7
SPIWrite 044a,f0,0,7
SPIWrite 044b,4f,0,7
SPIWrite 044c,09,0,7
SPIWrite 044d,af,0,7
SPIWrite 044e,9b,0,7
SPIWrite 044f,46,0,7
SPIWrite 0450,ad,0,7
SPIWrite 0451,f1,0,7
SPIWrite 0452,2c,0,7
SPIWrite 0453,0d,0,7
SPIWrite 0454,06,0,7
SPIWrite 0455,92,0,7
SPIWrite 0456,3e,0,7
SPIWrite 0457,7a,0,7
SPIWrite 0458,05,0,7
SPIWrite 0459,91,0,7
SPIWrite 045a,3b,0,7
SPIWrite 045b,68,0,7
SPIWrite 045c,04,0,7
SPIWrite 045d,90,0,7
SPIWrite 045e,00,0,7
SPIWrite 045f,2e,0,7
SPIWrite 0460,07,0,7
SPIWrite 0461,93,0,7
SPIWrite 0462,73,0,7
SPIWrite 0463,d1,0,7
SPIWrite 0464,a3,0,7
SPIWrite 0465,4c,0,7
SPIWrite 0466,60,0,7
SPIWrite 0467,78,0,7
SPIWrite 0468,00,0,7
SPIWrite 0469,28,0,7
SPIWrite 046a,04,0,7
SPIWrite 046b,bf,0,7
SPIWrite 046c,62,0,7
SPIWrite 046d,89,0,7
SPIWrite 046e,10,0,7
SPIWrite 046f,1c,0,7

SPIWrite 0470,0e,0,7
SPIWrite 0471,d0,0,7
SPIWrite 0472,04,0,7
SPIWrite 0473,98,0,7
SPIWrite 0474,05,0,7
SPIWrite 0475,9a,0,7
SPIWrite 0476,00,0,7
SPIWrite 0477,21,0,7
SPIWrite 0478,03,0,7
SPIWrite 0479,ab,0,7
SPIWrite 047a,03,0,7
SPIWrite 047b,91,0,7
SPIWrite 047c,ff,0,7
SPIWrite 047d,f7,0,7
SPIWrite 047e,f6,0,7
SPIWrite 047f,fd,0,7
SPIWrite 0480,a1,0,7
SPIWrite 0481,89,0,7
SPIWrite 0482,00,0,7
SPIWrite 0483,91,0,7
SPIWrite 0484,22,0,7
SPIWrite 0485,89,0,7
SPIWrite 0486,63,0,7
SPIWrite 0487,89,0,7
SPIWrite 0488,03,0,7
SPIWrite 0489,99,0,7
SPIWrite 048a,ff,0,7
SPIWrite 048b,f7,0,7
SPIWrite 048c,a9,0,7
SPIWrite 048d,ff,0,7
SPIWrite 048e,62,0,7
SPIWrite 048f,89,0,7
SPIWrite 0490,df,0,7
SPIWrite 0491,f8,0,7
SPIWrite 0492,64,0,7
SPIWrite 0493,82,0,7
SPIWrite 0494,00,0,7
SPIWrite 0495,27,0,7
SPIWrite 0496,99,0,7
SPIWrite 0497,49,0,7
SPIWrite 0498,df,0,7
SPIWrite 0499,f8,0,7
SPIWrite 049a,d0,0,7
SPIWrite 049b,a0,0,7
SPIWrite 049c,a8,0,7
SPIWrite 049d,f8,0,7
SPIWrite 049e,42,0,7
SPIWrite 049f,00,0,7
SPIWrite 04a0,3e,0,7
SPIWrite 04a1,46,0,7
SPIWrite 04a2,b8,0,7
SPIWrite 04a3,f8,0,7
SPIWrite 04a4,42,0,7
SPIWrite 04a5,30,0,7
SPIWrite 04a6,04,0,7
SPIWrite 04a7,9d,0,7
SPIWrite 04a8,05,0,7
SPIWrite 04a9,9c,0,7
SPIWrite 04aa,48,0,7
SPIWrite 04ab,78,0,7
SPIWrite 04ac,08,0,7
SPIWrite 04ad,93,0,7
SPIWrite 04ae,50,0,7
SPIWrite 04af,43,0,7
SPIWrite 04b0,1f,0,7
SPIWrite 04b1,fa,0,7
SPIWrite 04b2,90,0,7
SPIWrite 04b3,f0,0,7
SPIWrite 04b4,09,0,7

SPIWrite 04b5,90,0,7
SPIWrite 04b6,91,0,7
SPIWrite 04b7,48,0,7
SPIWrite 04b8,03,0,7
SPIWrite 04b9,78,0,7
SPIWrite 04ba,73,0,7
SPIWrite 04bb,b1,0,7
SPIWrite 04bc,93,0,7
SPIWrite 04bd,48,0,7
SPIWrite 04be,01,0,7
SPIWrite 04bf,78,0,7
SPIWrite 04c0,8f,0,7
SPIWrite 04c1,4b,0,7
SPIWrite 04c2,48,0,7
SPIWrite 04c3,02,0,7
SPIWrite 04c4,00,0,7
SPIWrite 04c5,eb,0,7
SPIWrite 04c6,81,0,7
SPIWrite 04c7,20,0,7
SPIWrite 04c8,00,0,7
SPIWrite 04c9,eb,0,7
SPIWrite 04ca,06,0,7
SPIWrite 04cb,10,0,7
SPIWrite 04cc,00,0,7
SPIWrite 04cd,eb,0,7
SPIWrite 04ce,c6,0,7
SPIWrite 04cf,00,0,7
SPIWrite 04d0,18,0,7
SPIWrite 04d1,5c,0,7
SPIWrite 04d2,01,0,7
SPIWrite 04d3,28,0,7
SPIWrite 04d4,08,0,7
SPIWrite 04d5,bf,0,7
SPIWrite 04d6,09,0,7
SPIWrite 04d7,98,0,7
SPIWrite 04d8,00,0,7
SPIWrite 04d9,d0,0,7
SPIWrite 04da,08,0,7
SPIWrite 04db,98,0,7
SPIWrite 04dc,a8,0,7
SPIWrite 04dd,f8,0,7
SPIWrite 04de,42,0,7
SPIWrite 04df,00,0,7
SPIWrite 04e0,da,0,7
SPIWrite 04e1,f8,0,7
SPIWrite 04e2,88,0,7
SPIWrite 04e3,21,0,7
SPIWrite 04e4,59,0,7
SPIWrite 04e5,46,0,7
SPIWrite 04e6,28,0,7
SPIWrite 04e7,46,0,7
SPIWrite 04e8,90,0,7
SPIWrite 04e9,47,0,7
SPIWrite 04ea,20,0,7
SPIWrite 04eb,73,0,7
SPIWrite 04ec,20,0,7
SPIWrite 04ed,7b,0,7
SPIWrite 04ee,01,0,7
SPIWrite 04ef,28,0,7
SPIWrite 04f0,13,0,7
SPIWrite 04f1,d1,0,7
SPIWrite 04f2,dd,0,7
SPIWrite 04f3,f8,0,7
SPIWrite 04f4,18,0,7
SPIWrite 04f5,90,0,7
SPIWrite 04f6,da,0,7
SPIWrite 04f7,f8,0,7
SPIWrite 04f8,7c,0,7
SPIWrite 04f9,31,0,7

SPIWrite 04fa,28,0,7
SPIWrite 04fb,46,0,7
SPIWrite 04fc,21,0,7
SPIWrite 04fd,46,0,7
SPIWrite 04fe,b9,0,7
SPIWrite 04ff,44,0,7
SPIWrite 0500,4a,0,7
SPIWrite 0501,46,0,7
SPIWrite 0502,98,0,7
SPIWrite 0503,47,0,7
SPIWrite 0504,db,0,7
SPIWrite 0505,f8,0,7
SPIWrite 0506,54,0,7
SPIWrite 0507,00,0,7
SPIWrite 0508,00,0,7
SPIWrite 0509,90,0,7
SPIWrite 050a,da,0,7
SPIWrite 050b,f8,0,7
SPIWrite 050c,98,0,7
SPIWrite 050d,c1,0,7
SPIWrite 050e,db,0,7
SPIWrite 050f,f8,0,7
SPIWrite 0510,50,0,7
SPIWrite 0511,30,0,7
SPIWrite 0512,21,0,7
SPIWrite 0513,46,0,7
SPIWrite 0514,4a,0,7
SPIWrite 0515,46,0,7
SPIWrite 0516,28,0,7
SPIWrite 0517,46,0,7
SPIWrite 0518,e0,0,7
SPIWrite 0519,47,0,7
SPIWrite 051a,76,0,7
SPIWrite 051b,1c,0,7
SPIWrite 051c,28,0,7
SPIWrite 051d,35,0,7
SPIWrite 051e,10,0,7
SPIWrite 051f,37,0,7
SPIWrite 0520,10,0,7
SPIWrite 0521,34,0,7
SPIWrite 0522,40,0,7
SPIWrite 0523,2e,0,7
SPIWrite 0524,c7,0,7
SPIWrite 0525,db,0,7
SPIWrite 0526,da,0,7
SPIWrite 0527,f8,0,7
SPIWrite 0528,c4,0,7
SPIWrite 0529,41,0,7
SPIWrite 052a,04,0,7
SPIWrite 052b,98,0,7
SPIWrite 052c,05,0,7
SPIWrite 052d,99,0,7
SPIWrite 052e,07,0,7
SPIWrite 052f,9a,0,7
SPIWrite 0530,5b,0,7
SPIWrite 0531,46,0,7
SPIWrite 0532,a0,0,7
SPIWrite 0533,47,0,7
SPIWrite 0534,73,0,7
SPIWrite 0535,48,0,7
SPIWrite 0536,00,0,7
SPIWrite 0537,78,0,7
SPIWrite 0538,88,0,7
SPIWrite 0539,b1,0,7
SPIWrite 053a,74,0,7
SPIWrite 053b,48,0,7
SPIWrite 053c,72,0,7
SPIWrite 053d,49,0,7
SPIWrite 053e,00,0,7

SPIWrite 053f,78,0,7
SPIWrite 0540,01,0,7
SPIWrite 0541,eb,0,7
SPIWrite 0542,80,0,7
SPIWrite 0543,21,0,7
SPIWrite 0544,05,0,7
SPIWrite 0545,98,0,7
SPIWrite 0546,ff,0,7
SPIWrite 0547,f7,0,7
SPIWrite 0548,fa,0,7
SPIWrite 0549,fe,0,7
SPIWrite 054a,08,0,7
SPIWrite 054b,e0,0,7
SPIWrite 054c,18,0,7
SPIWrite 054d,46,0,7
SPIWrite 054e,00,0,7
SPIWrite 054f,90,0,7
SPIWrite 0550,38,0,7
SPIWrite 0551,79,0,7
SPIWrite 0552,01,0,7
SPIWrite 0553,90,0,7
SPIWrite 0554,02,0,7
SPIWrite 0555,96,0,7
SPIWrite 0556,04,0,7
SPIWrite 0557,98,0,7
SPIWrite 0558,5b,0,7
SPIWrite 0559,46,0,7
SPIWrite 055a,ea,0,7
SPIWrite 055b,f7,0,7
SPIWrite 055c,97,0,7
SPIWrite 055d,fb,0,7
SPIWrite 055e,0b,0,7
SPIWrite 055f,b0,0,7
SPIWrite 0560,bd,0,7
SPIWrite 0561,e8,0,7
SPIWrite 0562,f0,0,7
SPIWrite 0563,8f,0,7
SPIWrite 0564,00,0,7
SPIWrite 0565,20,0,7
SPIWrite 0566,01,0,7
SPIWrite 0567,20,0,7
SPIWrite 0568,6c,0,7
SPIWrite 0569,0f,0,7
SPIWrite 056a,01,0,7
SPIWrite 056b,20,0,7
SPIWrite 056c,ac,0,7
SPIWrite 056d,13,0,7
SPIWrite 056e,01,0,7
SPIWrite 056f,20,0,7
SPIWrite 0570,2d,0,7
SPIWrite 0571,e9,0,7
SPIWrite 0572,fe,0,7
SPIWrite 0573,43,0,7
SPIWrite 0574,0a,0,7
SPIWrite 0575,af,0,7
SPIWrite 0576,3f,0,7
SPIWrite 0577,78,0,7
SPIWrite 0578,1d,0,7
SPIWrite 0579,46,0,7
SPIWrite 057a,91,0,7
SPIWrite 057b,46,0,7
SPIWrite 057c,0e,0,7
SPIWrite 057d,46,0,7
SPIWrite 057e,80,0,7
SPIWrite 057f,46,0,7
SPIWrite 0580,af,0,7
SPIWrite 0581,b9,0,7
SPIWrite 0582,5c,0,7
SPIWrite 0583,4c,0,7

SPIWrite 0584,20,0,7
SPIWrite 0585,78,0,7
SPIWrite 0586,00,0,7
SPIWrite 0587,28,0,7
SPIWrite 0588,08,0,7
SPIWrite 0589,bf,0,7
SPIWrite 058a,a0,0,7
SPIWrite 058b,88,0,7
SPIWrite 058c,0d,0,7
SPIWrite 058d,d0,0,7
SPIWrite 058e,00,0,7
SPIWrite 058f,21,0,7
SPIWrite 0590,40,0,7
SPIWrite 0591,46,0,7
SPIWrite 0592,32,0,7
SPIWrite 0593,46,0,7
SPIWrite 0594,01,0,7
SPIWrite 0595,ab,0,7
SPIWrite 0596,01,0,7
SPIWrite 0597,91,0,7
SPIWrite 0598,ff,0,7
SPIWrite 0599,f7,0,7
SPIWrite 059a,68,0,7
SPIWrite 059b,fd,0,7
SPIWrite 059c,e1,0,7
SPIWrite 059d,88,0,7
SPIWrite 059e,00,0,7
SPIWrite 059f,91,0,7
SPIWrite 05a0,62,0,7
SPIWrite 05a1,88,0,7
SPIWrite 05a2,a3,0,7
SPIWrite 05a3,88,0,7
SPIWrite 05a4,01,0,7
SPIWrite 05a5,99,0,7
SPIWrite 05a6,ff,0,7
SPIWrite 05a7,f7,0,7
SPIWrite 05a8,1b,0,7
SPIWrite 05a9,ff,0,7
SPIWrite 05aa,59,0,7
SPIWrite 05ab,49,0,7
SPIWrite 05ac,08,0,7
SPIWrite 05ad,80,0,7
SPIWrite 05ae,2b,0,7
SPIWrite 05af,46,0,7
SPIWrite 05b0,00,0,7
SPIWrite 05b1,97,0,7
SPIWrite 05b2,40,0,7
SPIWrite 05b3,46,0,7
SPIWrite 05b4,31,0,7
SPIWrite 05b5,46,0,7
SPIWrite 05b6,4a,0,7
SPIWrite 05b7,46,0,7
SPIWrite 05b8,ea,0,7
SPIWrite 05b9,f7,0,7
SPIWrite 05ba,42,0,7
SPIWrite 05bb,fd,0,7
SPIWrite 05bc,bd,0,7
SPIWrite 05bd,e8,0,7
SPIWrite 05be,fe,0,7
SPIWrite 05bf,83,0,7
SPIWrite 05c0,d4,0,7
SPIWrite 05c1,11,0,7
SPIWrite 05c2,01,0,7
SPIWrite 05c3,20,0,7
SPIWrite 05c4,38,0,7
SPIWrite 05c5,b5,0,7
SPIWrite 05c6,04,0,7
SPIWrite 05c7,9d,0,7
SPIWrite 05c8,04,0,7

SPIWrite 05c9,46,0,7
SPIWrite 05ca,00,0,7
SPIWrite 05cb,95,0,7
SPIWrite 05cc,ea,0,7
SPIWrite 05cd,f7,0,7
SPIWrite 05ce,a8,0,7
SPIWrite 05cf,fd,0,7
SPIWrite 05d0,50,0,7
SPIWrite 05d1,49,0,7
SPIWrite 05d2,a0,0,7
SPIWrite 05d3,68,0,7
SPIWrite 05d4,81,0,7
SPIWrite 05d5,42,0,7
SPIWrite 05d6,3c,0,7
SPIWrite 05d7,bf,0,7
SPIWrite 05d8,40,0,7
SPIWrite 05d9,20,0,7
SPIWrite 05da,20,0,7
SPIWrite 05db,73,0,7
SPIWrite 05dc,38,0,7
SPIWrite 05dd,bd,0,7
SPIWrite 05de,c0,0,7
SPIWrite 05df,46,0,7
SPIWrite 05e0,8e,0,7
SPIWrite 05e1,21,0,7
SPIWrite 05e2,01,0,7
SPIWrite 05e3,20,0,7
SPIWrite 05e4,e4,0,7
SPIWrite 05e5,10,0,7
SPIWrite 05e6,01,0,7
SPIWrite 05e7,20,0,7
SPIWrite 05e8,14,0,7
SPIWrite 05e9,d1,0,7
SPIWrite 05ea,01,0,7
SPIWrite 05eb,00,0,7
SPIWrite 05ec,2d,0,7
SPIWrite 05ed,e9,0,7
SPIWrite 05ee,f0,0,7
SPIWrite 05ef,47,0,7
SPIWrite 05f0,df,0,7
SPIWrite 05f1,f8,0,7
SPIWrite 05f2,24,0,7
SPIWrite 05f3,81,0,7
SPIWrite 05f4,8a,0,7
SPIWrite 05f5,46,0,7
SPIWrite 05f6,01,0,7
SPIWrite 05f7,46,0,7
SPIWrite 05f8,9e,0,7
SPIWrite 05f9,46,0,7
SPIWrite 05fa,44,0,7
SPIWrite 05fb,48,0,7
SPIWrite 05fc,98,0,7
SPIWrite 05fd,f8,0,7
SPIWrite 05fe,00,0,7
SPIWrite 05ff,60,0,7
SPIWrite 0600,03,0,7
SPIWrite 0601,78,0,7
SPIWrite 0602,08,0,7
SPIWrite 0603,af,0,7
SPIWrite 0604,91,0,7
SPIWrite 0605,46,0,7
SPIWrite 0606,ad,0,7
SPIWrite 0607,f1,0,7
SPIWrite 0608,58,0,7
SPIWrite 0609,0d,0,7
SPIWrite 060a,00,0,7
SPIWrite 060b,2e,0,7
SPIWrite 060c,4e,0,7
SPIWrite 060d,d0,0,7

SPIWrite 060e,43,0,7
SPIWrite 060f,48,0,7
SPIWrite 0610,1a,0,7
SPIWrite 0611,02,0,7
SPIWrite 0612,00,0,7
SPIWrite 0613,26,0,7
SPIWrite 0614,02,0,7
SPIWrite 0615,eb,0,7
SPIWrite 0616,43,0,7
SPIWrite 0617,22,0,7
SPIWrite 0618,80,0,7
SPIWrite 0619,18,0,7
SPIWrite 061a,40,0,7
SPIWrite 061b,22,0,7
SPIWrite 061c,0c,0,7
SPIWrite 061d,38,0,7
SPIWrite 061e,0c,0,7
SPIWrite 061f,30,0,7
SPIWrite 0620,04,0,7
SPIWrite 0621,7a,0,7
SPIWrite 0622,01,0,7
SPIWrite 0623,2c,0,7
SPIWrite 0624,06,0,7
SPIWrite 0625,d0,0,7
SPIWrite 0626,52,0,7
SPIWrite 0627,1e,0,7
SPIWrite 0628,f9,0,7
SPIWrite 0629,d1,0,7
SPIWrite 062a,00,0,7
SPIWrite 062b,2e,0,7
SPIWrite 062c,08,0,7
SPIWrite 062d,bf,0,7
SPIWrite 062e,d8,0,7
SPIWrite 062f,f8,0,7
SPIWrite 0630,04,0,7
SPIWrite 0631,20,0,7
SPIWrite 0632,01,0,7
SPIWrite 0633,d0,0,7
SPIWrite 0634,d8,0,7
SPIWrite 0635,f8,0,7
SPIWrite 0636,08,0,7
SPIWrite 0637,20,0,7
SPIWrite 0638,39,0,7
SPIWrite 0639,48,0,7
SPIWrite 063a,30,0,7
SPIWrite 063b,f9,0,7
SPIWrite 063c,13,0,7
SPIWrite 063d,00,0,7
SPIWrite 063e,b0,0,7
SPIWrite 063f,f1,0,7
SPIWrite 0640,ff,0,7
SPIWrite 0641,3f,0,7
SPIWrite 0642,08,0,7
SPIWrite 0643,bf,0,7
SPIWrite 0644,4f,0,7
SPIWrite 0645,f4,0,7
SPIWrite 0646,80,0,7
SPIWrite 0647,00,0,7
SPIWrite 0648,0c,0,7
SPIWrite 0649,d0,0,7
SPIWrite 064a,36,0,7
SPIWrite 064b,48,0,7
SPIWrite 064c,00,0,7
SPIWrite 064d,eb,0,7
SPIWrite 064e,03,0,7
SPIWrite 064f,10,0,7
SPIWrite 0650,44,0,7
SPIWrite 0651,68,0,7
SPIWrite 0652,00,0,7

SPIWrite 0653,68,0,7
SPIWrite 0654,24,0,7
SPIWrite 0655,03,0,7
SPIWrite 0656,00,0,7
SPIWrite 0657,03,0,7
SPIWrite 0658,54,0,7
SPIWrite 0659,fb,0,7
SPIWrite 065a,14,0,7
SPIWrite 065b,f3,0,7
SPIWrite 065c,50,0,7
SPIWrite 065d,fb,0,7
SPIWrite 065e,10,0,7
SPIWrite 065f,f0,0,7
SPIWrite 0660,83,0,7
SPIWrite 0661,fa,0,7
SPIWrite 0662,80,0,7
SPIWrite 0663,f0,0,7
SPIWrite 0664,a0,0,7
SPIWrite 0665,fb,0,7
SPIWrite 0666,02,0,7
SPIWrite 0667,24,0,7
SPIWrite 0668,6f,0,7
SPIWrite 0669,f0,0,7
SPIWrite 066a,00,0,7
SPIWrite 066b,40,0,7
SPIWrite 066c,a5,0,7
SPIWrite 066d,02,0,7
SPIWrite 066e,a3,0,7
SPIWrite 066f,0d,0,7
SPIWrite 0670,92,0,7
SPIWrite 0671,0d,0,7
SPIWrite 0672,c4,0,7
SPIWrite 0673,17,0,7
SPIWrite 0674,2a,0,7
SPIWrite 0675,43,0,7
SPIWrite 0676,a3,0,7
SPIWrite 0677,42,0,7
SPIWrite 0678,08,0,7
SPIWrite 0679,bf,0,7
SPIWrite 067a,82,0,7
SPIWrite 067b,42,0,7
SPIWrite 067c,98,0,7
SPIWrite 067d,bf,0,7
SPIWrite 067e,10,0,7
SPIWrite 067f,1c,0,7
SPIWrite 0680,0d,0,7
SPIWrite 0681,f1,0,7
SPIWrite 0682,17,0,7
SPIWrite 0683,0c,0,7
SPIWrite 0684,a9,0,7
SPIWrite 0685,f1,0,7
SPIWrite 0686,10,0,7
SPIWrite 0687,04,0,7
SPIWrite 0688,02,0,7
SPIWrite 0689,23,0,7
SPIWrite 068a,10,0,7
SPIWrite 068b,34,0,7
SPIWrite 068c,a2,0,7
SPIWrite 068d,68,0,7
SPIWrite 068e,25,0,7
SPIWrite 068f,7b,0,7
SPIWrite 0690,90,0,7
SPIWrite 0691,42,0,7
SPIWrite 0692,0c,0,7
SPIWrite 0693,f8,0,7
SPIWrite 0694,01,0,7
SPIWrite 0695,5f,0,7
SPIWrite 0696,06,0,7
SPIWrite 0697,d2,0,7

SPIWrite 0698,f2,0,7
SPIWrite 0699,00,0,7
SPIWrite 069a,02,0,7
SPIWrite 069b,eb,0,7
SPIWrite 069c,06,0,7
SPIWrite 069d,12,0,7
SPIWrite 069e,8a,0,7
SPIWrite 069f,18,0,7
SPIWrite 06a0,15,0,7
SPIWrite 06a1,7d,0,7
SPIWrite 06a2,05,0,7
SPIWrite 06a3,b9,0,7
SPIWrite 06a4,23,0,7
SPIWrite 06a5,73,0,7
SPIWrite 06a6,76,0,7
SPIWrite 06a7,1c,0,7
SPIWrite 06a8,40,0,7
SPIWrite 06a9,2e,0,7
SPIWrite 06aa,ee,0,7
SPIWrite 06ab,db,0,7
SPIWrite 06ac,38,0,7
SPIWrite 06ad,68,0,7
SPIWrite 06ae,00,0,7
SPIWrite 06af,90,0,7
SPIWrite 06b0,78,0,7
SPIWrite 06b1,68,0,7
SPIWrite 06b2,01,0,7
SPIWrite 06b3,90,0,7
SPIWrite 06b4,38,0,7
SPIWrite 06b5,89,0,7
SPIWrite 06b6,02,0,7
SPIWrite 06b7,90,0,7
SPIWrite 06b8,38,0,7
SPIWrite 06b9,7b,0,7
SPIWrite 06ba,03,0,7
SPIWrite 06bb,90,0,7
SPIWrite 06bc,38,0,7
SPIWrite 06bd,69,0,7
SPIWrite 06be,04,0,7
SPIWrite 06bf,90,0,7
SPIWrite 06c0,38,0,7
SPIWrite 06c1,7d,0,7
SPIWrite 06c2,73,0,7
SPIWrite 06c3,46,0,7
SPIWrite 06c4,4a,0,7
SPIWrite 06c5,46,0,7
SPIWrite 06c6,05,0,7
SPIWrite 06c7,90,0,7
SPIWrite 06c8,08,0,7
SPIWrite 06c9,46,0,7
SPIWrite 06ca,51,0,7
SPIWrite 06cb,46,0,7
SPIWrite 06cc,ea,0,7
SPIWrite 06cd,f7,0,7
SPIWrite 06ce,37,0,7
SPIWrite 06cf,ff,0,7
SPIWrite 06d0,98,0,7
SPIWrite 06d1,f8,0,7
SPIWrite 06d2,00,0,7
SPIWrite 06d3,00,0,7
SPIWrite 06d4,50,0,7
SPIWrite 06d5,b1,0,7
SPIWrite 06d6,49,0,7
SPIWrite 06d7,46,0,7
SPIWrite 06d8,40,0,7
SPIWrite 06d9,20,0,7
SPIWrite 06da,06,0,7
SPIWrite 06db,ac,0,7
SPIWrite 06dc,14,0,7

SPIWrite 06dd,f8,0,7
SPIWrite 06de,01,0,7
SPIWrite 06df,3b,0,7
SPIWrite 06e0,0a,0,7
SPIWrite 06e1,46,0,7
SPIWrite 06e2,40,0,7
SPIWrite 06e3,1e,0,7
SPIWrite 06e4,02,0,7
SPIWrite 06e5,f1,0,7
SPIWrite 06e6,10,0,7
SPIWrite 06e7,01,0,7
SPIWrite 06e8,13,0,7
SPIWrite 06e9,73,0,7
SPIWrite 06ea,f7,0,7
SPIWrite 06eb,d1,0,7
SPIWrite 06ec,16,0,7
SPIWrite 06ed,b0,0,7
SPIWrite 06ee,bd,0,7
SPIWrite 06ef,e8,0,7
SPIWrite 06f0,f0,0,7
SPIWrite 06f1,87,0,7
SPIWrite 06f2,c0,0,7
SPIWrite 06f3,46,0,7
SPIWrite 06f4,60,0,7
SPIWrite 06f5,21,0,7
SPIWrite 06f6,01,0,7
SPIWrite 06f7,20,0,7
SPIWrite 06f8,a0,0,7
SPIWrite 06f9,19,0,7
SPIWrite 06fa,01,0,7
SPIWrite 06fb,20,0,7
SPIWrite 06fc,6e,0,7
SPIWrite 06fd,21,0,7
SPIWrite 06fe,01,0,7
SPIWrite 06ff,20,0,7
SPIWrite 0700,14,0,7
SPIWrite 0701,50,0,7
SPIWrite 0702,00,0,7
SPIWrite 0703,20,0,7
SPIWrite 0704,88,0,7
SPIWrite 0705,21,0,7
SPIWrite 0706,01,0,7
SPIWrite 0707,20,0,7
SPIWrite 0708,70,0,7
SPIWrite 0709,7f,0,7
SPIWrite 070a,00,0,7
SPIWrite 070b,20,0,7
SPIWrite 070c,99,0,7
SPIWrite 070d,21,0,7
SPIWrite 070e,01,0,7
SPIWrite 070f,20,0,7
SPIWrite 0710,e0,0,7
SPIWrite 0711,19,0,7
SPIWrite 0712,01,0,7
SPIWrite 0713,20,0,7
SPIWrite 0714,fe,0,7
SPIWrite 0715,ff,0,7
SPIWrite 0716,ff,0,7
SPIWrite 0717,7f,0,7
SPIWrite 0718,70,0,7
SPIWrite 0719,21,0,7
SPIWrite 071a,01,0,7
SPIWrite 071b,20,0,7
SPIWrite 071c,90,0,7
SPIWrite 071d,d3,0,7
SPIWrite 071e,00,0,7
SPIWrite 071f,20,0,7
SPIWrite 0720,7c,0,7
SPIWrite 0721,21,0,7

SPIWrite 0722,01,0,7
SPIWrite 0723,20,0,7
SPIWrite 0724,00,0,7
SPIWrite 0725,21,0,7
SPIWrite 0726,01,0,7
SPIWrite 0727,20,0,7
SPIWrite 0728,2d,0,7
SPIWrite 0729,e9,0,7
SPIWrite 072a,f8,0,7
SPIWrite 072b,4f,0,7
SPIWrite 072c,66,0,7
SPIWrite 072d,4d,0,7
SPIWrite 072e,20,0,7
SPIWrite 072f,20,0,7
SPIWrite 0730,19,0,7
SPIWrite 0731,27,0,7
SPIWrite 0732,04,0,7
SPIWrite 0733,26,0,7
SPIWrite 0734,01,0,7
SPIWrite 0735,21,0,7
SPIWrite 0736,85,0,7
SPIWrite 0737,f8,0,7
SPIWrite 0738,3c,0,7
SPIWrite 0739,00,0,7
SPIWrite 073a,11,0,7
SPIWrite 073b,22,0,7
SPIWrite 073c,85,0,7
SPIWrite 073d,f8,0,7
SPIWrite 073e,3e,0,7
SPIWrite 073f,70,0,7
SPIWrite 0740,62,0,7
SPIWrite 0741,4b,0,7
SPIWrite 0742,85,0,7
SPIWrite 0743,f8,0,7
SPIWrite 0744,3d,0,7
SPIWrite 0745,60,0,7
SPIWrite 0746,df,0,7
SPIWrite 0747,f8,0,7
SPIWrite 0748,88,0,7
SPIWrite 0749,c1,0,7
SPIWrite 074a,85,0,7
SPIWrite 074b,f8,0,7
SPIWrite 074c,38,0,7
SPIWrite 074d,10,0,7
SPIWrite 074e,4f,0,7
SPIWrite 074f,f0,0,7
SPIWrite 0750,ff,0,7
SPIWrite 0751,34,0,7
SPIWrite 0752,6a,0,7
SPIWrite 0753,87,0,7
SPIWrite 0754,5f,0,7
SPIWrite 0755,48,0,7
SPIWrite 0756,1a,0,7
SPIWrite 0757,70,0,7
SPIWrite 0758,37,0,7
SPIWrite 0759,46,0,7
SPIWrite 075a,08,0,7
SPIWrite 075b,38,0,7
SPIWrite 075c,40,0,7
SPIWrite 075d,f8,0,7
SPIWrite 075e,08,0,7
SPIWrite 075f,4f,0,7
SPIWrite 0760,0c,0,7
SPIWrite 0761,f8,0,7
SPIWrite 0762,01,0,7
SPIWrite 0763,1b,0,7
SPIWrite 0764,7f,0,7
SPIWrite 0765,1e,0,7
SPIWrite 0766,44,0,7

SPIWrite 0767,60,0,7
SPIWrite 0768,f8,0,7
SPIWrite 0769,d1,0,7
SPIWrite 076a,5b,0,7
SPIWrite 076b,4f,0,7
SPIWrite 076c,a2,0,7
SPIWrite 076d,46,0,7
SPIWrite 076e,0d,0,7
SPIWrite 076f,23,0,7
SPIWrite 0770,39,0,7
SPIWrite 0771,70,0,7
SPIWrite 0772,5b,0,7
SPIWrite 0773,4d,0,7
SPIWrite 0774,79,0,7
SPIWrite 0775,70,0,7
SPIWrite 0776,82,0,7
SPIWrite 0777,24,0,7
SPIWrite 0778,39,0,7
SPIWrite 0779,71,0,7
SPIWrite 077a,4f,0,7
SPIWrite 077b,f0,0,7
SPIWrite 077c,14,0,7
SPIWrite 077d,08,0,7
SPIWrite 077e,7b,0,7
SPIWrite 077f,80,0,7
SPIWrite 0780,42,0,7
SPIWrite 0781,f6,0,7
SPIWrite 0782,86,0,7
SPIWrite 0783,52,0,7
SPIWrite 0784,29,0,7
SPIWrite 0785,70,0,7
SPIWrite 0786,58,0,7
SPIWrite 0787,48,0,7
SPIWrite 0788,6c,0,7
SPIWrite 0789,80,0,7
SPIWrite 078a,54,0,7
SPIWrite 078b,4f,0,7
SPIWrite 078c,85,0,7
SPIWrite 078d,f8,0,7
SPIWrite 078e,01,0,7
SPIWrite 078f,80,0,7
SPIWrite 0790,07,0,7
SPIWrite 0791,23,0,7
SPIWrite 0792,7a,0,7
SPIWrite 0793,80,0,7
SPIWrite 0794,a0,0,7
SPIWrite 0795,f1,0,7
SPIWrite 0796,02,0,7
SPIWrite 0797,0c,0,7
SPIWrite 0798,bb,0,7
SPIWrite 0799,80,0,7
SPIWrite 079a,df,0,7
SPIWrite 079b,f8,0,7
SPIWrite 079c,48,0,7
SPIWrite 079d,b1,0,7
SPIWrite 079e,7b,0,7
SPIWrite 079f,81,0,7
SPIWrite 07a0,b6,0,7
SPIWrite 07a1,46,0,7
SPIWrite 07a2,79,0,7
SPIWrite 07a3,70,0,7
SPIWrite 07a4,df,0,7
SPIWrite 07a5,f8,0,7
SPIWrite 07a6,44,0,7
SPIWrite 07a7,91,0,7
SPIWrite 07a8,fc,0,7
SPIWrite 07a9,80,0,7
SPIWrite 07aa,00,0,7
SPIWrite 07ab,20,0,7

SPIWrite 07ac,bc,0,7
SPIWrite 07ad,81,0,7
SPIWrite 07ae,40,0,7
SPIWrite 07af,f6,0,7
SPIWrite 07b0,65,0,7
SPIWrite 07b1,62,0,7
SPIWrite 07b2,38,0,7
SPIWrite 07b3,70,0,7
SPIWrite 07b4,03,0,7
SPIWrite 07b5,46,0,7
SPIWrite 07b6,3a,0,7
SPIWrite 07b7,81,0,7
SPIWrite 07b8,04,0,7
SPIWrite 07b9,46,0,7
SPIWrite 07ba,ab,0,7
SPIWrite 07bb,f1,0,7
SPIWrite 07bc,01,0,7
SPIWrite 07bd,02,0,7
SPIWrite 07be,02,0,7
SPIWrite 07bf,f8,0,7
SPIWrite 07c0,01,0,7
SPIWrite 07c1,0f,0,7
SPIWrite 07c2,2c,0,7
SPIWrite 07c3,f8,0,7
SPIWrite 07c4,02,0,7
SPIWrite 07c5,af,0,7
SPIWrite 07c6,82,0,7
SPIWrite 07c7,f8,0,7
SPIWrite 07c8,24,0,7
SPIWrite 07c9,00,0,7
SPIWrite 07ca,82,0,7
SPIWrite 07cb,f8,0,7
SPIWrite 07cc,4c,0,7
SPIWrite 07cd,00,0,7
SPIWrite 07ce,0b,0,7
SPIWrite 07cf,eb,0,7
SPIWrite 07d0,04,0,7
SPIWrite 07d1,05,0,7
SPIWrite 07d2,82,0,7
SPIWrite 07d3,f8,0,7
SPIWrite 07d4,48,0,7
SPIWrite 07d5,00,0,7
SPIWrite 07d6,02,0,7
SPIWrite 07d7,27,0,7
SPIWrite 07d8,45,0,7
SPIWrite 07d9,f8,0,7
SPIWrite 07da,04,0,7
SPIWrite 07db,0f,0,7
SPIWrite 07dc,7f,0,7
SPIWrite 07dd,1e,0,7
SPIWrite 07de,68,0,7
SPIWrite 07df,62,0,7
SPIWrite 07e0,fa,0,7
SPIWrite 07e1,d1,0,7
SPIWrite 07e2,09,0,7
SPIWrite 07e3,eb,0,7
SPIWrite 07e4,03,0,7
SPIWrite 07e5,05,0,7
SPIWrite 07e6,40,0,7
SPIWrite 07e7,27,0,7
SPIWrite 07e8,7f,0,7
SPIWrite 07e9,1e,0,7
SPIWrite 07ea,05,0,7
SPIWrite 07eb,f8,0,7
SPIWrite 07ec,01,0,7
SPIWrite 07ed,0b,0,7
SPIWrite 07ee,fb,0,7
SPIWrite 07ef,d1,0,7
SPIWrite 07f0,be,0,7

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SPIWrite 07f1,f1,0,7
SPIWrite 07f2,01,0,7
SPIWrite 07f3,0e,0,7
SPIWrite 07f4,04,0,7
SPIWrite 07f5,f1,0,7
SPIWrite 07f6,08,0,7
SPIWrite 07f7,04,0,7
SPIWrite 07f8,03,0,7
SPIWrite 07f9,f1,0,7
SPIWrite 07fa,40,0,7
SPIWrite 07fb,03,0,7
SPIWrite 07fc,df,0,7
SPIWrite 07fd,d1,0,7
SPIWrite 07fe,3e,0,7
SPIWrite 07ff,4b,0,7
SPIWrite 0800,3b,0,7
SPIWrite 0801,4c,0,7
SPIWrite 0802,1e,0,7
SPIWrite 0803,70,0,7
SPIWrite 0804,3b,0,7
SPIWrite 0805,4a,0,7
SPIWrite 0806,21,0,7
SPIWrite 0807,70,0,7
SPIWrite 0808,b5,0,7
SPIWrite 0809,27,0,7
SPIWrite 080a,11,0,7
SPIWrite 080b,70,0,7
SPIWrite 080c,3b,0,7
SPIWrite 080d,4b,0,7
SPIWrite 080e,57,0,7
SPIWrite 080f,70,0,7
SPIWrite 0810,76,0,7
SPIWrite 0811,1e,0,7
SPIWrite 0812,03,0,7
SPIWrite 0813,f8,0,7
SPIWrite 0814,01,0,7
SPIWrite 0815,0b,0,7
SPIWrite 0816,fb,0,7
SPIWrite 0817,d1,0,7
SPIWrite 0818,39,0,7
SPIWrite 0819,48,0,7
SPIWrite 081a,01,0,7
SPIWrite 081b,70,0,7
SPIWrite 081c,3e,0,7
SPIWrite 081d,4b,0,7
SPIWrite 081e,19,0,7
SPIWrite 081f,70,0,7
SPIWrite 0012,00,4,4 //PAGE: sys_calib_macro_memory=0x0;(Meaning: ); Address(0x12[4:4],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1;(Meaning: ); Address(0x13[4:4],)
SPIWrite 00a3,08,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7

SPIPoll 00f0,0,0,1

SPIWrite 0193,81,0,7

\\Read macro_opcode_operand=0x81000000; Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0;(Meaning: ); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)

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SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address(0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address(0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address(0x20[6:6],)

\\Read MACRO_ERROR_IN_EXECUTION=0x0; Address(0x20[7:7],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)
SPIWrite 0012,10,4,4 //PAGE: sys_calib_macro_memory=0x1; (Meaning:); Address(0x12[4:4],)
SPIWrite 0040,00,0,7
SPIWrite 0020,3a,0,7
SPIWrite 0021,4a,0,7
SPIWrite 0022,a2,0,7
SPIWrite 0023,f8,0,7
SPIWrite 0024,1c,0,7
SPIWrite 0025,80,0,7
SPIWrite 0026,37,0,7
SPIWrite 0027,4f,0,7
SPIWrite 0028,a2,0,7
SPIWrite 0029,f8,0,7
SPIWrite 002a,1a,0,7
SPIWrite 002b,80,0,7
SPIWrite 002c,36,0,7
SPIWrite 002d,49,0,7
SPIWrite 002e,47,0,7
SPIWrite 002f,60,0,7
SPIWrite 0030,38,0,7
SPIWrite 0031,4a,0,7
SPIWrite 0032,81,0,7
SPIWrite 0033,60,0,7
SPIWrite 0034,36,0,7
SPIWrite 0035,48,0,7
SPIWrite 0036,02,0,7
SPIWrite 0037,60,0,7
SPIWrite 0038,00,0,7
SPIWrite 0039,f0,0,7
SPIWrite 003a,60,0,7
SPIWrite 003b,fc,0,7
SPIWrite 003c,bd,0,7
SPIWrite 003d,e8,0,7
SPIWrite 003e,f8,0,7
SPIWrite 003f,8f,0,7
SPIWrite 0040,08,0,7
SPIWrite 0041,b5,0,7
SPIWrite 0042,ff,0,7
SPIWrite 0043,f7,0,7
SPIWrite 0044,71,0,7
SPIWrite 0045,ff,0,7
SPIWrite 0046,35,0,7
SPIWrite 0047,48,0,7
SPIWrite 0048,35,0,7
SPIWrite 0049,49,0,7
SPIWrite 004a,c0,0,7
SPIWrite 004b,f8,0,7
SPIWrite 004c,14,0,7
SPIWrite 004d,12,0,7
SPIWrite 004e,35,0,7

SPIWrite 004f,49,0,7
SPIWrite 0050,c0,0,7
SPIWrite 0051,f8,0,7
SPIWrite 0052,cc,0,7
SPIWrite 0053,12,0,7
SPIWrite 0054,34,0,7
SPIWrite 0055,49,0,7
SPIWrite 0056,c0,0,7
SPIWrite 0057,f8,0,7
SPIWrite 0058,58,0,7
SPIWrite 0059,11,0,7
SPIWrite 005a,34,0,7
SPIWrite 005b,49,0,7
SPIWrite 005c,c0,0,7
SPIWrite 005d,f8,0,7
SPIWrite 005e,68,0,7
SPIWrite 005f,13,0,7
SPIWrite 0060,33,0,7
SPIWrite 0061,49,0,7
SPIWrite 0062,c0,0,7
SPIWrite 0063,f8,0,7
SPIWrite 0064,20,0,7
SPIWrite 0065,11,0,7
SPIWrite 0066,33,0,7
SPIWrite 0067,49,0,7
SPIWrite 0068,c0,0,7
SPIWrite 0069,f8,0,7
SPIWrite 006a,c0,0,7
SPIWrite 006b,11,0,7
SPIWrite 006c,32,0,7
SPIWrite 006d,49,0,7
SPIWrite 006e,c0,0,7
SPIWrite 006f,f8,0,7
SPIWrite 0070,6c,0,7
SPIWrite 0071,11,0,7
SPIWrite 0072,32,0,7
SPIWrite 0073,49,0,7
SPIWrite 0074,c0,0,7
SPIWrite 0075,f8,0,7
SPIWrite 0076,70,0,7
SPIWrite 0077,11,0,7
SPIWrite 0078,31,0,7
SPIWrite 0079,49,0,7
SPIWrite 007a,c0,0,7
SPIWrite 007b,f8,0,7
SPIWrite 007c,0c,0,7
SPIWrite 007d,12,0,7
SPIWrite 007e,31,0,7
SPIWrite 007f,49,0,7
SPIWrite 0080,c0,0,7
SPIWrite 0081,f8,0,7
SPIWrite 0082,1c,0,7
SPIWrite 0083,12,0,7
SPIWrite 0084,30,0,7
SPIWrite 0085,49,0,7
SPIWrite 0086,c0,0,7
SPIWrite 0087,f8,0,7
SPIWrite 0088,2c,0,7
SPIWrite 0089,12,0,7
SPIWrite 008a,30,0,7
SPIWrite 008b,49,0,7
SPIWrite 008c,c0,0,7
SPIWrite 008d,f8,0,7
SPIWrite 008e,0c,0,7
SPIWrite 008f,13,0,7
SPIWrite 0090,2f,0,7
SPIWrite 0091,49,0,7
SPIWrite 0092,c0,0,7
SPIWrite 0093,f8,0,7

SPIWrite 0094,f8,0,7
SPIWrite 0095,11,0,7
SPIWrite 0096,2f,0,7
SPIWrite 0097,49,0,7
SPIWrite 0098,c0,0,7
SPIWrite 0099,f8,0,7
SPIWrite 009a,fc,0,7
SPIWrite 009b,11,0,7
SPIWrite 009c,2e,0,7
SPIWrite 009d,49,0,7
SPIWrite 009e,01,0,7
SPIWrite 009f,63,0,7
SPIWrite 00a0,2e,0,7
SPIWrite 00a1,49,0,7
SPIWrite 00a2,c0,0,7
SPIWrite 00a3,f8,0,7
SPIWrite 00a4,54,0,7
SPIWrite 00a5,11,0,7
SPIWrite 00a6,2e,0,7
SPIWrite 00a7,49,0,7
SPIWrite 00a8,c0,0,7
SPIWrite 00a9,f8,0,7
SPIWrite 00aa,80,0,7
SPIWrite 00ab,12,0,7
SPIWrite 00ac,2d,0,7
SPIWrite 00ad,49,0,7
SPIWrite 00ae,c0,0,7
SPIWrite 00af,f8,0,7
SPIWrite 00b0,ac,0,7
SPIWrite 00b1,11,0,7
SPIWrite 00b2,2d,0,7
SPIWrite 00b3,49,0,7
SPIWrite 00b4,c0,0,7
SPIWrite 00b5,f8,0,7
SPIWrite 00b6,b4,0,7
SPIWrite 00b7,11,0,7
SPIWrite 00b8,2c,0,7
SPIWrite 00b9,49,0,7
SPIWrite 00ba,c0,0,7
SPIWrite 00bb,f8,0,7
SPIWrite 00bc,48,0,7
SPIWrite 00bd,11,0,7
SPIWrite 00be,2c,0,7
SPIWrite 00bf,49,0,7
SPIWrite 00c0,c0,0,7
SPIWrite 00c1,f8,0,7
SPIWrite 00c2,d4,0,7
SPIWrite 00c3,10,0,7
SPIWrite 00c4,08,0,7
SPIWrite 00c5,bd,0,7
SPIWrite 00c6,c0,0,7
SPIWrite 00c7,46,0,7
SPIWrite 00c8,00,0,7
SPIWrite 00c9,7c,0,7
SPIWrite 00ca,01,0,7
SPIWrite 00cb,20,0,7
SPIWrite 00cc,97,0,7
SPIWrite 00cd,21,0,7
SPIWrite 00ce,01,0,7
SPIWrite 00cf,20,0,7
SPIWrite 00d0,8e,0,7
SPIWrite 00d1,21,0,7
SPIWrite 00d2,01,0,7
SPIWrite 00d3,20,0,7
SPIWrite 00d4,40,0,7
SPIWrite 00d5,21,0,7
SPIWrite 00d6,01,0,7
SPIWrite 00d7,20,0,7
SPIWrite 00d8,84,0,7

SPIWrite 00d9,21,0,7
SPIWrite 00da,01,0,7
SPIWrite 00db,20,0,7
SPIWrite 00dc,60,0,7
SPIWrite 00dd,21,0,7
SPIWrite 00de,01,0,7
SPIWrite 00df,20,0,7
SPIWrite 00e0,8a,0,7
SPIWrite 00e1,21,0,7
SPIWrite 00e2,01,0,7
SPIWrite 00e3,20,0,7
SPIWrite 00e4,00,0,7
SPIWrite 00e5,7e,0,7
SPIWrite 00e6,01,0,7
SPIWrite 00e7,20,0,7
SPIWrite 00e8,7c,0,7
SPIWrite 00e9,21,0,7
SPIWrite 00ea,01,0,7
SPIWrite 00eb,20,0,7
SPIWrite 00ec,00,0,7
SPIWrite 00ed,20,0,7
SPIWrite 00ee,01,0,7
SPIWrite 00ef,20,0,7
SPIWrite 00f0,9b,0,7
SPIWrite 00f1,21,0,7
SPIWrite 00f2,01,0,7
SPIWrite 00f3,20,0,7
SPIWrite 00f4,6e,0,7
SPIWrite 00f5,21,0,7
SPIWrite 00f6,01,0,7
SPIWrite 00f7,20,0,7
SPIWrite 00f8,98,0,7
SPIWrite 00f9,21,0,7
SPIWrite 00fa,01,0,7
SPIWrite 00fb,20,0,7
SPIWrite 00fc,92,0,7
SPIWrite 00fd,21,0,7
SPIWrite 00fe,01,0,7
SPIWrite 00ff,20,0,7
SPIWrite 0100,70,0,7
SPIWrite 0101,21,0,7
SPIWrite 0102,01,0,7
SPIWrite 0103,20,0,7
SPIWrite 0104,66,0,7
SPIWrite 0105,5e,0,7
SPIWrite 0106,08,0,7
SPIWrite 0107,00,0,7
SPIWrite 0108,25,0,7
SPIWrite 0109,1a,0,7
SPIWrite 010a,02,0,7
SPIWrite 010b,00,0,7
SPIWrite 010c,a0,0,7
SPIWrite 010d,19,0,7
SPIWrite 010e,01,0,7
SPIWrite 010f,20,0,7
SPIWrite 0110,80,0,7
SPIWrite 0111,1c,0,7
SPIWrite 0112,01,0,7
SPIWrite 0113,20,0,7
SPIWrite 0114,8c,0,7
SPIWrite 0115,53,0,7
SPIWrite 0116,01,0,7
SPIWrite 0117,00,0,7
SPIWrite 0118,96,0,7
SPIWrite 0119,21,0,7
SPIWrite 011a,01,0,7
SPIWrite 011b,20,0,7
SPIWrite 011c,ac,0,7
SPIWrite 011d,13,0,7

SPIWrite 011e,01,0,7
SPIWrite 011f,20,0,7
SPIWrite 0120,7d,0,7
SPIWrite 0121,cb,0,7
SPIWrite 0122,01,0,7
SPIWrite 0123,00,0,7
SPIWrite 0124,35,0,7
SPIWrite 0125,d0,0,7
SPIWrite 0126,01,0,7
SPIWrite 0127,00,0,7
SPIWrite 0128,c9,0,7
SPIWrite 0129,cc,0,7
SPIWrite 012a,01,0,7
SPIWrite 012b,00,0,7
SPIWrite 012c,a1,0,7
SPIWrite 012d,d0,0,7
SPIWrite 012e,01,0,7
SPIWrite 012f,00,0,7
SPIWrite 0130,85,0,7
SPIWrite 0131,cf,0,7
SPIWrite 0132,01,0,7
SPIWrite 0133,00,0,7
SPIWrite 0134,d7,0,7
SPIWrite 0135,c0,0,7
SPIWrite 0136,01,0,7
SPIWrite 0137,00,0,7
SPIWrite 0138,51,0,7
SPIWrite 0139,c5,0,7
SPIWrite 013a,01,0,7
SPIWrite 013b,00,0,7
SPIWrite 013c,29,0,7
SPIWrite 013d,c4,0,7
SPIWrite 013e,01,0,7
SPIWrite 013f,00,0,7
SPIWrite 0140,19,0,7
SPIWrite 0141,cc,0,7
SPIWrite 0142,01,0,7
SPIWrite 0143,00,0,7
SPIWrite 0144,f9,0,7
SPIWrite 0145,cb,0,7
SPIWrite 0146,01,0,7
SPIWrite 0147,00,0,7
SPIWrite 0148,6d,0,7
SPIWrite 0149,cb,0,7
SPIWrite 014a,01,0,7
SPIWrite 014b,00,0,7
SPIWrite 014c,69,0,7
SPIWrite 014d,ce,0,7
SPIWrite 014e,01,0,7
SPIWrite 014f,00,0,7
SPIWrite 0150,c7,0,7
SPIWrite 0151,c9,0,7
SPIWrite 0152,01,0,7
SPIWrite 0153,00,0,7
SPIWrite 0154,d9,0,7
SPIWrite 0155,ca,0,7
SPIWrite 0156,01,0,7
SPIWrite 0157,00,0,7
SPIWrite 0158,7d,0,7
SPIWrite 0159,cd,0,7
SPIWrite 015a,01,0,7
SPIWrite 015b,00,0,7
SPIWrite 015c,8d,0,7
SPIWrite 015d,cc,0,7
SPIWrite 015e,01,0,7
SPIWrite 015f,00,0,7
SPIWrite 0160,f9,0,7
SPIWrite 0161,cf,0,7
SPIWrite 0162,01,0,7

SPIWrite 0163,00,0,7
SPIWrite 0164,cd,0,7
SPIWrite 0165,c5,0,7
SPIWrite 0166,01,0,7
SPIWrite 0167,00,0,7
SPIWrite 0168,a5,0,7
SPIWrite 0169,c5,0,7
SPIWrite 016a,01,0,7
SPIWrite 016b,00,0,7
SPIWrite 016c,35,0,7
SPIWrite 016d,cf,0,7
SPIWrite 016e,01,0,7
SPIWrite 016f,00,0,7
SPIWrite 0170,65,0,7
SPIWrite 0171,d0,0,7
SPIWrite 0172,01,0,7
SPIWrite 0173,00,0,7
SPIWrite 0174,0c,0,7
SPIWrite 0175,23,0,7
SPIWrite 0176,00,0,7
SPIWrite 0177,22,0,7
SPIWrite 0178,11,0,7
SPIWrite 0179,fb,0,7
SPIWrite 017a,03,0,7
SPIWrite 017b,00,0,7
SPIWrite 017c,02,0,7
SPIWrite 017d,72,0,7
SPIWrite 017e,02,0,7
SPIWrite 017f,80,0,7
SPIWrite 0180,42,0,7
SPIWrite 0181,80,0,7
SPIWrite 0182,70,0,7
SPIWrite 0183,47,0,7
SPIWrite 0184,2d,0,7
SPIWrite 0185,e9,0,7
SPIWrite 0186,f8,0,7
SPIWrite 0187,4f,0,7
SPIWrite 0188,df,0,7
SPIWrite 0189,f8,0,7
SPIWrite 018a,dc,0,7
SPIWrite 018b,b1,0,7
SPIWrite 018c,df,0,7
SPIWrite 018d,f8,0,7
SPIWrite 018e,dc,0,7
SPIWrite 018f,a1,0,7
SPIWrite 0190,00,0,7
SPIWrite 0191,27,0,7
SPIWrite 0192,4f,0,7
SPIWrite 0193,f0,0,7
SPIWrite 0194,ff,0,7
SPIWrite 0195,38,0,7
SPIWrite 0196,91,0,7
SPIWrite 0197,46,0,7
SPIWrite 0198,0d,0,7
SPIWrite 0199,46,0,7
SPIWrite 019a,06,0,7
SPIWrite 019b,46,0,7
SPIWrite 019c,3c,0,7
SPIWrite 019d,46,0,7
SPIWrite 019e,9b,0,7
SPIWrite 019f,f8,0,7
SPIWrite 01a0,00,0,7
SPIWrite 01a1,00,0,7
SPIWrite 01a2,01,0,7
SPIWrite 01a3,28,0,7
SPIWrite 01a4,02,0,7
SPIWrite 01a5,d1,0,7
SPIWrite 01a6,28,0,7
SPIWrite 01a7,19,0,7

SPIWrite 01a8,40,0,7
SPIWrite 01a9,28,0,7
SPIWrite 01aa,16,0,7
SPIWrite 01ab,d1,0,7
SPIWrite 01ac,ac,0,7
SPIWrite 01ad,42,0,7
SPIWrite 01ae,14,0,7
SPIWrite 01af,d0,0,7
SPIWrite 01b0,20,0,7
SPIWrite 01b1,01,0,7
SPIWrite 01b2,31,0,7
SPIWrite 01b3,18,0,7
SPIWrite 01b4,0a,0,7
SPIWrite 01b5,7b,0,7
SPIWrite 01b6,01,0,7
SPIWrite 01b7,2a,0,7
SPIWrite 01b8,0f,0,7
SPIWrite 01b9,d1,0,7
SPIWrite 01ba,4b,0,7
SPIWrite 01bb,7b,0,7
SPIWrite 01bc,6b,0,7
SPIWrite 01bd,b9,0,7
SPIWrite 01be,da,0,7
SPIWrite 01bf,f8,0,7
SPIWrite 01c0,00,0,7
SPIWrite 01c1,20,0,7
SPIWrite 01c2,89,0,7
SPIWrite 01c3,68,0,7
SPIWrite 01c4,80,0,7
SPIWrite 01c5,19,0,7
SPIWrite 01c6,05,0,7
SPIWrite 01c7,23,0,7
SPIWrite 01c8,94,0,7
SPIWrite 01c9,46,0,7
SPIWrite 01ca,14,0,7
SPIWrite 01cb,22,0,7
SPIWrite 01cc,e0,0,7
SPIWrite 01cd,47,0,7
SPIWrite 01ce,80,0,7
SPIWrite 01cf,45,0,7
SPIWrite 01d0,03,0,7
SPIWrite 01d1,d9,0,7
SPIWrite 01d2,89,0,7
SPIWrite 01d3,f8,0,7
SPIWrite 01d4,00,0,7
SPIWrite 01d5,40,0,7
SPIWrite 01d6,80,0,7
SPIWrite 01d7,46,0,7
SPIWrite 01d8,01,0,7
SPIWrite 01d9,27,0,7
SPIWrite 01da,64,0,7
SPIWrite 01db,1c,0,7
SPIWrite 01dc,40,0,7
SPIWrite 01dd,2c,0,7
SPIWrite 01de,de,0,7
SPIWrite 01df,db,0,7
SPIWrite 01e0,38,0,7
SPIWrite 01e1,46,0,7
SPIWrite 01e2,bd,0,7
SPIWrite 01e3,e8,0,7
SPIWrite 01e4,f8,0,7
SPIWrite 01e5,8f,0,7
SPIWrite 01e6,2d,0,7
SPIWrite 01e7,e9,0,7
SPIWrite 01e8,f0,0,7
SPIWrite 01e9,4f,0,7
SPIWrite 01ea,61,0,7
SPIWrite 01eb,4c,0,7
SPIWrite 01ec,05,0,7

SPIWrite 01ed,46,0,7
SPIWrite 01ee,ad,0,7
SPIWrite 01ef,f1,0,7
SPIWrite 01f0,1c,0,7
SPIWrite 01f1,0d,0,7
SPIWrite 01f2,8a,0,7
SPIWrite 01f3,46,0,7
SPIWrite 01f4,05,0,7
SPIWrite 01f5,93,0,7
SPIWrite 01f6,20,0,7
SPIWrite 01f7,78,0,7
SPIWrite 01f8,04,0,7
SPIWrite 01f9,92,0,7
SPIWrite 01fa,01,0,7
SPIWrite 01fb,28,0,7
SPIWrite 01fc,79,0,7
SPIWrite 01fd,d1,0,7
SPIWrite 01fe,a3,0,7
SPIWrite 01ff,46,0,7
SPIWrite 0200,28,0,7
SPIWrite 0201,68,0,7
SPIWrite 0202,db,0,7
SPIWrite 0203,f8,0,7
SPIWrite 0204,04,0,7
SPIWrite 0205,70,0,7
SPIWrite 0206,40,0,7
SPIWrite 0207,1c,0,7
SPIWrite 0208,28,0,7
SPIWrite 0209,60,0,7
SPIWrite 020a,29,0,7
SPIWrite 020b,68,0,7
SPIWrite 020c,8f,0,7
SPIWrite 020d,42,0,7
SPIWrite 020e,5f,0,7
SPIWrite 020f,d8,0,7
SPIWrite 0210,00,0,7
SPIWrite 0211,24,0,7
SPIWrite 0212,2c,0,7
SPIWrite 0213,60,0,7
SPIWrite 0214,00,0,7
SPIWrite 0215,20,0,7
SPIWrite 0216,8d,0,7
SPIWrite 0217,f8,0,7
SPIWrite 0218,0c,0,7
SPIWrite 0219,00,0,7
SPIWrite 021a,21,0,7
SPIWrite 021b,46,0,7
SPIWrite 021c,03,0,7
SPIWrite 021d,aa,0,7
SPIWrite 021e,50,0,7
SPIWrite 021f,46,0,7
SPIWrite 0220,ff,0,7
SPIWrite 0221,f7,0,7
SPIWrite 0222,b0,0,7
SPIWrite 0223,ff,0,7
SPIWrite 0224,01,0,7
SPIWrite 0225,28,0,7
SPIWrite 0226,50,0,7
SPIWrite 0227,d1,0,7
SPIWrite 0228,0a,0,7
SPIWrite 0229,eb,0,7
SPIWrite 022a,04,0,7
SPIWrite 022b,17,0,7
SPIWrite 022c,38,0,7
SPIWrite 022d,7b,0,7
SPIWrite 022e,01,0,7
SPIWrite 022f,28,0,7
SPIWrite 0230,4b,0,7
SPIWrite 0231,d1,0,7

SPIWrite 0232,78,0,7
SPIWrite 0233,7b,0,7
SPIWrite 0234,c0,0,7
SPIWrite 0235,08,0,7
SPIWrite 0236,48,0,7
SPIWrite 0237,d2,0,7
SPIWrite 0238,4e,0,7
SPIWrite 0239,48,0,7
SPIWrite 023a,07,0,7
SPIWrite 023b,c8,0,7
SPIWrite 023c,a6,0,7
SPIWrite 023d,00,0,7
SPIWrite 023e,e8,0,7
SPIWrite 023f,46,0,7
SPIWrite 0240,06,0,7
SPIWrite 0241,eb,0,7
SPIWrite 0242,c4,0,7
SPIWrite 0243,09,0,7
SPIWrite 0244,09,0,7
SPIWrite 0245,eb,0,7
SPIWrite 0246,05,0,7
SPIWrite 0247,06,0,7
SPIWrite 0248,0c,0,7
SPIWrite 0249,36,0,7
SPIWrite 024a,88,0,7
SPIWrite 024b,e8,0,7
SPIWrite 024c,07,0,7
SPIWrite 024d,00,0,7
SPIWrite 024e,30,0,7
SPIWrite 024f,78,0,7
SPIWrite 0250,01,0,7
SPIWrite 0251,28,0,7
SPIWrite 0252,18,0,7
SPIWrite 0253,bf,0,7
SPIWrite 0254,00,0,7
SPIWrite 0255,20,0,7
SPIWrite 0256,0f,0,7
SPIWrite 0257,d1,0,7
SPIWrite 0258,48,0,7
SPIWrite 0259,48,0,7
SPIWrite 025a,47,0,7
SPIWrite 025b,4a,0,7
SPIWrite 025c,48,0,7
SPIWrite 025d,4b,0,7
SPIWrite 025e,00,0,7
SPIWrite 025f,78,0,7
SPIWrite 0260,04,0,7
SPIWrite 0261,eb,0,7
SPIWrite 0262,80,0,7
SPIWrite 0263,11,0,7
SPIWrite 0264,51,0,7
SPIWrite 0265,5c,0,7
SPIWrite 0266,03,0,7
SPIWrite 0267,f1,0,7
SPIWrite 0268,08,0,7
SPIWrite 0269,00,0,7
SPIWrite 026a,02,0,7
SPIWrite 026b,68,0,7
SPIWrite 026c,09,0,7
SPIWrite 026d,eb,0,7
SPIWrite 026e,05,0,7
SPIWrite 026f,03,0,7
SPIWrite 0270,0a,0,7
SPIWrite 0271,eb,0,7
SPIWrite 0272,01,0,7
SPIWrite 0273,11,0,7
SPIWrite 0274,18,0,7
SPIWrite 0275,1d,0,7
SPIWrite 0276,90,0,7

SPIWrite 0277,47,0,7
SPIWrite 0278,9b,0,7
SPIWrite 0279,f8,0,7
SPIWrite 027a,01,0,7
SPIWrite 027b,10,0,7
SPIWrite 027c,ba,0,7
SPIWrite 027d,68,0,7
SPIWrite 027e,a2,0,7
SPIWrite 027f,fb,0,7
SPIWrite 0280,01,0,7
SPIWrite 0281,21,0,7
SPIWrite 0282,cb,0,7
SPIWrite 0283,08,0,7
SPIWrite 0284,04,0,7
SPIWrite 0285,d1,0,7
SPIWrite 0286,d2,0,7
SPIWrite 0287,08,0,7
SPIWrite 0288,49,0,7
SPIWrite 0289,07,0,7
SPIWrite 028a,0a,0,7
SPIWrite 028b,43,0,7
SPIWrite 028c,90,0,7
SPIWrite 028d,42,0,7
SPIWrite 028e,01,0,7
SPIWrite 028f,d8,0,7
SPIWrite 0290,33,0,7
SPIWrite 0291,78,0,7
SPIWrite 0292,d3,0,7
SPIWrite 0293,b9,0,7
SPIWrite 0294,3a,0,7
SPIWrite 0295,48,0,7
SPIWrite 0296,9d,0,7
SPIWrite 0297,f8,0,7
SPIWrite 0298,0c,0,7
SPIWrite 0299,10,0,7
SPIWrite 029a,00,0,7
SPIWrite 029b,68,0,7
SPIWrite 029c,6a,0,7
SPIWrite 029d,46,0,7
SPIWrite 029e,0a,0,7
SPIWrite 029f,eb,0,7
SPIWrite 02a0,01,0,7
SPIWrite 02a1,11,0,7
SPIWrite 02a2,03,0,7
SPIWrite 02a3,46,0,7
SPIWrite 02a4,38,0,7
SPIWrite 02a5,46,0,7
SPIWrite 02a6,98,0,7
SPIWrite 02a7,47,0,7
SPIWrite 02a8,98,0,7
SPIWrite 02a9,e8,0,7
SPIWrite 02aa,0e,0,7
SPIWrite 02ab,00,0,7
SPIWrite 02ac,09,0,7
SPIWrite 02ad,eb,0,7
SPIWrite 02ae,05,0,7
SPIWrite 02af,00,0,7
SPIWrite 02b0,00,0,7
SPIWrite 02b1,1d,0,7
SPIWrite 02b2,80,0,7
SPIWrite 02b3,e8,0,7
SPIWrite 02b4,0e,0,7
SPIWrite 02b5,00,0,7
SPIWrite 02b6,01,0,7
SPIWrite 02b7,27,0,7
SPIWrite 02b8,37,0,7
SPIWrite 02b9,70,0,7
SPIWrite 02ba,30,0,7
SPIWrite 02bb,48,0,7

SPIWrite 02bc,2e,0,7
SPIWrite 02bd,4a,0,7
SPIWrite 02be,9d,0,7
SPIWrite 02bf,f8,0,7
SPIWrite 02c0,0c,0,7
SPIWrite 02c1,30,0,7
SPIWrite 02c2,01,0,7
SPIWrite 02c3,78,0,7
SPIWrite 02c4,04,0,7
SPIWrite 02c5,eb,0,7
SPIWrite 02c6,81,0,7
SPIWrite 02c7,11,0,7
SPIWrite 02c8,53,0,7
SPIWrite 02c9,54,0,7
SPIWrite 02ca,64,0,7
SPIWrite 02cb,1c,0,7
SPIWrite 02cc,40,0,7
SPIWrite 02cd,2c,0,7
SPIWrite 02ce,a1,0,7
SPIWrite 02cf,db,0,7
SPIWrite 02d0,08,0,7
SPIWrite 02d1,35,0,7
SPIWrite 02d2,40,0,7
SPIWrite 02d3,24,0,7
SPIWrite 02d4,28,0,7
SPIWrite 02d5,79,0,7
SPIWrite 02d6,01,0,7
SPIWrite 02d7,28,0,7
SPIWrite 02d8,07,0,7
SPIWrite 02d9,d1,0,7
SPIWrite 02da,28,0,7
SPIWrite 02db,68,0,7
SPIWrite 02dc,db,0,7
SPIWrite 02dd,f8,0,7
SPIWrite 02de,08,0,7
SPIWrite 02df,10,0,7
SPIWrite 02e0,04,0,7
SPIWrite 02e1,9a,0,7
SPIWrite 02e2,05,0,7
SPIWrite 02e3,9b,0,7
SPIWrite 02e4,ef,0,7
SPIWrite 02e5,f7,0,7
SPIWrite 02e6,7d,0,7
SPIWrite 02e7,fa,0,7
SPIWrite 02e8,28,0,7
SPIWrite 02e9,60,0,7
SPIWrite 02ea,64,0,7
SPIWrite 02eb,1e,0,7
SPIWrite 02ec,05,0,7
SPIWrite 02ed,f1,0,7
SPIWrite 02ee,0c,0,7
SPIWrite 02ef,05,0,7
SPIWrite 02f0,f0,0,7
SPIWrite 02f1,d1,0,7
SPIWrite 02f2,07,0,7
SPIWrite 02f3,b0,0,7
SPIWrite 02f4,bd,0,7
SPIWrite 02f5,e8,0,7
SPIWrite 02f6,f0,0,7
SPIWrite 02f7,8f,0,7
SPIWrite 02f8,f8,0,7
SPIWrite 02f9,b5,0,7
SPIWrite 02fa,0e,0,7
SPIWrite 02fb,46,0,7
SPIWrite 02fc,14,0,7
SPIWrite 02fd,46,0,7
SPIWrite 02fe,1f,0,7
SPIWrite 02ff,49,0,7
SPIWrite 0300,1d,0,7

SPIWrite 0301,4a,0,7
SPIWrite 0302,09,0,7
SPIWrite 0303,78,0,7
SPIWrite 0304,03,0,7
SPIWrite 0305,eb,0,7
SPIWrite 0306,81,0,7
SPIWrite 0307,11,0,7
SPIWrite 0308,51,0,7
SPIWrite 0309,5c,0,7
SPIWrite 030a,06,0,7
SPIWrite 030b,eb,0,7
SPIWrite 030c,01,0,7
SPIWrite 030d,12,0,7
SPIWrite 030e,12,0,7
SPIWrite 030f,7b,0,7
SPIWrite 0310,01,0,7
SPIWrite 0311,2a,0,7
SPIWrite 0312,26,0,7
SPIWrite 0313,d1,0,7
SPIWrite 0314,16,0,7
SPIWrite 0315,4a,0,7
SPIWrite 0316,12,0,7
SPIWrite 0317,78,0,7
SPIWrite 0318,01,0,7
SPIWrite 0319,2a,0,7
SPIWrite 031a,22,0,7
SPIWrite 031b,d1,0,7
SPIWrite 031c,0c,0,7
SPIWrite 031d,22,0,7
SPIWrite 031e,13,0,7
SPIWrite 031f,fb,0,7
SPIWrite 0320,02,0,7
SPIWrite 0321,f5,0,7
SPIWrite 0322,42,0,7
SPIWrite 0323,19,0,7
SPIWrite 0324,12,0,7
SPIWrite 0325,7b,0,7
SPIWrite 0326,01,0,7
SPIWrite 0327,2a,0,7
SPIWrite 0328,1b,0,7
SPIWrite 0329,d1,0,7
SPIWrite 032a,16,0,7
SPIWrite 032b,4f,0,7
SPIWrite 032c,2d,0,7
SPIWrite 032d,18,0,7
SPIWrite 032e,01,0,7
SPIWrite 032f,20,0,7
SPIWrite 0330,06,0,7
SPIWrite 0331,eb,0,7
SPIWrite 0332,01,0,7
SPIWrite 0333,16,0,7
SPIWrite 0334,d7,0,7
SPIWrite 0335,f8,0,7
SPIWrite 0336,dc,0,7
SPIWrite 0337,21,0,7
SPIWrite 0338,20,0,7
SPIWrite 0339,73,0,7
SPIWrite 033a,2d,0,7
SPIWrite 033b,1d,0,7
SPIWrite 033c,31,0,7
SPIWrite 033d,46,0,7
SPIWrite 033e,13,0,7
SPIWrite 033f,46,0,7
SPIWrite 0340,28,0,7
SPIWrite 0341,46,0,7
SPIWrite 0342,22,0,7
SPIWrite 0343,46,0,7
SPIWrite 0344,98,0,7
SPIWrite 0345,47,0,7

SPIWrite 0346,d7,0,7
SPIWrite 0347,f8,0,7
SPIWrite 0348,e0,0,7
SPIWrite 0349,21,0,7
SPIWrite 034a,28,0,7
SPIWrite 034b,46,0,7
SPIWrite 034c,31,0,7
SPIWrite 034d,46,0,7
SPIWrite 034e,90,0,7
SPIWrite 034f,47,0,7
SPIWrite 0350,0d,0,7
SPIWrite 0351,4a,0,7
SPIWrite 0352,d7,0,7
SPIWrite 0353,f8,0,7
SPIWrite 0354,10,0,7
SPIWrite 0355,32,0,7
SPIWrite 0356,d1,0,7
SPIWrite 0357,69,0,7
SPIWrite 0358,12,0,7
SPIWrite 0359,6a,0,7
SPIWrite 035a,a0,0,7
SPIWrite 035b,60,0,7
SPIWrite 035c,20,0,7
SPIWrite 035d,46,0,7
SPIWrite 035e,98,0,7
SPIWrite 035f,47,0,7
SPIWrite 0360,00,0,7
SPIWrite 0361,e0,0,7
SPIWrite 0362,00,0,7
SPIWrite 0363,20,0,7
SPIWrite 0364,20,0,7
SPIWrite 0365,73,0,7
SPIWrite 0366,f8,0,7
SPIWrite 0367,bd,0,7
SPIWrite 0368,9b,0,7
SPIWrite 0369,21,0,7
SPIWrite 036a,01,0,7
SPIWrite 036b,20,0,7
SPIWrite 036c,0c,0,7
SPIWrite 036d,16,0,7
SPIWrite 036e,01,0,7
SPIWrite 036f,20,0,7
SPIWrite 0370,dc,0,7
SPIWrite 0371,1c,0,7
SPIWrite 0372,01,0,7
SPIWrite 0373,20,0,7
SPIWrite 0374,08,0,7
SPIWrite 0375,d1,0,7
SPIWrite 0376,01,0,7
SPIWrite 0377,00,0,7
SPIWrite 0378,00,0,7
SPIWrite 0379,20,0,7
SPIWrite 037a,01,0,7
SPIWrite 037b,20,0,7
SPIWrite 037c,99,0,7
SPIWrite 037d,21,0,7
SPIWrite 037e,01,0,7
SPIWrite 037f,20,0,7
SPIWrite 0380,84,0,7
SPIWrite 0381,15,0,7
SPIWrite 0382,01,0,7
SPIWrite 0383,20,0,7
SPIWrite 0384,ac,0,7
SPIWrite 0385,13,0,7
SPIWrite 0386,01,0,7
SPIWrite 0387,20,0,7
SPIWrite 0388,8c,0,7
SPIWrite 0389,1b,0,7
SPIWrite 038a,01,0,7

SPIWrite 038b,20,0,7
SPIWrite 038c,10,0,7
SPIWrite 038d,b5,0,7
SPIWrite 038e,0c,0,7
SPIWrite 038f,46,0,7
SPIWrite 0390,ef,0,7
SPIWrite 0391,f7,0,7
SPIWrite 0392,e4,0,7
SPIWrite 0393,f9,0,7
SPIWrite 0394,3f,0,7
SPIWrite 0395,49,0,7
SPIWrite 0396,01,0,7
SPIWrite 0397,20,0,7
SPIWrite 0398,08,0,7
SPIWrite 0399,55,0,7
SPIWrite 039a,10,0,7
SPIWrite 039b,bd,0,7
SPIWrite 039c,38,0,7
SPIWrite 039d,b5,0,7
SPIWrite 039e,3e,0,7
SPIWrite 039f,4d,0,7
SPIWrite 03a0,04,0,7
SPIWrite 03a1,7b,0,7
SPIWrite 03a2,01,0,7
SPIWrite 03a3,2c,0,7
SPIWrite 03a4,2b,0,7
SPIWrite 03a5,70,0,7
SPIWrite 03a6,09,0,7
SPIWrite 03a7,d0,0,7
SPIWrite 03a8,0c,0,7
SPIWrite 03a9,7b,0,7
SPIWrite 03aa,01,0,7
SPIWrite 03ab,2c,0,7
SPIWrite 03ac,06,0,7
SPIWrite 03ad,d1,0,7
SPIWrite 03ae,3b,0,7
SPIWrite 03af,4c,0,7
SPIWrite 03b0,24,0,7
SPIWrite 03b1,78,0,7
SPIWrite 03b2,01,0,7
SPIWrite 03b3,2c,0,7
SPIWrite 03b4,02,0,7
SPIWrite 03b5,d1,0,7
SPIWrite 03b6,37,0,7
SPIWrite 03b7,4d,0,7
SPIWrite 03b8,00,0,7
SPIWrite 03b9,24,0,7
SPIWrite 03ba,ec,0,7
SPIWrite 03bb,54,0,7
SPIWrite 03bc,ef,0,7
SPIWrite 03bd,f7,0,7
SPIWrite 03be,39,0,7
SPIWrite 03bf,fa,0,7
SPIWrite 03c0,38,0,7
SPIWrite 03c1,bd,0,7
SPIWrite 03c2,10,0,7
SPIWrite 03c3,b5,0,7
SPIWrite 03c4,0c,0,7
SPIWrite 03c5,24,0,7
SPIWrite 03c6,13,0,7
SPIWrite 03c7,fb,0,7
SPIWrite 03c8,04,0,7
SPIWrite 03c9,f4,0,7
SPIWrite 03ca,00,0,7
SPIWrite 03cb,23,0,7
SPIWrite 03cc,09,0,7
SPIWrite 03cd,19,0,7
SPIWrite 03ce,0b,0,7
SPIWrite 03cf,60,0,7

SPIWrite 03d0,4b,0,7
SPIWrite 03d1,60,0,7
SPIWrite 03d2,00,0,7
SPIWrite 03d3,19,0,7
SPIWrite 03d4,03,0,7
SPIWrite 03d5,80,0,7
SPIWrite 03d6,43,0,7
SPIWrite 03d7,80,0,7
SPIWrite 03d8,42,0,7
SPIWrite 03d9,60,0,7
SPIWrite 03da,02,0,7
SPIWrite 03db,23,0,7
SPIWrite 03dc,03,0,7
SPIWrite 03dd,72,0,7
SPIWrite 03de,8a,0,7
SPIWrite 03df,60,0,7
SPIWrite 03e0,10,0,7
SPIWrite 03e1,bd,0,7
SPIWrite 03e2,38,0,7
SPIWrite 03e3,b5,0,7
SPIWrite 03e4,0c,0,7
SPIWrite 03e5,25,0,7
SPIWrite 03e6,00,0,7
SPIWrite 03e7,24,0,7
SPIWrite 03e8,12,0,7
SPIWrite 03e9,fb,0,7
SPIWrite 03ea,05,0,7
SPIWrite 03eb,11,0,7
SPIWrite 03ec,0c,0,7
SPIWrite 03ed,60,0,7
SPIWrite 03ee,4c,0,7
SPIWrite 03ef,60,0,7
SPIWrite 03f0,00,0,7
SPIWrite 03f1,eb,0,7
SPIWrite 03f2,02,0,7
SPIWrite 03f3,10,0,7
SPIWrite 03f4,83,0,7
SPIWrite 03f5,60,0,7
SPIWrite 03f6,04,0,7
SPIWrite 03f7,60,0,7
SPIWrite 03f8,44,0,7
SPIWrite 03f9,60,0,7
SPIWrite 03fa,02,0,7
SPIWrite 03fb,25,0,7
SPIWrite 03fc,05,0,7
SPIWrite 03fd,73,0,7
SPIWrite 03fe,44,0,7
SPIWrite 03ff,73,0,7
SPIWrite 0400,8b,0,7
SPIWrite 0401,60,0,7
SPIWrite 0402,04,0,7
SPIWrite 0403,99,0,7
SPIWrite 0404,50,0,7
SPIWrite 0405,11,0,7
SPIWrite 0406,83,0,7
SPIWrite 0407,00,0,7
SPIWrite 0408,cc,0,7
SPIWrite 0409,58,0,7
SPIWrite 040a,02,0,7
SPIWrite 040b,f0,0,7
SPIWrite 040c,1f,0,7
SPIWrite 040d,02,0,7
SPIWrite 040e,01,0,7
SPIWrite 040f,20,0,7
SPIWrite 0410,90,0,7
SPIWrite 0411,40,0,7
SPIWrite 0412,20,0,7
SPIWrite 0413,43,0,7
SPIWrite 0414,c8,0,7

SPIWrite 0415,50,0,7
SPIWrite 0416,38,0,7
SPIWrite 0417,bd,0,7
SPIWrite 0418,10,0,7
SPIWrite 0419,b5,0,7
SPIWrite 041a,04,0,7
SPIWrite 041b,46,0,7
SPIWrite 041c,ef,0,7
SPIWrite 041d,f7,0,7
SPIWrite 041e,34,0,7
SPIWrite 041f,fb,0,7
SPIWrite 0420,1f,0,7
SPIWrite 0421,49,0,7
SPIWrite 0422,20,0,7
SPIWrite 0423,48,0,7
SPIWrite 0424,64,0,7
SPIWrite 0425,1a,0,7
SPIWrite 0426,a1,0,7
SPIWrite 0427,0a,0,7
SPIWrite 0428,ca,0,7
SPIWrite 0429,b2,0,7
SPIWrite 042a,4f,0,7
SPIWrite 042b,f0,0,7
SPIWrite 042c,ff,0,7
SPIWrite 042d,31,0,7
SPIWrite 042e,d2,0,7
SPIWrite 042f,00,0,7
SPIWrite 0430,81,0,7
SPIWrite 0431,50,0,7
SPIWrite 0432,80,0,7
SPIWrite 0433,18,0,7
SPIWrite 0434,41,0,7
SPIWrite 0435,60,0,7
SPIWrite 0436,10,0,7
SPIWrite 0437,bd,0,7
SPIWrite 0438,f0,0,7
SPIWrite 0439,b5,0,7
SPIWrite 043a,05,0,7
SPIWrite 043b,af,0,7
SPIWrite 043c,3c,0,7
SPIWrite 043d,68,0,7
SPIWrite 043e,3e,0,7
SPIWrite 043f,7a,0,7
SPIWrite 0440,ad,0,7
SPIWrite 0441,f1,0,7
SPIWrite 0442,14,0,7
SPIWrite 0443,0d,0,7
SPIWrite 0444,00,0,7
SPIWrite 0445,94,0,7
SPIWrite 0446,3c,0,7
SPIWrite 0447,79,0,7
SPIWrite 0448,01,0,7
SPIWrite 0449,94,0,7
SPIWrite 044a,02,0,7
SPIWrite 044b,96,0,7
SPIWrite 044c,fd,0,7
SPIWrite 044d,68,0,7
SPIWrite 044e,04,0,7
SPIWrite 044f,46,0,7
SPIWrite 0450,03,0,7
SPIWrite 0451,95,0,7
SPIWrite 0452,ef,0,7
SPIWrite 0453,f7,0,7
SPIWrite 0454,3d,0,7
SPIWrite 0455,fc,0,7
SPIWrite 0456,de,0,7
SPIWrite 0457,b9,0,7
SPIWrite 0458,12,0,7
SPIWrite 0459,49,0,7

SPIWrite 045a,13,0,7
SPIWrite 045b,4a,0,7
SPIWrite 045c,10,0,7
SPIWrite 045d,3c,0,7
SPIWrite 045e,00,0,7
SPIWrite 045f,23,0,7
SPIWrite 0460,01,0,7
SPIWrite 0461,25,0,7
SPIWrite 0462,10,0,7
SPIWrite 0463,34,0,7
SPIWrite 0464,20,0,7
SPIWrite 0465,7b,0,7
SPIWrite 0466,01,0,7
SPIWrite 0467,28,0,7
SPIWrite 0468,0f,0,7
SPIWrite 0469,d1,0,7
SPIWrite 046a,60,0,7
SPIWrite 046b,7b,0,7
SPIWrite 046c,40,0,7
SPIWrite 046d,08,0,7
SPIWrite 046e,0c,0,7
SPIWrite 046f,d2,0,7
SPIWrite 0470,16,0,7
SPIWrite 0471,78,0,7
SPIWrite 0472,d8,0,7
SPIWrite 0473,10,0,7
SPIWrite 0474,80,0,7
SPIWrite 0475,08,0,7
SPIWrite 0476,00,0,7
SPIWrite 0477,eb,0,7
SPIWrite 0478,46,0,7
SPIWrite 0479,00,0,7
SPIWrite 047a,86,0,7
SPIWrite 047b,00,0,7
SPIWrite 047c,88,0,7
SPIWrite 047d,59,0,7
SPIWrite 047e,03,0,7
SPIWrite 047f,f0,0,7
SPIWrite 0480,1f,0,7
SPIWrite 0481,07,0,7
SPIWrite 0482,05,0,7
SPIWrite 0483,fa,0,7
SPIWrite 0484,07,0,7
SPIWrite 0485,f7,0,7
SPIWrite 0486,b8,0,7
SPIWrite 0487,43,0,7
SPIWrite 0488,88,0,7
SPIWrite 0489,51,0,7
SPIWrite 048a,5b,0,7
SPIWrite 048b,1c,0,7
SPIWrite 048c,40,0,7
SPIWrite 048d,2b,0,7
SPIWrite 048e,e8,0,7
SPIWrite 048f,db,0,7
SPIWrite 0490,05,0,7
SPIWrite 0491,b0,0,7
SPIWrite 0492,f0,0,7
SPIWrite 0493,bd,0,7
SPIWrite 0494,8e,0,7
SPIWrite 0495,21,0,7
SPIWrite 0496,01,0,7
SPIWrite 0497,20,0,7
SPIWrite 0498,9a,0,7
SPIWrite 0499,21,0,7
SPIWrite 049a,01,0,7
SPIWrite 049b,20,0,7
SPIWrite 049c,0c,0,7
SPIWrite 049d,1b,0,7
SPIWrite 049e,01,0,7

SPIWrite 049f,20,0,7
SPIWrite 04a0,70,0,7
SPIWrite 04a1,8f,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a3,20,0,7
SPIWrite 04a4,40,0,7
SPIWrite 04a5,21,0,7
SPIWrite 04a6,01,0,7
SPIWrite 04a7,20,0,7
SPIWrite 04a8,99,0,7
SPIWrite 04a9,21,0,7
SPIWrite 04aa,01,0,7
SPIWrite 04ab,20,0,7
SPIWrite 04ac,b0,0,7
SPIWrite 04ad,b5,0,7
SPIWrite 04ae,36,0,7
SPIWrite 04af,4d,0,7
SPIWrite 04b0,36,0,7
SPIWrite 04b1,4c,0,7
SPIWrite 04b2,2d,0,7
SPIWrite 04b3,78,0,7
SPIWrite 04b4,67,0,7
SPIWrite 04b5,5d,0,7
SPIWrite 04b6,ad,0,7
SPIWrite 04b7,f1,0,7
SPIWrite 04b8,18,0,7
SPIWrite 04b9,0d,0,7
SPIWrite 04ba,77,0,7
SPIWrite 04bb,b9,0,7
SPIWrite 04bc,0a,0,7
SPIWrite 04bd,9c,0,7
SPIWrite 04be,00,0,7
SPIWrite 04bf,94,0,7
SPIWrite 04c0,0b,0,7
SPIWrite 04c1,9f,0,7
SPIWrite 04c2,01,0,7
SPIWrite 04c3,97,0,7
SPIWrite 04c4,0c,0,7
SPIWrite 04c5,9d,0,7
SPIWrite 04c6,02,0,7
SPIWrite 04c7,95,0,7
SPIWrite 04c8,0d,0,7
SPIWrite 04c9,9f,0,7
SPIWrite 04ca,03,0,7
SPIWrite 04cb,97,0,7
SPIWrite 04cc,0e,0,7
SPIWrite 04cd,9d,0,7
SPIWrite 04ce,04,0,7
SPIWrite 04cf,95,0,7
SPIWrite 04d0,0f,0,7
SPIWrite 04d1,9f,0,7
SPIWrite 04d2,05,0,7
SPIWrite 04d3,97,0,7
SPIWrite 04d4,f2,0,7
SPIWrite 04d5,f7,0,7
SPIWrite 04d6,2c,0,7
SPIWrite 04d7,fe,0,7
SPIWrite 04d8,04,0,7
SPIWrite 04d9,e0,0,7
SPIWrite 04da,64,0,7
SPIWrite 04db,19,0,7
SPIWrite 04dc,21,0,7
SPIWrite 04dd,78,0,7
SPIWrite 04de,00,0,7
SPIWrite 04df,20,0,7
SPIWrite 04e0,49,0,7
SPIWrite 04e1,1e,0,7
SPIWrite 04e2,21,0,7
SPIWrite 04e3,70,0,7

SPIWrite 04e4,06,0,7
SPIWrite 04e5,b0,0,7
SPIWrite 04e6,b0,0,7
SPIWrite 04e7,bd,0,7
SPIWrite 04e8,2d,0,7
SPIWrite 04e9,e9,0,7
SPIWrite 04ea,fe,0,7
SPIWrite 04eb,4f,0,7
SPIWrite 04ec,83,0,7
SPIWrite 04ed,46,0,7
SPIWrite 04ee,8a,0,7
SPIWrite 04ef,46,0,7
SPIWrite 04f0,25,0,7
SPIWrite 04f1,48,0,7
SPIWrite 04f2,28,0,7
SPIWrite 04f3,4d,0,7
SPIWrite 04f4,01,0,7
SPIWrite 04f5,78,0,7
SPIWrite 04f6,26,0,7
SPIWrite 04f7,4c,0,7
SPIWrite 04f8,9e,0,7
SPIWrite 04f9,46,0,7
SPIWrite 04fa,0c,0,7
SPIWrite 04fb,af,0,7
SPIWrite 04fc,0b,0,7
SPIWrite 04fd,02,0,7
SPIWrite 04fe,0c,0,7
SPIWrite 04ff,20,0,7
SPIWrite 0500,03,0,7
SPIWrite 0501,eb,0,7
SPIWrite 0502,41,0,7
SPIWrite 0503,23,0,7
SPIWrite 0504,ed,0,7
SPIWrite 0505,18,0,7
SPIWrite 0506,ab,0,7
SPIWrite 0507,eb,0,7
SPIWrite 0508,05,0,7
SPIWrite 0509,03,0,7
SPIWrite 050a,b3,0,7
SPIWrite 050b,fb,0,7
SPIWrite 050c,f0,0,7
SPIWrite 050d,f0,0,7
SPIWrite 050e,22,0,7
SPIWrite 050f,4b,0,7
SPIWrite 0510,b7,0,7
SPIWrite 0511,f8,0,7
SPIWrite 0512,04,0,7
SPIWrite 0513,80,0,7
SPIWrite 0514,1d,0,7
SPIWrite 0515,78,0,7
SPIWrite 0516,c0,0,7
SPIWrite 0517,b2,0,7
SPIWrite 0518,00,0,7
SPIWrite 0519,eb,0,7
SPIWrite 051a,81,0,7
SPIWrite 051b,10,0,7
SPIWrite 051c,c1,0,7
SPIWrite 051d,00,0,7
SPIWrite 051e,01,0,7
SPIWrite 051f,eb,0,7
SPIWrite 0520,40,0,7
SPIWrite 0521,11,0,7
SPIWrite 0522,64,0,7
SPIWrite 0523,18,0,7
SPIWrite 0524,26,0,7
SPIWrite 0525,68,0,7
SPIWrite 0526,a1,0,7
SPIWrite 0527,8c,0,7
SPIWrite 0528,e0,0,7

SPIWrite 0529,8c,0,7
SPIWrite 052a,91,0,7
SPIWrite 052b,46,0,7
SPIWrite 052c,01,0,7
SPIWrite 052d,24,0,7
SPIWrite 052e,0b,0,7
SPIWrite 052f,30,0,7
SPIWrite 0530,76,0,7
SPIWrite 0531,00,0,7
SPIWrite 0532,04,0,7
SPIWrite 0533,fa,0,7
SPIWrite 0534,00,0,7
SPIWrite 0535,f0,0,7
SPIWrite 0536,b0,0,7
SPIWrite 0537,fb,0,7
SPIWrite 0538,f1,0,7
SPIWrite 0539,f0,0,7
SPIWrite 053a,ac,0,7
SPIWrite 053b,40,0,7
SPIWrite 053c,00,0,7
SPIWrite 053d,21,0,7
SPIWrite 053e,83,0,7
SPIWrite 053f,b2,0,7
SPIWrite 0540,db,0,7
SPIWrite 0541,04,0,7
SPIWrite 0542,08,0,7
SPIWrite 0543,46,0,7
SPIWrite 0544,45,0,7
SPIWrite 0545,46,0,7
SPIWrite 0546,e6,0,7
SPIWrite 0547,fb,0,7
SPIWrite 0548,63,0,7
SPIWrite 0549,10,0,7
SPIWrite 054a,00,0,7
SPIWrite 054b,1c,0,7
SPIWrite 054c,76,0,7
SPIWrite 054d,46,0,7
SPIWrite 054e,40,0,7
SPIWrite 054f,00,0,7
SPIWrite 0550,84,0,7
SPIWrite 0551,42,0,7
SPIWrite 0552,0c,0,7
SPIWrite 0553,d9,0,7
SPIWrite 0554,00,0,7
SPIWrite 0555,21,0,7
SPIWrite 0556,b4,0,7
SPIWrite 0557,fb,0,7
SPIWrite 0558,f0,0,7
SPIWrite 0559,f0,0,7
SPIWrite 055a,0a,0,7
SPIWrite 055b,46,0,7
SPIWrite 055c,f0,0,7
SPIWrite 055d,f7,0,7
SPIWrite 055e,e1,0,7
SPIWrite 055f,fb,0,7
SPIWrite 0560,4f,0,7
SPIWrite 0561,f6,0,7
SPIWrite 0562,ff,0,7
SPIWrite 0563,71,0,7
SPIWrite 0564,45,0,7
SPIWrite 0565,43,0,7
SPIWrite 0566,a9,0,7
SPIWrite 0567,42,0,7
SPIWrite 0568,2c,0,7
SPIWrite 0569,bf,0,7
SPIWrite 056a,ad,0,7
SPIWrite 056b,b2,0,7
SPIWrite 056c,0d,0,7
SPIWrite 056d,1c,0,7

SPIWrite 056e,38,0,7
SPIWrite 056f,88,0,7
SPIWrite 0570,00,0,7
SPIWrite 0571,90,0,7
SPIWrite 0572,01,0,7
SPIWrite 0573,95,0,7
SPIWrite 0574,b8,0,7
SPIWrite 0575,68,0,7
SPIWrite 0576,51,0,7
SPIWrite 0577,46,0,7
SPIWrite 0578,4a,0,7
SPIWrite 0579,46,0,7
SPIWrite 057a,33,0,7
SPIWrite 057b,46,0,7
SPIWrite 057c,02,0,7
SPIWrite 057d,90,0,7
SPIWrite 057e,58,0,7
SPIWrite 057f,46,0,7
SPIWrite 0580,f2,0,7
SPIWrite 0581,f7,0,7
SPIWrite 0582,4d,0,7
SPIWrite 0583,fe,0,7
SPIWrite 0584,bd,0,7
SPIWrite 0585,e8,0,7
SPIWrite 0586,fe,0,7
SPIWrite 0587,8f,0,7
SPIWrite 0588,99,0,7
SPIWrite 0589,21,0,7
SPIWrite 058a,01,0,7
SPIWrite 058b,20,0,7
SPIWrite 058c,92,0,7
SPIWrite 058d,21,0,7
SPIWrite 058e,01,0,7
SPIWrite 058f,20,0,7
SPIWrite 0590,00,0,7
SPIWrite 0591,28,0,7
SPIWrite 0592,00,0,7
SPIWrite 0593,20,0,7
SPIWrite 0594,90,0,7
SPIWrite 0595,c7,0,7
SPIWrite 0596,00,0,7
SPIWrite 0597,20,0,7
SPIWrite 0598,97,0,7
SPIWrite 0599,21,0,7
SPIWrite 059a,01,0,7
SPIWrite 059b,20,0,7
SPIWrite 059c,2d,0,7
SPIWrite 059d,e9,0,7
SPIWrite 059e,f0,0,7
SPIWrite 059f,4f,0,7
SPIWrite 05a0,86,0,7
SPIWrite 05a1,46,0,7
SPIWrite 05a2,35,0,7
SPIWrite 05a3,48,0,7
SPIWrite 05a4,09,0,7
SPIWrite 05a5,af,0,7
SPIWrite 05a6,04,0,7
SPIWrite 05a7,78,0,7
SPIWrite 05a8,d7,0,7
SPIWrite 05a9,f8,0,7
SPIWrite 05aa,0c,0,7
SPIWrite 05ab,c0,0,7
SPIWrite 05ac,3e,0,7
SPIWrite 05ad,7a,0,7
SPIWrite 05ae,d7,0,7
SPIWrite 05af,f8,0,7
SPIWrite 05b0,04,0,7
SPIWrite 05b1,a0,0,7
SPIWrite 05b2,3d,0,7

SPIWrite 05b3,78,0,7
SPIWrite 05b4,38,0,7
SPIWrite 05b5,7c,0,7
SPIWrite 05b6,ad,0,7
SPIWrite 05b7,f1,0,7
SPIWrite 05b8,2c,0,7
SPIWrite 05b9,0d,0,7
SPIWrite 05ba,01,0,7
SPIWrite 05bb,2c,0,7
SPIWrite 05bc,1f,0,7
SPIWrite 05bd,46,0,7
SPIWrite 05be,4d,0,7
SPIWrite 05bf,d0,0,7
SPIWrite 05c0,b2,0,7
SPIWrite 05c1,f9,0,7
SPIWrite 05c2,00,0,7
SPIWrite 05c3,90,0,7
SPIWrite 05c4,00,0,7
SPIWrite 05c5,95,0,7
SPIWrite 05c6,cd,0,7
SPIWrite 05c7,f8,0,7
SPIWrite 05c8,04,0,7
SPIWrite 05c9,a0,0,7
SPIWrite 05ca,00,0,7
SPIWrite 05cb,24,0,7
SPIWrite 05cc,02,0,7
SPIWrite 05cd,94,0,7
SPIWrite 05ce,cd,0,7
SPIWrite 05cf,f8,0,7
SPIWrite 05d0,0c,0,7
SPIWrite 05d1,c0,0,7
SPIWrite 05d2,04,0,7
SPIWrite 05d3,90,0,7
SPIWrite 05d4,70,0,7
SPIWrite 05d5,46,0,7
SPIWrite 05d6,e9,0,7
SPIWrite 05d7,f7,0,7
SPIWrite 05d8,19,0,7
SPIWrite 05d9,fb,0,7
SPIWrite 05da,00,0,7
SPIWrite 05db,2e,0,7
SPIWrite 05dc,48,0,7
SPIWrite 05dd,d0,0,7
SPIWrite 05de,01,0,7
SPIWrite 05df,2d,0,7
SPIWrite 05e0,0c,0,7
SPIWrite 05e1,bf,0,7
SPIWrite 05e2,4f,0,7
SPIWrite 05e3,f0,0,7
SPIWrite 05e4,02,0,7
SPIWrite 05e5,08,0,7
SPIWrite 05e6,4f,0,7
SPIWrite 05e7,f0,0,7
SPIWrite 05e8,01,0,7
SPIWrite 05e9,08,0,7
SPIWrite 05ea,a7,0,7
SPIWrite 05eb,eb,0,7
SPIWrite 05ec,09,0,7
SPIWrite 05ed,07,0,7
SPIWrite 05ee,ff,0,7
SPIWrite 05ef,10,0,7
SPIWrite 05f0,3e,0,7
SPIWrite 05f1,d0,0,7
SPIWrite 05f2,22,0,7
SPIWrite 05f3,4e,0,7
SPIWrite 05f4,df,0,7
SPIWrite 05f5,f8,0,7
SPIWrite 05f6,8c,0,7
SPIWrite 05f7,b0,0,7

SPIWrite 05f8,a1,0,7
SPIWrite 05f9,46,0,7
SPIWrite 05fa,6f,0,7
SPIWrite 05fb,f0,0,7
SPIWrite 05fc,1f,0,7
SPIWrite 05fd,04,0,7
SPIWrite 05fe,00,0,7
SPIWrite 05ff,2c,0,7
SPIWrite 0600,54,0,7
SPIWrite 0601,bf,0,7
SPIWrite 0602,e5,0,7
SPIWrite 0603,b2,0,7
SPIWrite 0604,04,0,7
SPIWrite 0605,f1,0,7
SPIWrite 0606,40,0,7
SPIWrite 0607,05,0,7
SPIWrite 0608,9b,0,7
SPIWrite 0609,f8,0,7
SPIWrite 060a,00,0,7
SPIWrite 060b,00,0,7
SPIWrite 060c,1c,0,7
SPIWrite 060d,49,0,7
SPIWrite 060e,05,0,7
SPIWrite 060f,eb,0,7
SPIWrite 0610,80,0,7
SPIWrite 0611,10,0,7
SPIWrite 0612,08,0,7
SPIWrite 0613,5c,0,7
SPIWrite 0614,20,0,7
SPIWrite 0615,28,0,7
SPIWrite 0616,a8,0,7
SPIWrite 0617,bf,0,7
SPIWrite 0618,40,0,7
SPIWrite 0619,38,0,7
SPIWrite 061a,cd,0,7
SPIWrite 061b,f8,0,7
SPIWrite 061c,20,0,7
SPIWrite 061d,90,0,7
SPIWrite 061e,72,0,7
SPIWrite 061f,6b,0,7
SPIWrite 0620,cd,0,7
SPIWrite 0621,f8,0,7
SPIWrite 0622,24,0,7
SPIWrite 0623,90,0,7
SPIWrite 0624,08,0,7
SPIWrite 0625,a9,0,7
SPIWrite 0626,20,0,7
SPIWrite 0627,1a,0,7
SPIWrite 0628,10,0,7
SPIWrite 0629,fb,0,7
SPIWrite 062a,07,0,7
SPIWrite 062b,f0,0,7
SPIWrite 062c,08,0,7
SPIWrite 062d,fb,0,7
SPIWrite 062e,00,0,7
SPIWrite 062f,f0,0,7
SPIWrite 0630,90,0,7
SPIWrite 0631,47,0,7
SPIWrite 0632,08,0,7
SPIWrite 0633,98,0,7
SPIWrite 0634,09,0,7
SPIWrite 0635,9a,0,7
SPIWrite 0636,06,0,7
SPIWrite 0637,a9,0,7
SPIWrite 0638,80,0,7
SPIWrite 0639,10,0,7
SPIWrite 063a,b3,0,7
SPIWrite 063b,6e,0,7
SPIWrite 063c,06,0,7

SPIWrite 063d,90,0,7
SPIWrite 063e,90,0,7
SPIWrite 063f,10,0,7
SPIWrite 0640,07,0,7
SPIWrite 0641,90,0,7
SPIWrite 0642,06,0,7
SPIWrite 0643,c9,0,7
SPIWrite 0644,a8,0,7
SPIWrite 0645,00,0,7
SPIWrite 0646,00,0,7
SPIWrite 0647,eb,0,7
SPIWrite 0648,c5,0,7
SPIWrite 0649,00,0,7
SPIWrite 064a,0a,0,7
SPIWrite 064b,eb,0,7
SPIWrite 064c,00,0,7
SPIWrite 064d,05,0,7
SPIWrite 064e,28,0,7
SPIWrite 064f,68,0,7
SPIWrite 0650,98,0,7
SPIWrite 0651,47,0,7
SPIWrite 0652,64,0,7
SPIWrite 0653,1c,0,7
SPIWrite 0654,28,0,7
SPIWrite 0655,60,0,7
SPIWrite 0656,20,0,7
SPIWrite 0657,2c,0,7
SPIWrite 0658,d1,0,7
SPIWrite 0659,db,0,7
SPIWrite 065a,09,0,7
SPIWrite 065b,e0,0,7
SPIWrite 065c,00,0,7
SPIWrite 065d,95,0,7
SPIWrite 065e,cd,0,7
SPIWrite 065f,f8,0,7
SPIWrite 0660,04,0,7
SPIWrite 0661,a0,0,7
SPIWrite 0662,02,0,7
SPIWrite 0663,96,0,7
SPIWrite 0664,cd,0,7
SPIWrite 0665,f8,0,7
SPIWrite 0666,0c,0,7
SPIWrite 0667,c0,0,7
SPIWrite 0668,04,0,7
SPIWrite 0669,90,0,7
SPIWrite 066a,70,0,7
SPIWrite 066b,46,0,7
SPIWrite 066c,e9,0,7
SPIWrite 066d,f7,0,7
SPIWrite 066e,ce,0,7
SPIWrite 066f,fa,0,7
SPIWrite 0670,0b,0,7
SPIWrite 0671,b0,0,7
SPIWrite 0672,bd,0,7
SPIWrite 0673,e8,0,7
SPIWrite 0674,f0,0,7
SPIWrite 0675,8f,0,7
SPIWrite 0676,c0,0,7
SPIWrite 0677,46,0,7
SPIWrite 0678,9b,0,7
SPIWrite 0679,21,0,7
SPIWrite 067a,01,0,7
SPIWrite 067b,20,0,7
SPIWrite 067c,ac,0,7
SPIWrite 067d,13,0,7
SPIWrite 067e,01,0,7
SPIWrite 067f,20,0,7
SPIWrite 0680,00,0,7
SPIWrite 0681,20,0,7

SPIWrite 0682,01,0,7
SPIWrite 0683,20,0,7
SPIWrite 0684,99,0,7
SPIWrite 0685,21,0,7
SPIWrite 0686,01,0,7
SPIWrite 0687,20,0,7
SPIWrite 0688,2d,0,7
SPIWrite 0689,e9,0,7
SPIWrite 068a,ff,0,7
SPIWrite 068b,41,0,7
SPIWrite 068c,0a,0,7
SPIWrite 068d,af,0,7
SPIWrite 068e,3d,0,7
SPIWrite 068f,78,0,7
SPIWrite 0690,3c,0,7
SPIWrite 0691,7a,0,7
SPIWrite 0692,00,0,7
SPIWrite 0693,95,0,7
SPIWrite 0694,3d,0,7
SPIWrite 0695,79,0,7
SPIWrite 0696,01,0,7
SPIWrite 0697,95,0,7
SPIWrite 0698,06,0,7
SPIWrite 0699,46,0,7
SPIWrite 069a,02,0,7
SPIWrite 069b,94,0,7
SPIWrite 069c,ef,0,7
SPIWrite 069d,f7,0,7
SPIWrite 069e,02,0,7
SPIWrite 069f,fe,0,7
SPIWrite 06a0,df,0,7
SPIWrite 06a1,f8,0,7
SPIWrite 06a2,a0,0,7
SPIWrite 06a3,80,0,7
SPIWrite 06a4,98,0,7
SPIWrite 06a5,f8,0,7
SPIWrite 06a6,00,0,7
SPIWrite 06a7,00,0,7
SPIWrite 06a8,00,0,7
SPIWrite 06a9,28,0,7
SPIWrite 06aa,48,0,7
SPIWrite 06ab,d0,0,7
SPIWrite 06ac,30,0,7
SPIWrite 06ad,7b,0,7
SPIWrite 06ae,01,0,7
SPIWrite 06af,28,0,7
SPIWrite 06b0,45,0,7
SPIWrite 06b1,d1,0,7
SPIWrite 06b2,25,0,7
SPIWrite 06b3,49,0,7
SPIWrite 06b4,2c,0,7
SPIWrite 06b5,20,0,7
SPIWrite 06b6,14,0,7
SPIWrite 06b7,fb,0,7
SPIWrite 06b8,00,0,7
SPIWrite 06b9,15,0,7
SPIWrite 06ba,ab,0,7
SPIWrite 06bb,69,0,7
SPIWrite 06bc,ea,0,7
SPIWrite 06bd,69,0,7
SPIWrite 06be,28,0,7
SPIWrite 06bf,69,0,7
SPIWrite 06c0,69,0,7
SPIWrite 06c1,69,0,7
SPIWrite 06c2,80,0,7
SPIWrite 06c3,fa,0,7
SPIWrite 06c4,a3,0,7
SPIWrite 06c5,f0,0,7
SPIWrite 06c6,81,0,7

SPIWrite 06c7,fa,0,7
SPIWrite 06c8,a2,0,7
SPIWrite 06c9,f1,0,7
SPIWrite 06ca,f0,0,7
SPIWrite 06cb,f7,0,7
SPIWrite 06cc,e6,0,7
SPIWrite 06cd,f9,0,7
SPIWrite 06ce,07,0,7
SPIWrite 06cf,46,0,7
SPIWrite 06d0,b5,0,7
SPIWrite 06d1,f9,0,7
SPIWrite 06d2,24,0,7
SPIWrite 06d3,20,0,7
SPIWrite 06d4,b5,0,7
SPIWrite 06d5,f9,0,7
SPIWrite 06d6,20,0,7
SPIWrite 06d7,30,0,7
SPIWrite 06d8,b5,0,7
SPIWrite 06d9,f9,0,7
SPIWrite 06da,22,0,7
SPIWrite 06db,10,0,7
SPIWrite 06dc,b5,0,7
SPIWrite 06dd,f9,0,7
SPIWrite 06de,26,0,7
SPIWrite 06df,00,0,7
SPIWrite 06e0,d3,0,7
SPIWrite 06e1,fa,0,7
SPIWrite 06e2,12,0,7
SPIWrite 06e3,f2,0,7
SPIWrite 06e4,d1,0,7
SPIWrite 06e5,fa,0,7
SPIWrite 06e6,10,0,7
SPIWrite 06e7,f0,0,7
SPIWrite 06e8,01,0,7
SPIWrite 06e9,b2,0,7
SPIWrite 06ea,10,0,7
SPIWrite 06eb,b2,0,7
SPIWrite 06ec,f0,0,7
SPIWrite 06ed,f7,0,7
SPIWrite 06ee,d5,0,7
SPIWrite 06ef,f9,0,7
SPIWrite 06f0,07,0,7
SPIWrite 06f1,b9,0,7
SPIWrite 06f2,01,0,7
SPIWrite 06f3,27,0,7
SPIWrite 06f4,41,0,7
SPIWrite 06f5,46,0,7
SPIWrite 06f6,4a,0,7
SPIWrite 06f7,78,0,7
SPIWrite 06f8,c0,0,7
SPIWrite 06f9,03,0,7
SPIWrite 06fa,b0,0,7
SPIWrite 06fb,fb,0,7
SPIWrite 06fc,f7,0,7
SPIWrite 06fd,f0,0,7
SPIWrite 06fe,82,0,7
SPIWrite 06ff,42,0,7
SPIWrite 0700,1d,0,7
SPIWrite 0701,d3,0,7
SPIWrite 0702,50,0,7
SPIWrite 0703,04,0,7
SPIWrite 0704,20,0,7
SPIWrite 0705,23,0,7
SPIWrite 0706,57,0,7
SPIWrite 0707,fb,0,7
SPIWrite 0708,10,0,7
SPIWrite 0709,f2,0,7
SPIWrite 070a,48,0,7
SPIWrite 070b,88,0,7

SPIWrite 070c,33,0,7
SPIWrite 070d,73,0,7
SPIWrite 070e,00,0,7
SPIWrite 070f,2a,0,7
SPIWrite 0710,02,0,7
SPIWrite 0711,d4,0,7
SPIWrite 0712,90,0,7
SPIWrite 0713,42,0,7
SPIWrite 0714,04,0,7
SPIWrite 0715,dc,0,7
SPIWrite 0716,02,0,7
SPIWrite 0717,e0,0,7
SPIWrite 0718,40,0,7
SPIWrite 0719,42,0,7
SPIWrite 071a,90,0,7
SPIWrite 071b,42,0,7
SPIWrite 071c,00,0,7
SPIWrite 071d,db,0,7
SPIWrite 071e,02,0,7
SPIWrite 071f,46,0,7
SPIWrite 0720,0a,0,7
SPIWrite 0721,4d,0,7
SPIWrite 0722,0b,0,7
SPIWrite 0723,48,0,7
SPIWrite 0724,30,0,7
SPIWrite 0725,23,0,7
SPIWrite 0726,11,0,7
SPIWrite 0727,b2,0,7
SPIWrite 0728,00,0,7
SPIWrite 0729,27,0,7
SPIWrite 072a,14,0,7
SPIWrite 072b,fb,0,7
SPIWrite 072c,03,0,7
SPIWrite 072d,52,0,7
SPIWrite 072e,00,0,7
SPIWrite 072f,19,0,7
SPIWrite 0730,11,0,7
SPIWrite 0731,81,0,7
SPIWrite 0732,90,0,7
SPIWrite 0733,f8,0,7
SPIWrite 0734,4c,0,7
SPIWrite 0735,10,0,7
SPIWrite 0736,57,0,7
SPIWrite 0737,81,0,7
SPIWrite 0738,49,0,7
SPIWrite 0739,1c,0,7
SPIWrite 073a,80,0,7
SPIWrite 073b,f8,0,7
SPIWrite 073c,4c,0,7
SPIWrite 073d,10,0,7
SPIWrite 073e,00,0,7
SPIWrite 073f,90,0,7
SPIWrite 0740,bd,0,7
SPIWrite 0741,e8,0,7
SPIWrite 0742,ff,0,7
SPIWrite 0743,81,0,7
SPIWrite 0744,8a,0,7
SPIWrite 0745,21,0,7
SPIWrite 0746,01,0,7
SPIWrite 0747,20,0,7
SPIWrite 0748,94,0,7
SPIWrite 0749,0b,0,7
SPIWrite 074a,01,0,7
SPIWrite 074b,20,0,7
SPIWrite 074c,14,0,7
SPIWrite 074d,0a,0,7
SPIWrite 074e,01,0,7
SPIWrite 074f,20,0,7
SPIWrite 0750,00,0,7

SPIWrite 0751,7e,0,7
SPIWrite 0752,01,0,7
SPIWrite 0753,20,0,7
SPIWrite 0754,90,0,7
SPIWrite 0755,b5,0,7
SPIWrite 0756,2d,0,7
SPIWrite 0757,4c,0,7
SPIWrite 0758,03,0,7
SPIWrite 0759,af,0,7
SPIWrite 075a,23,0,7
SPIWrite 075b,70,0,7
SPIWrite 075c,3c,0,7
SPIWrite 075d,78,0,7
SPIWrite 075e,ad,0,7
SPIWrite 075f,f1,0,7
SPIWrite 0760,1c,0,7
SPIWrite 0761,0d,0,7
SPIWrite 0762,00,0,7
SPIWrite 0763,94,0,7
SPIWrite 0764,3c,0,7
SPIWrite 0765,79,0,7
SPIWrite 0766,01,0,7
SPIWrite 0767,94,0,7
SPIWrite 0768,bc,0,7
SPIWrite 0769,68,0,7
SPIWrite 076a,02,0,7
SPIWrite 076b,94,0,7
SPIWrite 076c,3c,0,7
SPIWrite 076d,7b,0,7
SPIWrite 076e,03,0,7
SPIWrite 076f,94,0,7
SPIWrite 0770,3c,0,7
SPIWrite 0771,7c,0,7
SPIWrite 0772,04,0,7
SPIWrite 0773,94,0,7
SPIWrite 0774,7c,0,7
SPIWrite 0775,69,0,7
SPIWrite 0776,05,0,7
SPIWrite 0777,94,0,7
SPIWrite 0778,bc,0,7
SPIWrite 0779,69,0,7
SPIWrite 077a,06,0,7
SPIWrite 077b,94,0,7
SPIWrite 077c,ea,0,7
SPIWrite 077d,f7,0,7
SPIWrite 077e,1a,0,7
SPIWrite 077f,fd,0,7
SPIWrite 0780,07,0,7
SPIWrite 0781,b0,0,7
SPIWrite 0782,90,0,7
SPIWrite 0783,bd,0,7
SPIWrite 0784,00,0,7
SPIWrite 0785,28,0,7
SPIWrite 0786,10,0,7
SPIWrite 0787,b5,0,7
SPIWrite 0788,03,0,7
SPIWrite 0789,d5,0,7
SPIWrite 078a,21,0,7
SPIWrite 078b,4a,0,7
SPIWrite 078c,82,0,7
SPIWrite 078d,42,0,7
SPIWrite 078e,08,0,7
SPIWrite 078f,dd,0,7
SPIWrite 0790,03,0,7
SPIWrite 0791,e0,0,7
SPIWrite 0792,41,0,7
SPIWrite 0793,f6,0,7
SPIWrite 0794,f8,0,7
SPIWrite 0795,72,0,7

SPIWrite 0796,82,0,7
SPIWrite 0797,42,0,7
SPIWrite 0798,03,0,7
SPIWrite 0799,da,0,7
SPIWrite 079a,1e,0,7
SPIWrite 079b,4c,0,7
SPIWrite 079c,01,0,7
SPIWrite 079d,23,0,7
SPIWrite 079e,10,0,7
SPIWrite 079f,46,0,7
SPIWrite 07a0,63,0,7
SPIWrite 07a1,54,0,7
SPIWrite 07a2,10,0,7
SPIWrite 07a3,bd,0,7
SPIWrite 07a4,2d,0,7
SPIWrite 07a5,e9,0,7
SPIWrite 07a6,fe,0,7
SPIWrite 07a7,4f,0,7
SPIWrite 07a8,0c,0,7
SPIWrite 07a9,af,0,7
SPIWrite 07aa,3d,0,7
SPIWrite 07ab,79,0,7
SPIWrite 07ac,d7,0,7
SPIWrite 07ad,f8,0,7
SPIWrite 07ae,00,0,7
SPIWrite 07af,80,0,7
SPIWrite 07b0,9c,0,7
SPIWrite 07b1,46,0,7
SPIWrite 07b2,00,0,7
SPIWrite 07b3,24,0,7
SPIWrite 07b4,92,0,7
SPIWrite 07b5,46,0,7
SPIWrite 07b6,8b,0,7
SPIWrite 07b7,46,0,7
SPIWrite 07b8,81,0,7
SPIWrite 07b9,46,0,7
SPIWrite 07ba,0f,0,7
SPIWrite 07bb,e0,0,7
SPIWrite 07bc,09,0,7
SPIWrite 07bd,eb,0,7
SPIWrite 07be,84,0,7
SPIWrite 07bf,06,0,7
SPIWrite 07c0,b6,0,7
SPIWrite 07c1,f9,0,7
SPIWrite 07c2,00,0,7
SPIWrite 07c3,00,0,7
SPIWrite 07c4,29,0,7
SPIWrite 07c5,46,0,7
SPIWrite 07c6,ff,0,7
SPIWrite 07c7,f7,0,7
SPIWrite 07c8,dd,0,7
SPIWrite 07c9,ff,0,7
SPIWrite 07ca,b6,0,7
SPIWrite 07cb,f9,0,7
SPIWrite 07cc,02,0,7
SPIWrite 07cd,20,0,7
SPIWrite 07ce,30,0,7
SPIWrite 07cf,80,0,7
SPIWrite 07d0,10,0,7
SPIWrite 07d1,46,0,7
SPIWrite 07d2,ff,0,7
SPIWrite 07d3,f7,0,7
SPIWrite 07d4,d7,0,7
SPIWrite 07d5,ff,0,7
SPIWrite 07d6,64,0,7
SPIWrite 07d7,1c,0,7
SPIWrite 07d8,e4,0,7
SPIWrite 07d9,b2,0,7
SPIWrite 07da,70,0,7

SPIWrite 07db,80,0,7
SPIWrite 07dc,98,0,7
SPIWrite 07dd,f8,0,7
SPIWrite 07de,00,0,7
SPIWrite 07df,00,0,7
SPIWrite 07e0,a0,0,7
SPIWrite 07e1,42,0,7
SPIWrite 07e2,eb,0,7
SPIWrite 07e3,dc,0,7
SPIWrite 07e4,cd,0,7
SPIWrite 07e5,f8,0,7
SPIWrite 07e6,00,0,7
SPIWrite 07e7,80,0,7
SPIWrite 07e8,01,0,7
SPIWrite 07e9,95,0,7
SPIWrite 07ea,38,0,7
SPIWrite 07eb,7a,0,7
SPIWrite 07ec,4f,0,7
SPIWrite 07ed,f6,0,7
SPIWrite 07ee,f8,0,7
SPIWrite 07ef,73,0,7
SPIWrite 07f0,0a,0,7
SPIWrite 07f1,f1,0,7
SPIWrite 07f2,07,0,7
SPIWrite 07f3,02,0,7
SPIWrite 07f4,0c,0,7
SPIWrite 07f5,f1,0,7
SPIWrite 07f6,07,0,7
SPIWrite 07f7,01,0,7
SPIWrite 07f8,1a,0,7
SPIWrite 07f9,40,0,7
SPIWrite 07fa,0b,0,7
SPIWrite 07fb,40,0,7
SPIWrite 07fc,02,0,7
SPIWrite 07fd,90,0,7
SPIWrite 07fe,59,0,7
SPIWrite 07ff,46,0,7
SPIWrite 0800,48,0,7
SPIWrite 0801,46,0,7
SPIWrite 0802,eb,0,7
SPIWrite 0803,f7,0,7
SPIWrite 0804,81,0,7
SPIWrite 0805,f8,0,7
SPIWrite 0806,bd,0,7
SPIWrite 0807,e8,0,7
SPIWrite 0808,fe,0,7
SPIWrite 0809,8f,0,7
SPIWrite 080a,c0,0,7
SPIWrite 080b,46,0,7
SPIWrite 080c,96,0,7
SPIWrite 080d,21,0,7
SPIWrite 080e,01,0,7
SPIWrite 080f,20,0,7
SPIWrite 0810,08,0,7
SPIWrite 0811,e0,0,7
SPIWrite 0812,ff,0,7
SPIWrite 0813,ff,0,7
SPIWrite 0814,44,0,7
SPIWrite 0815,04,0,7
SPIWrite 0816,00,0,7
SPIWrite 0817,38,0,7
SPIWrite 0818,f8,0,7
SPIWrite 0819,b5,0,7
SPIWrite 081a,16,0,7
SPIWrite 081b,4c,0,7
SPIWrite 081c,04,0,7
SPIWrite 081d,20,0,7
SPIWrite 081e,69,0,7
SPIWrite 081f,46,0,7

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SPIWrite 0012,00,4,4    //PAGE: sys_calib_macro_memory=0x0; (Meaning: );      Address(0x12[4:4],)
SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1; (Meaning: );      Address(0x13[4:4],)
SPIWrite 00a3,08,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,08,0,7
SPIWrite 00a4,00,0,7

SPIPoll 00f0,0,0,1

SPIWrite 0193,81,0,7

\\Read  macro_opcode_operand=0x81000000;      Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0; (Meaning: );      Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;  Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;  Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;      Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;      Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;  Address(0x20[7:7],)

SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;  Address(0x13[5:5],)
SPIWrite 0012,10,4,4    //PAGE: sys_calib_macro_memory=0x1; (Meaning: );      Address(0x12[4:4],)
SPIWrite 0040,00,0,7
SPIWrite 0020,22,0,7
SPIWrite 0021,46,0,7
SPIWrite 0022,12,0,7
SPIWrite 0023,f8,0,7
SPIWrite 0024,01,0,7
SPIWrite 0025,3b,0,7
SPIWrite 0026,40,0,7
SPIWrite 0027,1e,0,7
SPIWrite 0028,01,0,7
SPIWrite 0029,f8,0,7
SPIWrite 002a,01,0,7
SPIWrite 002b,3b,0,7
SPIWrite 002c,f9,0,7
SPIWrite 002d,d1,0,7
SPIWrite 002e,e3,0,7
SPIWrite 002f,f7,0,7
SPIWrite 0030,45,0,7
SPIWrite 0031,fc,0,7
SPIWrite 0032,12,0,7
SPIWrite 0033,4e,0,7
SPIWrite 0034,10,0,7
SPIWrite 0035,4a,0,7
SPIWrite 0036,04,0,7
SPIWrite 0037,21,0,7
SPIWrite 0038,00,0,7
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SPIWrite 0039,20,0,7
SPIWrite 003a,6f,0,7
SPIWrite 003b,46,0,7
SPIWrite 003c,15,0,7
SPIWrite 003d,78,0,7
SPIWrite 003e,17,0,7
SPIWrite 003f,f8,0,7
SPIWrite 0040,01,0,7
SPIWrite 0041,2b,0,7
SPIWrite 0042,14,0,7
SPIWrite 0043,f8,0,7
SPIWrite 0044,01,0,7
SPIWrite 0045,3b,0,7
SPIWrite 0046,9a,0,7
SPIWrite 0047,42,0,7
SPIWrite 0048,18,0,7
SPIWrite 0049,bf,0,7
SPIWrite 004a,35,0,7
SPIWrite 004b,54,0,7
SPIWrite 004c,40,0,7
SPIWrite 004d,1c,0,7
SPIWrite 004e,49,0,7
SPIWrite 004f,1e,0,7
SPIWrite 0050,f5,0,7
SPIWrite 0051,d1,0,7
SPIWrite 0052,f8,0,7
SPIWrite 0053,bd,0,7
SPIWrite 0054,bf,0,7
SPIWrite 0055,b5,0,7
SPIWrite 0056,0a,0,7
SPIWrite 0057,4d,0,7
SPIWrite 0058,08,0,7
SPIWrite 0059,af,0,7
SPIWrite 005a,3c,0,7
SPIWrite 005b,7b,0,7
SPIWrite 005c,2c,0,7
SPIWrite 005d,70,0,7
SPIWrite 005e,3d,0,7
SPIWrite 005f,68,0,7
SPIWrite 0060,00,0,7
SPIWrite 0061,95,0,7
SPIWrite 0062,3d,0,7
SPIWrite 0063,79,0,7
SPIWrite 0064,01,0,7
SPIWrite 0065,95,0,7
SPIWrite 0066,3d,0,7
SPIWrite 0067,7a,0,7
SPIWrite 0068,02,0,7
SPIWrite 0069,95,0,7
SPIWrite 006a,03,0,7
SPIWrite 006b,94,0,7
SPIWrite 006c,e4,0,7
SPIWrite 006d,f7,0,7
SPIWrite 006e,16,0,7
SPIWrite 006f,fc,0,7
SPIWrite 0070,00,0,7
SPIWrite 0071,90,0,7
SPIWrite 0072,bf,0,7
SPIWrite 0073,bd,0,7
SPIWrite 0074,cc,0,7
SPIWrite 0075,7c,0,7
SPIWrite 0076,01,0,7
SPIWrite 0077,20,0,7
SPIWrite 0078,98,0,7
SPIWrite 0079,21,0,7
SPIWrite 007a,01,0,7
SPIWrite 007b,20,0,7
SPIWrite 007c,92,0,7
SPIWrite 007d,21,0,7

SPIWrite 007e,01,0,7
SPIWrite 007f,20,0,7
SPIWrite 0080,99,0,7
SPIWrite 0081,21,0,7
SPIWrite 0082,01,0,7
SPIWrite 0083,20,0,7
SPIWrite 0084,bc,0,7
SPIWrite 0085,b5,0,7
SPIWrite 0086,0b,0,7
SPIWrite 0087,4c,0,7
SPIWrite 0088,06,0,7
SPIWrite 0089,af,0,7
SPIWrite 008a,24,0,7
SPIWrite 008b,78,0,7
SPIWrite 008c,bd,0,7
SPIWrite 008d,88,0,7
SPIWrite 008e,01,0,7
SPIWrite 008f,2c,0,7
SPIWrite 0090,09,0,7
SPIWrite 0091,d0,0,7
SPIWrite 0092,09,0,7
SPIWrite 0093,4b,0,7
SPIWrite 0094,09,0,7
SPIWrite 0095,4c,0,7
SPIWrite 0096,da,0,7
SPIWrite 0097,79,0,7
SPIWrite 0098,24,0,7
SPIWrite 0099,68,0,7
SPIWrite 009a,00,0,7
SPIWrite 009b,92,0,7
SPIWrite 009c,01,0,7
SPIWrite 009d,95,0,7
SPIWrite 009e,da,0,7
SPIWrite 009f,78,0,7
SPIWrite 00a0,5b,0,7
SPIWrite 00a1,79,0,7
SPIWrite 00a2,a0,0,7
SPIWrite 00a3,47,0,7
SPIWrite 00a4,bc,0,7
SPIWrite 00a5,bd,0,7
SPIWrite 00a6,3c,0,7
SPIWrite 00a7,78,0,7
SPIWrite 00a8,00,0,7
SPIWrite 00a9,94,0,7
SPIWrite 00aa,01,0,7
SPIWrite 00ab,95,0,7
SPIWrite 00ac,ed,0,7
SPIWrite 00ad,f7,0,7
SPIWrite 00ae,a6,0,7
SPIWrite 00af,fe,0,7
SPIWrite 00b0,bc,0,7
SPIWrite 00b1,bd,0,7
SPIWrite 00b2,c0,0,7
SPIWrite 00b3,46,0,7
SPIWrite 00b4,96,0,7
SPIWrite 00b5,21,0,7
SPIWrite 00b6,01,0,7
SPIWrite 00b7,20,0,7
SPIWrite 00b8,38,0,7
SPIWrite 00b9,19,0,7
SPIWrite 00ba,01,0,7
SPIWrite 00bb,20,0,7
SPIWrite 00bc,84,0,7
SPIWrite 00bd,14,0,7
SPIWrite 00be,01,0,7
SPIWrite 00bf,20,0,7
SPIWrite 00c0,38,0,7
SPIWrite 00c1,b5,0,7
SPIWrite 00c2,04,0,7

SPIWrite 00c3,9c,0,7
SPIWrite 00c4,00,0,7
SPIWrite 00c5,94,0,7
SPIWrite 00c6,e7,0,7
SPIWrite 00c7,f7,0,7
SPIWrite 00c8,38,0,7
SPIWrite 00c9,fc,0,7
SPIWrite 00ca,09,0,7
SPIWrite 00cb,49,0,7
SPIWrite 00cc,09,0,7
SPIWrite 00cd,4a,0,7
SPIWrite 00ce,09,0,7
SPIWrite 00cf,78,0,7
SPIWrite 00d0,09,0,7
SPIWrite 00d1,4c,0,7
SPIWrite 00d2,8b,0,7
SPIWrite 00d3,00,0,7
SPIWrite 00d4,03,0,7
SPIWrite 00d5,eb,0,7
SPIWrite 00d6,41,0,7
SPIWrite 00d7,13,0,7
SPIWrite 00d8,d2,0,7
SPIWrite 00d9,18,0,7
SPIWrite 00da,92,0,7
SPIWrite 00db,f8,0,7
SPIWrite 00dc,20,0,7
SPIWrite 00dd,50,0,7
SPIWrite 00de,0b,0,7
SPIWrite 00df,01,0,7
SPIWrite 00e0,03,0,7
SPIWrite 00e1,eb,0,7
SPIWrite 00e2,41,0,7
SPIWrite 00e3,13,0,7
SPIWrite 00e4,e4,0,7
SPIWrite 00e5,18,0,7
SPIWrite 00e6,65,0,7
SPIWrite 00e7,77,0,7
SPIWrite 00e8,92,0,7
SPIWrite 00e9,f8,0,7
SPIWrite 00ea,21,0,7
SPIWrite 00eb,10,0,7
SPIWrite 00ec,a1,0,7
SPIWrite 00ed,77,0,7
SPIWrite 00ee,38,0,7
SPIWrite 00ef,bd,0,7
SPIWrite 00f0,9a,0,7
SPIWrite 00f1,21,0,7
SPIWrite 00f2,01,0,7
SPIWrite 00f3,20,0,7
SPIWrite 00f4,dc,0,7
SPIWrite 00f5,0e,0,7
SPIWrite 00f6,01,0,7
SPIWrite 00f7,20,0,7
SPIWrite 00f8,14,0,7
SPIWrite 00f9,0a,0,7
SPIWrite 00fa,01,0,7
SPIWrite 00fb,20,0,7
SPIWrite 00fc,08,0,7
SPIWrite 00fd,49,0,7
SPIWrite 00fe,09,0,7
SPIWrite 00ff,48,0,7
SPIWrite 0100,0a,0,7
SPIWrite 0101,68,0,7
SPIWrite 0102,92,0,7
SPIWrite 0103,0f,0,7
SPIWrite 0104,03,0,7
SPIWrite 0105,d3,0,7
SPIWrite 0106,0a,0,7
SPIWrite 0107,68,0,7

SPIWrite 0108,c2,0,7
SPIWrite 0109,f3,0,7
SPIWrite 010a,0c,0,7
SPIWrite 010b,42,0,7
SPIWrite 010c,02,0,7
SPIWrite 010d,80,0,7
SPIWrite 010e,4a,0,7
SPIWrite 010f,68,0,7
SPIWrite 0110,92,0,7
SPIWrite 0111,0b,0,7
SPIWrite 0112,04,0,7
SPIWrite 0113,d3,0,7
SPIWrite 0114,49,0,7
SPIWrite 0115,68,0,7
SPIWrite 0116,80,0,7
SPIWrite 0117,1d,0,7
SPIWrite 0118,c1,0,7
SPIWrite 0119,f3,0,7
SPIWrite 011a,0c,0,7
SPIWrite 011b,01,0,7
SPIWrite 011c,01,0,7
SPIWrite 011d,80,0,7
SPIWrite 011e,70,0,7
SPIWrite 011f,47,0,7
SPIWrite 0120,b4,0,7
SPIWrite 0121,02,0,7
SPIWrite 0122,00,0,7
SPIWrite 0123,38,0,7
SPIWrite 0124,64,0,7
SPIWrite 0125,21,0,7
SPIWrite 0126,01,0,7
SPIWrite 0127,20,0,7
SPIWrite 0128,00,0,7
SPIWrite 0129,00,0,7
SPIWrite 012a,00,0,7
SPIWrite 012b,00,0,7
SPIWrite 012c,00,0,7
SPIWrite 012d,00,0,7
SPIWrite 012e,00,0,7
SPIWrite 012f,00,0,7
SPIWrite 0130,00,0,7
SPIWrite 0131,00,0,7
SPIWrite 0132,00,0,7
SPIWrite 0133,00,0,7
SPIWrite 0134,51,0,7
SPIWrite 0135,f0,0,7
SPIWrite 0136,37,0,7
SPIWrite 0137,98,0,7
SPIWrite 0138,33,0,7
SPIWrite 0139,f3,0,7
SPIWrite 013a,04,0,7
SPIWrite 013b,b5,0,7
SPIWrite 013c,ca,0,7
SPIWrite 013d,fc,0,7
SPIWrite 013e,44,0,7
SPIWrite 013f,d7,0,7
SPIWrite 0140,00,0,7
SPIWrite 0141,00,0,7
SPIWrite 0142,00,0,7
SPIWrite 0143,00,0,7
SPIWrite 0144,00,0,7
SPIWrite 0145,00,0,7
SPIWrite 0146,00,0,7
SPIWrite 0147,00,0,7
SPIWrite 0148,00,0,7
SPIWrite 0149,00,0,7
SPIWrite 014a,00,0,7
SPIWrite 014b,00,0,7
SPIWrite 014c,00,0,7

SPIWrite 014d,00,0,7
SPIWrite 014e,00,0,7
SPIWrite 014f,00,0,7
SPIWrite 0150,00,0,7
SPIWrite 0151,00,0,7
SPIWrite 0152,00,0,7
SPIWrite 0153,00,0,7
SPIWrite 0154,f8,0,7
SPIWrite 0155,1f,0,7
SPIWrite 0156,0c,0,7
SPIWrite 0157,00,0,7
SPIWrite 0158,00,0,7
SPIWrite 0159,00,0,7
SPIWrite 015a,00,0,7
SPIWrite 015b,00,0,7
SPIWrite 015c,00,0,7
SPIWrite 015d,00,0,7
SPIWrite 015e,00,0,7
SPIWrite 015f,00,0,7
SPIWrite 0160,00,0,7
SPIWrite 0161,00,0,7
SPIWrite 0162,00,0,7
SPIWrite 0163,00,0,7
SPIWrite 0164,00,0,7
SPIWrite 0165,00,0,7
SPIWrite 0166,00,0,7
SPIWrite 0167,00,0,7
SPIWrite 0168,00,0,7
SPIWrite 0169,00,0,7
SPIWrite 016a,00,0,7
SPIWrite 016b,00,0,7
SPIWrite 016c,00,0,7
SPIWrite 016d,00,0,7
SPIWrite 016e,00,0,7
SPIWrite 016f,00,0,7
SPIWrite 0170,00,0,7
SPIWrite 0171,00,0,7
SPIWrite 0172,00,0,7
SPIWrite 0173,00,0,7
SPIWrite 0174,00,0,7
SPIWrite 0175,00,0,7
SPIWrite 0176,00,0,7
SPIWrite 0177,00,0,7
SPIWrite 0178,00,0,7
SPIWrite 0179,00,0,7
SPIWrite 017a,00,0,7
SPIWrite 017b,00,0,7
SPIWrite 017c,00,0,7
SPIWrite 017d,00,0,7
SPIWrite 017e,00,0,7
SPIWrite 017f,00,0,7
SPIWrite 0180,00,0,7
SPIWrite 0181,00,0,7
SPIWrite 0182,00,0,7
SPIWrite 0183,00,0,7
SPIWrite 0184,00,0,7
SPIWrite 0185,00,0,7
SPIWrite 0186,00,0,7
SPIWrite 0187,00,0,7
SPIWrite 0188,00,0,7
SPIWrite 0189,00,0,7
SPIWrite 018a,00,0,7
SPIWrite 018b,00,0,7
SPIWrite 018c,00,0,7
SPIWrite 018d,00,0,7
SPIWrite 018e,00,0,7
SPIWrite 018f,00,0,7
SPIWrite 0190,00,0,7
SPIWrite 0191,00,0,7

SPIWrite 0192,00,0,7
SPIWrite 0193,00,0,7
SPIWrite 0194,00,0,7
SPIWrite 0195,00,0,7
SPIWrite 0196,00,0,7
SPIWrite 0197,00,0,7
SPIWrite 0198,00,0,7
SPIWrite 0199,00,0,7
SPIWrite 019a,00,0,7
SPIWrite 019b,00,0,7
SPIWrite 019c,00,0,7
SPIWrite 019d,00,0,7
SPIWrite 019e,00,0,7
SPIWrite 019f,00,0,7
SPIWrite 01a0,00,0,7
SPIWrite 01a1,00,0,7
SPIWrite 01a2,00,0,7
SPIWrite 01a3,00,0,7
SPIWrite 01a4,00,0,7
SPIWrite 01a5,00,0,7
SPIWrite 01a6,00,0,7
SPIWrite 01a7,00,0,7
SPIWrite 01a8,00,0,7
SPIWrite 01a9,00,0,7
SPIWrite 01aa,00,0,7
SPIWrite 01ab,00,0,7
SPIWrite 01ac,00,0,7
SPIWrite 01ad,00,0,7
SPIWrite 01ae,00,0,7
SPIWrite 01af,00,0,7
SPIWrite 01b0,00,0,7
SPIWrite 01b1,00,0,7
SPIWrite 01b2,00,0,7
SPIWrite 01b3,00,0,7
SPIWrite 01b4,00,0,7
SPIWrite 01b5,00,0,7
SPIWrite 01b6,00,0,7
SPIWrite 01b7,00,0,7
SPIWrite 01b8,00,0,7
SPIWrite 01b9,00,0,7
SPIWrite 01ba,00,0,7
SPIWrite 01bb,00,0,7
SPIWrite 01bc,00,0,7
SPIWrite 01bd,00,0,7
SPIWrite 01be,00,0,7
SPIWrite 01bf,00,0,7
SPIWrite 01c0,00,0,7
SPIWrite 01c1,00,0,7
SPIWrite 01c2,00,0,7
SPIWrite 01c3,00,0,7
SPIWrite 01c4,00,0,7
SPIWrite 01c5,00,0,7
SPIWrite 01c6,00,0,7
SPIWrite 01c7,00,0,7
SPIWrite 01c8,00,0,7
SPIWrite 01c9,00,0,7
SPIWrite 01ca,00,0,7
SPIWrite 01cb,00,0,7
SPIWrite 01cc,00,0,7
SPIWrite 01cd,00,0,7
SPIWrite 01ce,00,0,7
SPIWrite 01cf,00,0,7
SPIWrite 01d0,00,0,7
SPIWrite 01d1,00,0,7
SPIWrite 01d2,00,0,7
SPIWrite 01d3,00,0,7
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SPIWrite 01d5,00,0,7
SPIWrite 01d6,00,0,7

SPIWrite 01d7,00,0,7
SPIWrite 01d8,00,0,7
SPIWrite 01d9,00,0,7
SPIWrite 01da,00,0,7
SPIWrite 01db,00,0,7
SPIWrite 01dc,00,0,7
SPIWrite 01dd,00,0,7
SPIWrite 01de,00,0,7
SPIWrite 01df,00,0,7
SPIWrite 01e0,00,0,7
SPIWrite 01e1,00,0,7
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SPIWrite 01e3,00,0,7
SPIWrite 01e4,00,0,7
SPIWrite 01e5,00,0,7
SPIWrite 01e6,00,0,7
SPIWrite 01e7,00,0,7
SPIWrite 01e8,00,0,7
SPIWrite 01e9,00,0,7
SPIWrite 01ea,00,0,7
SPIWrite 01eb,00,0,7
SPIWrite 01ec,00,0,7
SPIWrite 01ed,00,0,7
SPIWrite 01ee,00,0,7
SPIWrite 01ef,00,0,7
SPIWrite 01f0,00,0,7
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SPIWrite 01f5,00,0,7
SPIWrite 01f6,00,0,7
SPIWrite 01f7,00,0,7
SPIWrite 01f8,00,0,7
SPIWrite 01f9,00,0,7
SPIWrite 01fa,00,0,7
SPIWrite 01fb,00,0,7
SPIWrite 01fc,00,0,7
SPIWrite 01fd,00,0,7
SPIWrite 01fe,00,0,7
SPIWrite 01ff,00,0,7
SPIWrite 0200,00,0,7
SPIWrite 0201,00,0,7
SPIWrite 0202,00,0,7
SPIWrite 0203,00,0,7
SPIWrite 0204,00,0,7
SPIWrite 0205,00,0,7
SPIWrite 0206,00,0,7
SPIWrite 0207,00,0,7
SPIWrite 0208,00,0,7
SPIWrite 0209,00,0,7
SPIWrite 020a,00,0,7
SPIWrite 020b,00,0,7
SPIWrite 020c,00,0,7
SPIWrite 020d,00,0,7
SPIWrite 020e,00,0,7
SPIWrite 020f,00,0,7
SPIWrite 0210,00,0,7
SPIWrite 0211,00,0,7
SPIWrite 0212,00,0,7
SPIWrite 0213,00,0,7
SPIWrite 0214,00,0,7
SPIWrite 0215,00,0,7
SPIWrite 0216,00,0,7
SPIWrite 0217,00,0,7
SPIWrite 0218,00,0,7
SPIWrite 0219,00,0,7
SPIWrite 021a,00,0,7
SPIWrite 021b,00,0,7

SPIWrite 021c,00,0,7
SPIWrite 021d,00,0,7
SPIWrite 021e,00,0,7
SPIWrite 021f,00,0,7
SPIWrite 0220,00,0,7
SPIWrite 0221,00,0,7
SPIWrite 0222,00,0,7
SPIWrite 0223,00,0,7
SPIWrite 0224,00,0,7
SPIWrite 0225,00,0,7
SPIWrite 0226,00,0,7
SPIWrite 0227,00,0,7
SPIWrite 0228,00,0,7
SPIWrite 0229,00,0,7
SPIWrite 022a,00,0,7
SPIWrite 022b,00,0,7
SPIWrite 022c,00,0,7
SPIWrite 022d,00,0,7
SPIWrite 022e,00,0,7
SPIWrite 022f,00,0,7
SPIWrite 0230,00,0,7
SPIWrite 0231,00,0,7
SPIWrite 0232,00,0,7
SPIWrite 0233,00,0,7
SPIWrite 0234,00,0,7
SPIWrite 0235,00,0,7
SPIWrite 0236,00,0,7
SPIWrite 0237,00,0,7
SPIWrite 0238,00,0,7
SPIWrite 0239,00,0,7
SPIWrite 023a,00,0,7
SPIWrite 023b,00,0,7
SPIWrite 023c,00,0,7
SPIWrite 023d,00,0,7
SPIWrite 023e,00,0,7
SPIWrite 023f,00,0,7
SPIWrite 0240,00,0,7
SPIWrite 0241,00,0,7
SPIWrite 0242,00,0,7
SPIWrite 0243,00,0,7
SPIWrite 0244,00,0,7
SPIWrite 0245,00,0,7
SPIWrite 0246,00,0,7
SPIWrite 0247,00,0,7
SPIWrite 0248,00,0,7
SPIWrite 0249,00,0,7
SPIWrite 024a,00,0,7
SPIWrite 024b,00,0,7
SPIWrite 024c,00,0,7
SPIWrite 024d,00,0,7
SPIWrite 024e,00,0,7
SPIWrite 024f,00,0,7
SPIWrite 0250,00,0,7
SPIWrite 0251,00,0,7
SPIWrite 0252,00,0,7
SPIWrite 0253,00,0,7
SPIWrite 0254,00,0,7
SPIWrite 0255,00,0,7
SPIWrite 0256,00,0,7
SPIWrite 0257,00,0,7
SPIWrite 0258,00,0,7
SPIWrite 0259,00,0,7
SPIWrite 025a,00,0,7
SPIWrite 025b,00,0,7
SPIWrite 025c,00,0,7
SPIWrite 025d,00,0,7
SPIWrite 025e,00,0,7
SPIWrite 025f,00,0,7
SPIWrite 0260,00,0,7

SPIWrite 0261,00,0,7
SPIWrite 0262,00,0,7
SPIWrite 0263,00,0,7
SPIWrite 0264,00,0,7
SPIWrite 0265,00,0,7
SPIWrite 0266,00,0,7
SPIWrite 0267,00,0,7
SPIWrite 0268,00,0,7
SPIWrite 0269,00,0,7
SPIWrite 026a,00,0,7
SPIWrite 026b,00,0,7
SPIWrite 026c,00,0,7
SPIWrite 026d,00,0,7
SPIWrite 026e,00,0,7
SPIWrite 026f,00,0,7
SPIWrite 0270,00,0,7
SPIWrite 0271,00,0,7
SPIWrite 0272,00,0,7
SPIWrite 0273,00,0,7
SPIWrite 0274,00,0,7
SPIWrite 0275,00,0,7
SPIWrite 0276,00,0,7
SPIWrite 0277,00,0,7
SPIWrite 0278,00,0,7
SPIWrite 0279,00,0,7
SPIWrite 027a,00,0,7
SPIWrite 027b,00,0,7
SPIWrite 027c,00,0,7
SPIWrite 027d,00,0,7
SPIWrite 027e,00,0,7
SPIWrite 027f,00,0,7
SPIWrite 0280,00,0,7
SPIWrite 0281,00,0,7
SPIWrite 0282,00,0,7
SPIWrite 0283,00,0,7
SPIWrite 0284,00,0,7
SPIWrite 0285,00,0,7
SPIWrite 0286,00,0,7
SPIWrite 0287,00,0,7
SPIWrite 0288,00,0,7
SPIWrite 0289,00,0,7
SPIWrite 028a,00,0,7
SPIWrite 028b,00,0,7
SPIWrite 028c,00,0,7
SPIWrite 028d,00,0,7
SPIWrite 028e,00,0,7
SPIWrite 028f,00,0,7
SPIWrite 0290,00,0,7
SPIWrite 0291,00,0,7
SPIWrite 0292,00,0,7
SPIWrite 0293,00,0,7
SPIWrite 0294,00,0,7
SPIWrite 0295,00,0,7
SPIWrite 0296,00,0,7
SPIWrite 0297,00,0,7
SPIWrite 0298,00,0,7
SPIWrite 0299,00,0,7
SPIWrite 029a,00,0,7
SPIWrite 029b,00,0,7
SPIWrite 029c,00,0,7
SPIWrite 029d,00,0,7
SPIWrite 029e,00,0,7
SPIWrite 029f,00,0,7
SPIWrite 02a0,00,0,7
SPIWrite 02a1,00,0,7
SPIWrite 02a2,00,0,7
SPIWrite 02a3,00,0,7
SPIWrite 02a4,00,0,7
SPIWrite 02a5,00,0,7

SPIWrite 02a6,00,0,7
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SPIWrite 02a9,00,0,7
SPIWrite 02aa,00,0,7
SPIWrite 02ab,00,0,7
SPIWrite 02ac,00,0,7
SPIWrite 02ad,00,0,7
SPIWrite 02ae,00,0,7
SPIWrite 02af,00,0,7
SPIWrite 02b0,00,0,7
SPIWrite 02b1,00,0,7
SPIWrite 02b2,00,0,7
SPIWrite 02b3,00,0,7
SPIWrite 02b4,00,0,7
SPIWrite 02b5,00,0,7
SPIWrite 02b6,00,0,7
SPIWrite 02b7,00,0,7
SPIWrite 02b8,00,0,7
SPIWrite 02b9,00,0,7
SPIWrite 02ba,00,0,7
SPIWrite 02bb,00,0,7
SPIWrite 02bc,00,0,7
SPIWrite 02bd,00,0,7
SPIWrite 02be,00,0,7
SPIWrite 02bf,00,0,7
SPIWrite 02c0,00,0,7
SPIWrite 02c1,00,0,7
SPIWrite 02c2,00,0,7
SPIWrite 02c3,00,0,7
SPIWrite 02c4,00,0,7
SPIWrite 02c5,00,0,7
SPIWrite 02c6,00,0,7
SPIWrite 02c7,00,0,7
SPIWrite 02c8,00,0,7
SPIWrite 02c9,00,0,7
SPIWrite 02ca,00,0,7
SPIWrite 02cb,00,0,7
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SPIWrite 02d6,00,0,7
SPIWrite 02d7,00,0,7
SPIWrite 02d8,00,0,7
SPIWrite 02d9,00,0,7
SPIWrite 02da,00,0,7
SPIWrite 02db,00,0,7
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SPIWrite 02dd,00,0,7
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SPIWrite 02e0,00,0,7
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SPIWrite 02e6,00,0,7
SPIWrite 02e7,00,0,7
SPIWrite 02e8,00,0,7
SPIWrite 02e9,00,0,7
SPIWrite 02ea,00,0,7

SPIWrite 02eb,00,0,7
SPIWrite 02ec,00,0,7
SPIWrite 02ed,00,0,7
SPIWrite 02ee,00,0,7
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SPIWrite 02f0,00,0,7
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SPIWrite 02f6,00,0,7
SPIWrite 02f7,00,0,7
SPIWrite 02f8,00,0,7
SPIWrite 02f9,00,0,7
SPIWrite 02fa,00,0,7
SPIWrite 02fb,00,0,7
SPIWrite 02fc,00,0,7
SPIWrite 02fd,00,0,7
SPIWrite 02fe,00,0,7
SPIWrite 02ff,00,0,7
SPIWrite 0300,00,0,7
SPIWrite 0301,00,0,7
SPIWrite 0302,00,0,7
SPIWrite 0303,00,0,7
SPIWrite 0304,00,0,7
SPIWrite 0305,00,0,7
SPIWrite 0306,00,0,7
SPIWrite 0307,00,0,7
SPIWrite 0308,00,0,7
SPIWrite 0309,00,0,7
SPIWrite 030a,00,0,7
SPIWrite 030b,00,0,7
SPIWrite 030c,00,0,7
SPIWrite 030d,00,0,7
SPIWrite 030e,00,0,7
SPIWrite 030f,00,0,7
SPIWrite 0310,00,0,7
SPIWrite 0311,00,0,7
SPIWrite 0312,00,0,7
SPIWrite 0313,00,0,7
SPIWrite 0314,00,0,7
SPIWrite 0315,00,0,7
SPIWrite 0316,00,0,7
SPIWrite 0317,00,0,7
SPIWrite 0318,00,0,7
SPIWrite 0319,00,0,7
SPIWrite 031a,00,0,7
SPIWrite 031b,00,0,7
SPIWrite 031c,00,0,7
SPIWrite 031d,00,0,7
SPIWrite 031e,00,0,7
SPIWrite 031f,00,0,7
SPIWrite 0320,00,0,7
SPIWrite 0321,00,0,7
SPIWrite 0322,00,0,7
SPIWrite 0323,00,0,7
SPIWrite 0324,00,0,7
SPIWrite 0325,00,0,7
SPIWrite 0326,00,0,7
SPIWrite 0327,00,0,7
SPIWrite 0328,00,0,7
SPIWrite 0329,00,0,7
SPIWrite 032a,00,0,7
SPIWrite 032b,00,0,7
SPIWrite 032c,00,0,7
SPIWrite 032d,00,0,7
SPIWrite 032e,00,0,7
SPIWrite 032f,00,0,7

SPIWrite 0330,00,0,7
SPIWrite 0331,00,0,7
SPIWrite 0332,00,0,7
SPIWrite 0333,00,0,7
SPIWrite 0334,00,0,7
SPIWrite 0335,00,0,7
SPIWrite 0336,00,0,7
SPIWrite 0337,00,0,7
SPIWrite 0338,00,0,7
SPIWrite 0339,00,0,7
SPIWrite 033a,00,0,7
SPIWrite 033b,00,0,7
SPIWrite 033c,00,0,7
SPIWrite 033d,00,0,7
SPIWrite 033e,00,0,7
SPIWrite 033f,00,0,7
SPIWrite 0340,00,0,7
SPIWrite 0341,00,0,7
SPIWrite 0342,00,0,7
SPIWrite 0343,00,0,7
SPIWrite 0344,00,0,7
SPIWrite 0345,00,0,7
SPIWrite 0346,00,0,7
SPIWrite 0347,00,0,7
SPIWrite 0348,00,0,7
SPIWrite 0349,00,0,7
SPIWrite 034a,00,0,7
SPIWrite 034b,00,0,7
SPIWrite 034c,00,0,7
SPIWrite 034d,00,0,7
SPIWrite 034e,00,0,7
SPIWrite 034f,00,0,7
SPIWrite 0350,00,0,7
SPIWrite 0351,00,0,7
SPIWrite 0352,00,0,7
SPIWrite 0353,00,0,7
SPIWrite 0354,00,0,7
SPIWrite 0355,00,0,7
SPIWrite 0356,00,0,7
SPIWrite 0357,00,0,7
SPIWrite 0358,00,0,7
SPIWrite 0359,00,0,7
SPIWrite 035a,00,0,7
SPIWrite 035b,00,0,7
SPIWrite 035c,00,0,7
SPIWrite 035d,00,0,7
SPIWrite 035e,00,0,7
SPIWrite 035f,00,0,7
SPIWrite 0360,00,0,7
SPIWrite 0361,00,0,7
SPIWrite 0362,00,0,7
SPIWrite 0363,00,0,7
SPIWrite 0364,00,0,7
SPIWrite 0365,00,0,7
SPIWrite 0366,00,0,7
SPIWrite 0367,00,0,7
SPIWrite 0368,00,0,7
SPIWrite 0369,00,0,7
SPIWrite 036a,00,0,7
SPIWrite 036b,00,0,7
SPIWrite 036c,00,0,7
SPIWrite 036d,00,0,7
SPIWrite 036e,00,0,7
SPIWrite 036f,00,0,7
SPIWrite 0370,00,0,7
SPIWrite 0371,00,0,7
SPIWrite 0372,00,0,7
SPIWrite 0373,00,0,7
SPIWrite 0374,00,0,7

SPIWrite 0375,00,0,7
SPIWrite 0376,00,0,7
SPIWrite 0377,00,0,7
SPIWrite 0378,00,0,7
SPIWrite 0379,00,0,7
SPIWrite 037a,00,0,7
SPIWrite 037b,00,0,7
SPIWrite 037c,00,0,7
SPIWrite 037d,00,0,7
SPIWrite 037e,00,0,7
SPIWrite 037f,00,0,7
SPIWrite 0380,00,0,7
SPIWrite 0381,00,0,7
SPIWrite 0382,00,0,7
SPIWrite 0383,00,0,7
SPIWrite 0384,00,0,7
SPIWrite 0385,00,0,7
SPIWrite 0386,00,0,7
SPIWrite 0387,00,0,7
SPIWrite 0388,00,0,7
SPIWrite 0389,00,0,7
SPIWrite 038a,00,0,7
SPIWrite 038b,00,0,7
SPIWrite 038c,00,0,7
SPIWrite 038d,00,0,7
SPIWrite 038e,00,0,7
SPIWrite 038f,00,0,7
SPIWrite 0390,00,0,7
SPIWrite 0391,00,0,7
SPIWrite 0392,00,0,7
SPIWrite 0393,00,0,7
SPIWrite 0394,00,0,7
SPIWrite 0395,00,0,7
SPIWrite 0396,00,0,7
SPIWrite 0397,00,0,7
SPIWrite 0398,00,0,7
SPIWrite 0399,00,0,7
SPIWrite 039a,00,0,7
SPIWrite 039b,00,0,7
SPIWrite 039c,00,0,7
SPIWrite 039d,00,0,7
SPIWrite 039e,00,0,7
SPIWrite 039f,00,0,7
SPIWrite 03a0,00,0,7
SPIWrite 03a1,00,0,7
SPIWrite 03a2,00,0,7
SPIWrite 03a3,00,0,7
SPIWrite 03a4,00,0,7
SPIWrite 03a5,00,0,7
SPIWrite 03a6,00,0,7
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SPIWrite 03a8,00,0,7
SPIWrite 03a9,00,0,7
SPIWrite 03aa,00,0,7
SPIWrite 03ab,00,0,7
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SPIWrite 03ba,00,0,7
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SPIWrite 03ca,00,0,7
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SPIWrite 03da,00,0,7
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SPIWrite 03ea,00,0,7
SPIWrite 03eb,00,0,7
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SPIWrite 03f6,00,0,7
SPIWrite 03f7,00,0,7
SPIWrite 03f8,00,0,7
SPIWrite 03f9,00,0,7
SPIWrite 03fa,00,0,7
SPIWrite 03fb,00,0,7
SPIWrite 03fc,00,0,7
SPIWrite 03fd,00,0,7
SPIWrite 03fe,00,0,7

SPIWrite 03ff,00,0,7
SPIWrite 0400,00,0,7
SPIWrite 0401,00,0,7
SPIWrite 0402,00,0,7
SPIWrite 0403,00,0,7
SPIWrite 0404,00,0,7
SPIWrite 0405,00,0,7
SPIWrite 0406,00,0,7
SPIWrite 0407,00,0,7
SPIWrite 0408,00,0,7
SPIWrite 0409,00,0,7
SPIWrite 040a,00,0,7
SPIWrite 040b,00,0,7
SPIWrite 040c,00,0,7
SPIWrite 040d,00,0,7
SPIWrite 040e,00,0,7
SPIWrite 040f,00,0,7
SPIWrite 0410,00,0,7
SPIWrite 0411,00,0,7
SPIWrite 0412,00,0,7
SPIWrite 0413,00,0,7
SPIWrite 0414,00,0,7
SPIWrite 0415,00,0,7
SPIWrite 0416,00,0,7
SPIWrite 0417,00,0,7
SPIWrite 0418,00,0,7
SPIWrite 0419,00,0,7
SPIWrite 041a,00,0,7
SPIWrite 041b,00,0,7
SPIWrite 041c,00,0,7
SPIWrite 041d,00,0,7
SPIWrite 041e,00,0,7
SPIWrite 041f,00,0,7
SPIWrite 0420,00,0,7
SPIWrite 0421,00,0,7
SPIWrite 0422,00,0,7
SPIWrite 0423,00,0,7
SPIWrite 0424,00,0,7
SPIWrite 0425,00,0,7
SPIWrite 0426,00,0,7
SPIWrite 0427,00,0,7
SPIWrite 0428,00,0,7
SPIWrite 0429,00,0,7
SPIWrite 042a,00,0,7
SPIWrite 042b,00,0,7
SPIWrite 042c,00,0,7
SPIWrite 042d,00,0,7
SPIWrite 042e,00,0,7
SPIWrite 042f,00,0,7
SPIWrite 0430,00,0,7
SPIWrite 0431,00,0,7
SPIWrite 0432,00,0,7
SPIWrite 0433,00,0,7
SPIWrite 0434,00,0,7
SPIWrite 0435,00,0,7
SPIWrite 0436,00,0,7
SPIWrite 0437,00,0,7
SPIWrite 0438,00,0,7
SPIWrite 0439,00,0,7
SPIWrite 043a,00,0,7
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SPIWrite 043c,00,0,7
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SPIWrite 043e,00,0,7
SPIWrite 043f,00,0,7
SPIWrite 0440,00,0,7
SPIWrite 0441,00,0,7
SPIWrite 0442,00,0,7
SPIWrite 0443,00,0,7

SPIWrite 0444,00,0,7
SPIWrite 0445,00,0,7
SPIWrite 0446,00,0,7
SPIWrite 0447,00,0,7
SPIWrite 0448,00,0,7
SPIWrite 0449,00,0,7
SPIWrite 044a,00,0,7
SPIWrite 044b,00,0,7
SPIWrite 044c,00,0,7
SPIWrite 044d,00,0,7
SPIWrite 044e,00,0,7
SPIWrite 044f,00,0,7
SPIWrite 0450,00,0,7
SPIWrite 0451,00,0,7
SPIWrite 0452,00,0,7
SPIWrite 0453,00,0,7
SPIWrite 0454,00,0,7
SPIWrite 0455,00,0,7
SPIWrite 0456,00,0,7
SPIWrite 0457,00,0,7
SPIWrite 0458,00,0,7
SPIWrite 0459,00,0,7
SPIWrite 045a,00,0,7
SPIWrite 045b,00,0,7
SPIWrite 045c,00,0,7
SPIWrite 045d,00,0,7
SPIWrite 045e,00,0,7
SPIWrite 045f,00,0,7
SPIWrite 0460,00,0,7
SPIWrite 0461,00,0,7
SPIWrite 0462,00,0,7
SPIWrite 0463,00,0,7
SPIWrite 0464,00,0,7
SPIWrite 0465,00,0,7
SPIWrite 0466,00,0,7
SPIWrite 0467,00,0,7
SPIWrite 0468,00,0,7
SPIWrite 0469,00,0,7
SPIWrite 046a,00,0,7
SPIWrite 046b,00,0,7
SPIWrite 046c,00,0,7
SPIWrite 046d,00,0,7
SPIWrite 046e,00,0,7
SPIWrite 046f,00,0,7
SPIWrite 0470,00,0,7
SPIWrite 0471,00,0,7
SPIWrite 0472,00,0,7
SPIWrite 0473,00,0,7
SPIWrite 0474,00,0,7
SPIWrite 0475,00,0,7
SPIWrite 0476,00,0,7
SPIWrite 0477,00,0,7
SPIWrite 0478,00,0,7
SPIWrite 0479,00,0,7
SPIWrite 047a,00,0,7
SPIWrite 047b,00,0,7
SPIWrite 047c,00,0,7
SPIWrite 047d,00,0,7
SPIWrite 047e,00,0,7
SPIWrite 047f,00,0,7
SPIWrite 0480,00,0,7
SPIWrite 0481,00,0,7
SPIWrite 0482,00,0,7
SPIWrite 0483,00,0,7
SPIWrite 0484,00,0,7
SPIWrite 0485,00,0,7
SPIWrite 0486,00,0,7
SPIWrite 0487,00,0,7
SPIWrite 0488,00,0,7

SPIWrite 0489,00,0,7
SPIWrite 048a,00,0,7
SPIWrite 048b,00,0,7
SPIWrite 048c,00,0,7
SPIWrite 048d,00,0,7
SPIWrite 048e,00,0,7
SPIWrite 048f,00,0,7
SPIWrite 0490,00,0,7
SPIWrite 0491,00,0,7
SPIWrite 0492,00,0,7
SPIWrite 0493,00,0,7
SPIWrite 0494,00,0,7
SPIWrite 0495,00,0,7
SPIWrite 0496,00,0,7
SPIWrite 0497,00,0,7
SPIWrite 0498,00,0,7
SPIWrite 0499,00,0,7
SPIWrite 049a,00,0,7
SPIWrite 049b,00,0,7
SPIWrite 049c,00,0,7
SPIWrite 049d,00,0,7
SPIWrite 049e,00,0,7
SPIWrite 049f,00,0,7
SPIWrite 04a0,00,0,7
SPIWrite 04a1,00,0,7
SPIWrite 04a2,00,0,7
SPIWrite 04a3,00,0,7
SPIWrite 04a4,00,0,7
SPIWrite 04a5,00,0,7
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SPIWrite 04a9,00,0,7
SPIWrite 04aa,00,0,7
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SPIWrite 04b0,00,0,7
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SPIWrite 04b9,00,0,7
SPIWrite 04ba,00,0,7
SPIWrite 04bb,00,0,7
SPIWrite 04bc,00,0,7
SPIWrite 04bd,00,0,7
SPIWrite 04be,00,0,7
SPIWrite 04bf,00,0,7
SPIWrite 04c0,00,0,7
SPIWrite 04c1,00,0,7
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SPIWrite 04c8,00,0,7
SPIWrite 04c9,00,0,7
SPIWrite 04ca,00,0,7
SPIWrite 04cb,00,0,7
SPIWrite 04cc,00,0,7
SPIWrite 04cd,00,0,7

SPIWrite 04ce,00,0,7
SPIWrite 04cf,00,0,7
SPIWrite 04d0,00,0,7
SPIWrite 04d1,00,0,7
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SPIWrite 04d7,00,0,7
SPIWrite 04d8,00,0,7
SPIWrite 04d9,00,0,7
SPIWrite 04da,00,0,7
SPIWrite 04db,00,0,7
SPIWrite 04dc,00,0,7
SPIWrite 04dd,00,0,7
SPIWrite 04de,00,0,7
SPIWrite 04df,00,0,7
SPIWrite 04e0,00,0,7
SPIWrite 04e1,00,0,7
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SPIWrite 04e3,00,0,7
SPIWrite 04e4,00,0,7
SPIWrite 04e5,00,0,7
SPIWrite 04e6,00,0,7
SPIWrite 04e7,00,0,7
SPIWrite 04e8,00,0,7
SPIWrite 04e9,00,0,7
SPIWrite 04ea,00,0,7
SPIWrite 04eb,00,0,7
SPIWrite 04ec,00,0,7
SPIWrite 04ed,00,0,7
SPIWrite 04ee,00,0,7
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SPIWrite 04f0,00,0,7
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SPIWrite 04f5,00,0,7
SPIWrite 04f6,00,0,7
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SPIWrite 04f9,00,0,7
SPIWrite 04fa,00,0,7
SPIWrite 04fb,00,0,7
SPIWrite 04fc,00,0,7
SPIWrite 04fd,00,0,7
SPIWrite 04fe,00,0,7
SPIWrite 04ff,00,0,7
SPIWrite 0500,00,0,7
SPIWrite 0501,00,0,7
SPIWrite 0502,00,0,7
SPIWrite 0503,00,0,7
SPIWrite 0504,00,0,7
SPIWrite 0505,00,0,7
SPIWrite 0506,00,0,7
SPIWrite 0507,00,0,7
SPIWrite 0508,00,0,7
SPIWrite 0509,00,0,7
SPIWrite 050a,00,0,7
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SPIWrite 0510,00,0,7
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SPIWrite 0513,00,0,7
SPIWrite 0514,00,0,7
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SPIWrite 0518,00,0,7
SPIWrite 0519,00,0,7
SPIWrite 051a,00,0,7
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SPIWrite 051c,00,0,7
SPIWrite 051d,00,0,7
SPIWrite 051e,00,0,7
SPIWrite 051f,00,0,7
SPIWrite 0520,00,0,7
SPIWrite 0521,00,0,7
SPIWrite 0522,00,0,7
SPIWrite 0523,00,0,7
SPIWrite 0524,00,0,7
SPIWrite 0525,00,0,7
SPIWrite 0526,00,0,7
SPIWrite 0527,00,0,7
SPIWrite 0528,00,0,7
SPIWrite 0529,00,0,7
SPIWrite 052a,00,0,7
SPIWrite 052b,00,0,7
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SPIWrite 052e,00,0,7
SPIWrite 052f,00,0,7
SPIWrite 0530,00,0,7
SPIWrite 0531,00,0,7
SPIWrite 0532,00,0,7
SPIWrite 0533,00,0,7
SPIWrite 0534,00,0,7
SPIWrite 0535,00,0,7
SPIWrite 0536,00,0,7
SPIWrite 0537,00,0,7
SPIWrite 0538,00,0,7
SPIWrite 0539,00,0,7
SPIWrite 053a,00,0,7
SPIWrite 053b,00,0,7
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SPIWrite 0540,00,0,7
SPIWrite 0541,00,0,7
SPIWrite 0542,00,0,7
SPIWrite 0543,00,0,7
SPIWrite 0544,00,0,7
SPIWrite 0545,00,0,7
SPIWrite 0546,00,0,7
SPIWrite 0547,00,0,7
SPIWrite 0548,00,0,7
SPIWrite 0549,00,0,7
SPIWrite 054a,00,0,7
SPIWrite 054b,00,0,7
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SPIWrite 0550,00,0,7
SPIWrite 0551,00,0,7
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SPIWrite 055b,00,0,7
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SPIWrite 055e,00,0,7
SPIWrite 055f,00,0,7
SPIWrite 0560,00,0,7
SPIWrite 0561,00,0,7
SPIWrite 0562,00,0,7
SPIWrite 0563,00,0,7
SPIWrite 0564,00,0,7
SPIWrite 0565,00,0,7
SPIWrite 0566,00,0,7
SPIWrite 0567,00,0,7
SPIWrite 0568,00,0,7
SPIWrite 0569,00,0,7
SPIWrite 056a,00,0,7
SPIWrite 056b,00,0,7
SPIWrite 056c,00,0,7
SPIWrite 056d,00,0,7
SPIWrite 056e,00,0,7
SPIWrite 056f,00,0,7
SPIWrite 0570,00,0,7
SPIWrite 0571,00,0,7
SPIWrite 0572,00,0,7
SPIWrite 0573,00,0,7
SPIWrite 0574,00,0,7
SPIWrite 0575,00,0,7
SPIWrite 0576,00,0,7
SPIWrite 0577,00,0,7
SPIWrite 0578,00,0,7
SPIWrite 0579,00,0,7
SPIWrite 057a,00,0,7
SPIWrite 057b,00,0,7
SPIWrite 057c,00,0,7
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SPIWrite 057f,00,0,7
SPIWrite 0580,00,0,7
SPIWrite 0581,00,0,7
SPIWrite 0582,00,0,7
SPIWrite 0583,00,0,7
SPIWrite 0584,00,0,7
SPIWrite 0585,00,0,7
SPIWrite 0586,00,0,7
SPIWrite 0587,00,0,7
SPIWrite 0588,00,0,7
SPIWrite 0589,00,0,7
SPIWrite 058a,00,0,7
SPIWrite 058b,00,0,7
SPIWrite 058c,00,0,7
SPIWrite 058d,00,0,7
SPIWrite 058e,00,0,7
SPIWrite 058f,00,0,7
SPIWrite 0590,00,0,7
SPIWrite 0591,00,0,7
SPIWrite 0592,00,0,7
SPIWrite 0593,00,0,7
SPIWrite 0594,00,0,7
SPIWrite 0595,00,0,7
SPIWrite 0596,00,0,7
SPIWrite 0597,00,0,7
SPIWrite 0598,00,0,7
SPIWrite 0599,00,0,7
SPIWrite 059a,00,0,7
SPIWrite 059b,00,0,7
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SPIWrite 05a0,00,0,7
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SPIWrite 05aa,00,0,7
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SPIWrite 05ba,00,0,7
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SPIWrite 05ca,00,0,7
SPIWrite 05cb,00,0,7
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SPIWrite 05d9,00,0,7
SPIWrite 05da,00,0,7
SPIWrite 05db,00,0,7
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SPIWrite 05ea,00,0,7
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SPIWrite 05fa,00,0,7
SPIWrite 05fb,00,0,7
SPIWrite 05fc,00,0,7
SPIWrite 05fd,00,0,7
SPIWrite 05fe,00,0,7
SPIWrite 05ff,00,0,7
SPIWrite 0600,00,0,7
SPIWrite 0601,00,0,7
SPIWrite 0602,00,0,7
SPIWrite 0603,00,0,7
SPIWrite 0604,00,0,7
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SPIWrite 060a,00,0,7
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SPIWrite 060f,00,0,7
SPIWrite 0610,00,0,7
SPIWrite 0611,00,0,7
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SPIWrite 061b,00,0,7
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SPIWrite 0620,00,0,7
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SPIWrite 0630,00,0,7
SPIWrite 0631,00,0,7
SPIWrite 0632,00,0,7
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SPIWrite 0635,00,0,7
SPIWrite 0636,00,0,7
SPIWrite 0637,00,0,7
SPIWrite 0638,00,0,7
SPIWrite 0639,00,0,7
SPIWrite 063a,00,0,7
SPIWrite 063b,00,0,7
SPIWrite 063c,00,0,7
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SPIWrite 063e,00,0,7
SPIWrite 063f,00,0,7
SPIWrite 0640,00,0,7
SPIWrite 0641,00,0,7
SPIWrite 0642,00,0,7
SPIWrite 0643,00,0,7
SPIWrite 0644,00,0,7
SPIWrite 0645,00,0,7
SPIWrite 0646,00,0,7
SPIWrite 0647,00,0,7
SPIWrite 0648,00,0,7
SPIWrite 0649,00,0,7
SPIWrite 064a,00,0,7
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SPIWrite 064e,00,0,7
SPIWrite 064f,00,0,7
SPIWrite 0650,00,0,7
SPIWrite 0651,00,0,7
SPIWrite 0652,00,0,7
SPIWrite 0653,00,0,7
SPIWrite 0654,00,0,7
SPIWrite 0655,00,0,7
SPIWrite 0656,00,0,7
SPIWrite 0657,00,0,7
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SPIWrite 0660,00,0,7
SPIWrite 0661,00,0,7
SPIWrite 0662,00,0,7
SPIWrite 0663,00,0,7
SPIWrite 0664,00,0,7
SPIWrite 0665,00,0,7
SPIWrite 0666,00,0,7
SPIWrite 0667,00,0,7
SPIWrite 0668,00,0,7
SPIWrite 0669,00,0,7
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SPIWrite 066f,00,0,7
SPIWrite 0670,00,0,7
SPIWrite 0671,00,0,7
SPIWrite 0672,00,0,7
SPIWrite 0673,00,0,7
SPIWrite 0674,00,0,7
SPIWrite 0675,00,0,7
SPIWrite 0676,00,0,7
SPIWrite 0677,00,0,7
SPIWrite 0678,00,0,7
SPIWrite 0679,00,0,7
SPIWrite 067a,00,0,7
SPIWrite 067b,00,0,7
SPIWrite 067c,00,0,7
SPIWrite 067d,00,0,7
SPIWrite 067e,00,0,7
SPIWrite 067f,00,0,7
SPIWrite 0680,00,0,7
SPIWrite 0681,00,0,7
SPIWrite 0682,00,0,7
SPIWrite 0683,00,0,7
SPIWrite 0684,00,0,7
SPIWrite 0685,00,0,7
SPIWrite 0686,00,0,7
SPIWrite 0687,00,0,7
SPIWrite 0688,00,0,7
SPIWrite 0689,00,0,7
SPIWrite 068a,00,0,7
SPIWrite 068b,00,0,7
SPIWrite 068c,00,0,7
SPIWrite 068d,00,0,7
SPIWrite 068e,00,0,7
SPIWrite 068f,00,0,7
SPIWrite 0690,00,0,7
SPIWrite 0691,00,0,7
SPIWrite 0692,00,0,7
SPIWrite 0693,00,0,7
SPIWrite 0694,00,0,7
SPIWrite 0695,00,0,7
SPIWrite 0696,00,0,7
SPIWrite 0697,00,0,7
SPIWrite 0698,00,0,7
SPIWrite 0699,00,0,7
SPIWrite 069a,00,0,7
SPIWrite 069b,00,0,7
SPIWrite 069c,00,0,7
SPIWrite 069d,00,0,7
SPIWrite 069e,00,0,7
SPIWrite 069f,00,0,7
SPIWrite 06a0,00,0,7
SPIWrite 06a1,00,0,7
SPIWrite 06a2,00,0,7
SPIWrite 06a3,00,0,7
SPIWrite 06a4,00,0,7
SPIWrite 06a5,00,0,7
SPIWrite 06a6,00,0,7
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SPIWrite 06a9,00,0,7
SPIWrite 06aa,00,0,7
SPIWrite 06ab,00,0,7
SPIWrite 06ac,00,0,7
SPIWrite 06ad,00,0,7
SPIWrite 06ae,00,0,7
SPIWrite 06af,00,0,7
SPIWrite 06b0,00,0,7

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SPIWrite 06c3,00,0,7
SPIWrite 06c4,00,0,7
SPIWrite 06c5,00,0,7
SPIWrite 06c6,00,0,7
SPIWrite 06c7,00,0,7
SPIWrite 06c8,00,0,7
SPIWrite 06c9,00,0,7
SPIWrite 06ca,00,0,7
SPIWrite 06cb,00,0,7
SPIWrite 06cc,00,0,7
SPIWrite 06cd,00,0,7
SPIWrite 06ce,00,0,7
SPIWrite 06cf,00,0,7
SPIWrite 06d0,00,0,7
SPIWrite 06d1,00,0,7
SPIWrite 06d2,00,0,7
SPIWrite 06d3,00,0,7
SPIWrite 06d4,00,0,7
SPIWrite 06d5,00,0,7
SPIWrite 06d6,00,0,7
SPIWrite 06d7,00,0,7
SPIWrite 06d8,00,0,7
SPIWrite 06d9,00,0,7
SPIWrite 06da,00,0,7
SPIWrite 06db,00,0,7
SPIWrite 06dc,00,0,7
SPIWrite 06dd,00,0,7
SPIWrite 06de,00,0,7
SPIWrite 06df,00,0,7
SPIWrite 06e0,00,0,7
SPIWrite 06e1,00,0,7
SPIWrite 06e2,00,0,7
SPIWrite 06e3,00,0,7
SPIWrite 06e4,00,0,7
SPIWrite 06e5,00,0,7
SPIWrite 06e6,00,0,7
SPIWrite 06e7,00,0,7
SPIWrite 06e8,00,0,7
SPIWrite 06e9,00,0,7
SPIWrite 06ea,00,0,7
SPIWrite 06eb,00,0,7
SPIWrite 06ec,00,0,7
SPIWrite 06ed,00,0,7
SPIWrite 06ee,00,0,7
SPIWrite 06ef,00,0,7
SPIWrite 06f0,00,0,7
SPIWrite 06f1,00,0,7
SPIWrite 06f2,00,0,7
SPIWrite 06f3,00,0,7
SPIWrite 06f4,00,0,7
SPIWrite 06f5,00,0,7

SPIWrite 06f6,00,0,7
SPIWrite 06f7,00,0,7
SPIWrite 06f8,00,0,7
SPIWrite 06f9,00,0,7
SPIWrite 06fa,00,0,7
SPIWrite 06fb,00,0,7
SPIWrite 06fc,00,0,7
SPIWrite 06fd,00,0,7
SPIWrite 06fe,00,0,7
SPIWrite 06ff,00,0,7
SPIWrite 0700,00,0,7
SPIWrite 0701,00,0,7
SPIWrite 0702,00,0,7
SPIWrite 0703,00,0,7
SPIWrite 0704,00,0,7
SPIWrite 0705,00,0,7
SPIWrite 0706,00,0,7
SPIWrite 0707,00,0,7
SPIWrite 0708,00,0,7
SPIWrite 0709,00,0,7
SPIWrite 070a,00,0,7
SPIWrite 070b,00,0,7
SPIWrite 070c,00,0,7
SPIWrite 070d,00,0,7
SPIWrite 070e,00,0,7
SPIWrite 070f,00,0,7
SPIWrite 0710,00,0,7
SPIWrite 0711,00,0,7
SPIWrite 0712,00,0,7
SPIWrite 0713,00,0,7
SPIWrite 0714,00,0,7
SPIWrite 0715,00,0,7
SPIWrite 0716,00,0,7
SPIWrite 0717,00,0,7
SPIWrite 0718,00,0,7
SPIWrite 0719,00,0,7
SPIWrite 071a,00,0,7
SPIWrite 071b,00,0,7
SPIWrite 071c,00,0,7
SPIWrite 071d,00,0,7
SPIWrite 071e,00,0,7
SPIWrite 071f,00,0,7
SPIWrite 0720,00,0,7
SPIWrite 0721,00,0,7
SPIWrite 0722,00,0,7
SPIWrite 0723,00,0,7
SPIWrite 0724,00,0,7
SPIWrite 0725,00,0,7
SPIWrite 0726,00,0,7
SPIWrite 0727,00,0,7
SPIWrite 0728,00,0,7
SPIWrite 0729,00,0,7
SPIWrite 072a,00,0,7
SPIWrite 072b,00,0,7
SPIWrite 072c,00,0,7
SPIWrite 072d,00,0,7
SPIWrite 072e,00,0,7
SPIWrite 072f,00,0,7
SPIWrite 0730,00,0,7
SPIWrite 0731,00,0,7
SPIWrite 0732,00,0,7
SPIWrite 0733,00,0,7
SPIWrite 0734,00,0,7
SPIWrite 0735,00,0,7
SPIWrite 0736,00,0,7
SPIWrite 0737,00,0,7
SPIWrite 0738,00,0,7
SPIWrite 0739,00,0,7
SPIWrite 073a,00,0,7

SPIWrite 073b,00,0,7
SPIWrite 073c,00,0,7
SPIWrite 073d,00,0,7
SPIWrite 073e,00,0,7
SPIWrite 073f,00,0,7
SPIWrite 0740,00,0,7
SPIWrite 0741,00,0,7
SPIWrite 0742,00,0,7
SPIWrite 0743,00,0,7
SPIWrite 0744,00,0,7
SPIWrite 0745,00,0,7
SPIWrite 0746,00,0,7
SPIWrite 0747,00,0,7
SPIWrite 0748,00,0,7
SPIWrite 0749,00,0,7
SPIWrite 074a,00,0,7
SPIWrite 074b,00,0,7
SPIWrite 074c,00,0,7
SPIWrite 074d,00,0,7
SPIWrite 074e,00,0,7
SPIWrite 074f,00,0,7
SPIWrite 0750,00,0,7
SPIWrite 0751,00,0,7
SPIWrite 0752,00,0,7
SPIWrite 0753,00,0,7
SPIWrite 0754,00,0,7
SPIWrite 0755,00,0,7
SPIWrite 0756,00,0,7
SPIWrite 0757,00,0,7
SPIWrite 0758,00,0,7
SPIWrite 0759,00,0,7
SPIWrite 075a,00,0,7
SPIWrite 075b,00,0,7
SPIWrite 075c,00,0,7
SPIWrite 075d,00,0,7
SPIWrite 075e,00,0,7
SPIWrite 075f,00,0,7
SPIWrite 0760,00,0,7
SPIWrite 0761,00,0,7
SPIWrite 0762,00,0,7
SPIWrite 0763,00,0,7
SPIWrite 0764,00,0,7
SPIWrite 0765,00,0,7
SPIWrite 0766,00,0,7
SPIWrite 0767,00,0,7
SPIWrite 0768,00,0,7
SPIWrite 0769,00,0,7
SPIWrite 076a,00,0,7
SPIWrite 076b,00,0,7
SPIWrite 076c,00,0,7
SPIWrite 076d,00,0,7
SPIWrite 076e,00,0,7
SPIWrite 076f,00,0,7
SPIWrite 0770,00,0,7
SPIWrite 0771,00,0,7
SPIWrite 0772,00,0,7
SPIWrite 0773,00,0,7
SPIWrite 0774,00,0,7
SPIWrite 0775,00,0,7
SPIWrite 0776,00,0,7
SPIWrite 0777,00,0,7
SPIWrite 0778,00,0,7
SPIWrite 0779,00,0,7
SPIWrite 077a,00,0,7
SPIWrite 077b,00,0,7
SPIWrite 077c,00,0,7
SPIWrite 077d,00,0,7
SPIWrite 077e,00,0,7
SPIWrite 077f,00,0,7

SPIWrite 0780,00,0,7
SPIWrite 0781,00,0,7
SPIWrite 0782,00,0,7
SPIWrite 0783,00,0,7
SPIWrite 0784,00,0,7
SPIWrite 0785,00,0,7
SPIWrite 0786,00,0,7
SPIWrite 0787,00,0,7
SPIWrite 0788,00,0,7
SPIWrite 0789,00,0,7
SPIWrite 078a,00,0,7
SPIWrite 078b,00,0,7
SPIWrite 078c,00,0,7
SPIWrite 078d,00,0,7
SPIWrite 078e,00,0,7
SPIWrite 078f,00,0,7
SPIWrite 0790,00,0,7
SPIWrite 0791,00,0,7
SPIWrite 0792,00,0,7
SPIWrite 0793,00,0,7
SPIWrite 0794,00,0,7
SPIWrite 0795,00,0,7
SPIWrite 0796,00,0,7
SPIWrite 0797,00,0,7
SPIWrite 0798,00,0,7
SPIWrite 0799,00,0,7
SPIWrite 079a,00,0,7
SPIWrite 079b,00,0,7
SPIWrite 079c,00,0,7
SPIWrite 079d,00,0,7
SPIWrite 079e,00,0,7
SPIWrite 079f,00,0,7
SPIWrite 07a0,00,0,7
SPIWrite 07a1,00,0,7
SPIWrite 07a2,00,0,7
SPIWrite 07a3,00,0,7
SPIWrite 07a4,00,0,7
SPIWrite 07a5,00,0,7
SPIWrite 07a6,00,0,7
SPIWrite 07a7,00,0,7
SPIWrite 07a8,00,0,7
SPIWrite 07a9,00,0,7
SPIWrite 07aa,00,0,7
SPIWrite 07ab,00,0,7
SPIWrite 07ac,00,0,7
SPIWrite 07ad,00,0,7
SPIWrite 07ae,00,0,7
SPIWrite 07af,00,0,7
SPIWrite 07b0,00,0,7
SPIWrite 07b1,00,0,7
SPIWrite 07b2,00,0,7
SPIWrite 07b3,00,0,7
SPIWrite 07b4,00,0,7
SPIWrite 07b5,00,0,7
SPIWrite 07b6,00,0,7
SPIWrite 07b7,00,0,7
SPIWrite 07b8,00,0,7
SPIWrite 07b9,00,0,7
SPIWrite 07ba,00,0,7
SPIWrite 07bb,00,0,7
SPIWrite 07bc,00,0,7
SPIWrite 07bd,00,0,7
SPIWrite 07be,00,0,7
SPIWrite 07bf,00,0,7
SPIWrite 07c0,00,0,7
SPIWrite 07c1,00,0,7
SPIWrite 07c2,00,0,7
SPIWrite 07c3,00,0,7
SPIWrite 07c4,00,0,7

SPIWrite 07c5,00,0,7
SPIWrite 07c6,00,0,7
SPIWrite 07c7,00,0,7
SPIWrite 07c8,00,0,7
SPIWrite 07c9,00,0,7
SPIWrite 07ca,00,0,7
SPIWrite 07cb,00,0,7
SPIWrite 07cc,00,0,7
SPIWrite 07cd,00,0,7
SPIWrite 07ce,00,0,7
SPIWrite 07cf,00,0,7
SPIWrite 07d0,00,0,7
SPIWrite 07d1,00,0,7
SPIWrite 07d2,00,0,7
SPIWrite 07d3,00,0,7
SPIWrite 07d4,00,0,7
SPIWrite 07d5,00,0,7
SPIWrite 07d6,00,0,7
SPIWrite 07d7,00,0,7
SPIWrite 07d8,00,0,7
SPIWrite 07d9,00,0,7
SPIWrite 07da,00,0,7
SPIWrite 07db,00,0,7
SPIWrite 07dc,00,0,7
SPIWrite 07dd,00,0,7
SPIWrite 07de,00,0,7
SPIWrite 07df,00,0,7
SPIWrite 07e0,00,0,7
SPIWrite 07e1,00,0,7
SPIWrite 07e2,00,0,7
SPIWrite 07e3,00,0,7
SPIWrite 07e4,00,0,7
SPIWrite 07e5,00,0,7
SPIWrite 07e6,00,0,7
SPIWrite 07e7,00,0,7
SPIWrite 07e8,00,0,7
SPIWrite 07e9,00,0,7
SPIWrite 07ea,00,0,7
SPIWrite 07eb,00,0,7
SPIWrite 07ec,00,0,7
SPIWrite 07ed,00,0,7
SPIWrite 07ee,00,0,7
SPIWrite 07ef,00,0,7
SPIWrite 07f0,00,0,7
SPIWrite 07f1,00,0,7
SPIWrite 07f2,00,0,7
SPIWrite 07f3,00,0,7
SPIWrite 07f4,00,0,7
SPIWrite 07f5,00,0,7
SPIWrite 07f6,00,0,7
SPIWrite 07f7,00,0,7
SPIWrite 07f8,00,0,7
SPIWrite 07f9,00,0,7
SPIWrite 07fa,00,0,7
SPIWrite 07fb,00,0,7
SPIWrite 07fc,00,0,7
SPIWrite 07fd,00,0,7
SPIWrite 07fe,00,0,7
SPIWrite 07ff,00,0,7
SPIWrite 0800,00,0,7
SPIWrite 0801,00,0,7
SPIWrite 0802,00,0,7
SPIWrite 0803,00,0,7
SPIWrite 0804,00,0,7
SPIWrite 0805,00,0,7
SPIWrite 0806,00,0,7
SPIWrite 0807,00,0,7
SPIWrite 0808,00,0,7
SPIWrite 0809,00,0,7

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SPIWrite 080a,00,0,7
SPIWrite 080b,00,0,7
SPIWrite 080c,00,0,7
SPIWrite 080d,00,0,7
SPIWrite 080e,00,0,7
SPIWrite 080f,00,0,7
SPIWrite 0810,00,0,7
SPIWrite 0811,00,0,7
SPIWrite 0812,00,0,7
SPIWrite 0813,00,0,7
SPIWrite 0814,00,0,7
SPIWrite 0815,00,0,7
SPIWrite 0816,00,0,7
SPIWrite 0817,00,0,7
SPIWrite 0818,00,0,7
SPIWrite 0819,00,0,7
SPIWrite 081a,00,0,7
SPIWrite 081b,00,0,7
SPIWrite 081c,00,0,7
SPIWrite 081d,00,0,7
SPIWrite 081e,00,0,7
SPIWrite 081f,00,0,7
SPIWrite 0012,00,4,4 //PAGE: sys_calib_macro_memory=0x0;(Meaning: ); Address(0x12[4:4],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1;(Meaning: ); Address(0x13[4:4],)
SPIWrite 00a3,01,0,7
SPIWrite 00a2,38,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,10,0,7
SPIWrite 00a4,00,0,7

SPIPoll 00f0,0,0,1

SPIWrite 0193,81,0,7

\\Read macro_opcode_operand=0x81000000; Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0;(Meaning: ); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address(0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address(0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address(0x20[6:6],)

\\Read MACRO_ERROR_IN_EXECUTION=0x0; Address(0x20[7:7],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1;(Meaning: ); Address(0x13[4:4],)
SPIWrite 00a3,11,0,7
SPIWrite 00a2,38,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,01,0,7

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SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,00,0,7

SPIPoll 00f0,0,0,1

SPIWrite 0193,81,0,7

\\Read  macro_opcode_operand=0x81000000;    Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;  Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;  Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;    Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;    Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;  Address(0x20[7:7],)

\\END: Completed downloading TX IQMC Steady State Patch

SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;  Address(0x13[5:5],)
SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,80,0,7
SPIWrite 0193,31,0,7

\\Read  macro_opcode_operand=0x31000000;    Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;  Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;  Address(0x20[4:4],)
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\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;      Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;      Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;      Address(0x20[7:7],)

SPIWrite 0013,00,5,5      //PAGE: cm4_macro=0x0;  Address(0x13[5:5],)
SPIWrite 0013,10,4,4      //PAGE: customer_macro=0x1;(Meaning: );      Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,04,0,7
SPIWrite 0193,31,0,7

\\Read  macro_opcode_operand=0x31000000;      Address(0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4      //PAGE: customer_macro=0x0;(Meaning: );      Address(0x13[4:4],)
SPIWrite 0013,20,5,5      //PAGE: cm4_macro=0x1;  Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;      Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;      Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;      Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;      Address(0x20[7:7],)

SPIWrite 0013,00,5,5      //PAGE: cm4_macro=0x0;  Address(0x13[5:5],)
SPIWrite 0013,10,4,4      //PAGE: customer_macro=0x1;(Meaning: );      Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,10,0,7
SPIWrite 0193,31,0,7

\\Read  macro_opcode_operand=0x31000000;      Address(0x190[31:0],)
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SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0; (Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;    Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;    Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;    Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;    Address(0x20[7:7],)

SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)
SPIWrite 0016,08,3,3    //PAGE: tx_iqmc=0x1;    Address(0x16[3:3],)

\\Read  STAT_PATCH_VALIDITY=0x1; (Meaning: );    Address(0x49c[2:0],)

SPIWrite 0016,00,3,3    //PAGE: tx_iqmc=0x0;    Address(0x16[3:3],)
SPIWrite 0015,80,7,7    //PAGE: DIGTOP_MISC=0x1; (Meaning: );    Address(0x15[7:7],)
SPIWrite 06e4,02,0,2    //txiqmc_cm4_mem_addr_high=0x2;    Address(0x6e4[2:0],)
SPIWrite 0015,00,7,7    //PAGE: DIGTOP_MISC=0x0; (Meaning: );    Address(0x15[7:7],)
SPIWrite 0016,01,0,0    //PAGE: tx_iqmc_dram=0x1;    Address(0x16[0:0],)
SPIWrite 19ba,c4,0,7
SPIWrite 19bb,40,0,7
SPIWrite 0016,00,0,0    //PAGE: tx_iqmc_dram=0x0;    Address(0x16[0:0],)
SPIWrite 0015,80,7,7    //PAGE: DIGTOP_MISC=0x1; (Meaning: );    Address(0x15[7:7],)
SPIWrite 06e4,02,0,2    //txiqmc_cm4_mem_addr_high=0x2;    Address(0x6e4[2:0],)
SPIWrite 0015,00,7,7    //PAGE: DIGTOP_MISC=0x0; (Meaning: );    Address(0x15[7:7],)
SPIWrite 0016,01,0,0    //PAGE: tx_iqmc_dram=0x1;    Address(0x16[0:0],)
SPIWrite 19ab,02,0,7

\\END: Done initializing TXIQMC

SPIWrite 0016,00,0,7    //PAGE: tx_iqmc_dram=0x0;    Address(0x16[0:0],)
\\ STEP: calibration/step1

\\START: Sending Sysref to device from Pin/SPI

\\START: Requesting/releasing SPI Access to PLL Pages

SPIWrite 0015,80,0,7    //PAGE: DIGTOP_MISC=0x1; (Meaning: );    Address(0x15[7:7],)
SPIWrite 01d4,01,0,7    //pll_reg_spi_req_a=0x1;    Address(0x1d4[0:0],)
SPIWrite 0374,00,0,7    //pll_reg_spi_req_b1=0x0; (Meaning: );    Address(0x374[0:0],)

SPIPoll 01d5,0,0,1

\\Read  pll_reg_spi_a_ack=0x1;    Address(0x1d4[8:8],)

\\END: Done requesting/releasing SPI Access to PLL Pages

SPIWrite 0015,00,0,7    //PAGE: DIGTOP_MISC=0x0; (Meaning: );    Address(0x15[7:7],)
SPIWrite 0014,01,0,7    //PAGE: clktop_2t=0x1;    Address(0x14[1:0],)
SPIWrite 002c,00,0,7    //TRIM_100N_INT_TXCMLLDVREF1=0x0;    Address(0x2c[7:5],)

WAIT 0.001

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SPIWrite 0014,00,0,7 //PAGE: clktop_2t=0x0; Address(0x14[1:0],)
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 04d1,02,0,7 //count_len_sysref=0x2ff; Address(0x4d0[15:0],)
SPIWrite 04d0,ff,0,7
SPIWrite 0717,00,0,7 //spare_reg_port=0x0; Address(0x714[31:0],)
SPIWrite 0716,00,0,7
SPIWrite 0715,00,0,7
SPIWrite 0714,00,0,7
SPIWrite 0717,00,0,7 //spare_reg_port=0x101; Address(0x714[31:0],)
SPIWrite 0716,00,0,7
SPIWrite 0715,01,0,7
SPIWrite 0714,01,0,7
SPIWrite 0717,00,0,7 //spare_reg_port=0x0; Address(0x714[31:0],)
SPIWrite 0716,00,0,7
SPIWrite 0715,00,0,7
SPIWrite 0714,00,0,7
SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0014,08,0,7 //PAGE: pll=0x1; Address(0x14[7:3],)
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 0051,8e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x1; Address(0x50[15:15],)
SPIWrite 0051,8e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)
SPIWrite 004e,34,0,7 //lcmgen_usespisysref=0x1; Address(0x4c[18:18],)
SPIWrite 0014,08,0,7 //PAGE: pll=0x1; Address(0x14[7:3],)
SPIWrite 004e,30,0,7 //lcmgen_div=0x2ff; Address(0x4c[17:2],)
SPIWrite 004d,0b,0,7
SPIWrite 004c,fc,0,7
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 004c,fd,0,7 //lcmgen_sync_ena=0x1; Address(0x4c[0:0],)
SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 0051,8c,0,7 //SYS_REF_FROM_PIN_BYPAS=0x1; Address(0x50[15:15],)
SPIWrite 0051,8c,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 004e,34,0,7 //lcmgen_usespisysref=0x1; Address(0x4c[18:18],)
SPIWrite 004e,34,0,7 //lcmgen_div=0x2ff; Address(0x4c[17:2],)
SPIWrite 004d,0b,0,7
SPIWrite 004c,fc,0,7
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 004c,fd,0,7 //lcmgen_sync_ena=0x1; Address(0x4c[0:0],)
SPIWrite 0014,20,0,7 //PAGE: pll=0x4; Address(0x14[7:3],)
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 0051,8e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x1; Address(0x50[15:15],)
SPIWrite 0051,8e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)
SPIWrite 004e,34,0,7 //lcmgen_usespisysref=0x1; Address(0x4c[18:18],)
SPIWrite 0014,20,0,7 //PAGE: pll=0x4; Address(0x14[7:3],)
SPIWrite 004e,30,0,7 //lcmgen_div=0x2ff; Address(0x4c[17:2],)
SPIWrite 004d,0b,0,7
SPIWrite 004c,fc,0,7
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 004c,fd,0,7 //lcmgen_sync_ena=0x1; Address(0x4c[0:0],)
SPIWrite 0014,40,0,7 //PAGE: pll=0x8; Address(0x14[7:3],)
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 0051,8e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x1; Address(0x50[15:15],)
SPIWrite 0051,8e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)
SPIWrite 004e,34,0,7 //lcmgen_usespisysref=0x1; Address(0x4c[18:18],)
SPIWrite 0014,40,0,7 //PAGE: pll=0x8; Address(0x14[7:3],)
SPIWrite 004e,30,0,7 //lcmgen_div=0x2ff; Address(0x4c[17:2],)
SPIWrite 004d,0b,0,7
SPIWrite 004c,fc,0,7
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 004c,fd,0,7 //lcmgen_sync_ena=0x1; Address(0x4c[0:0],)
SPIWrite 0014,80,0,7 //PAGE: pll=0x10; Address(0x14[7:3],)
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 0051,8e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x1; Address(0x50[15:15],)
SPIWrite 0051,8e,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x1; Address(0x50[11:11],)
SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)

```

```
SPIWrite 004e,34,0,7 //lcmgen_usespisysref=0x1; Address(0x4c[18:18],)
SPIWrite 0014,80,0,7 //PAGE: pll=0x10; Address(0x14[7:3],)
SPIWrite 004e,30,0,7 //lcmgen_div=0x2ff; Address(0x4c[17:2],)
SPIWrite 004d,0b,0,7
SPIWrite 004c,fc,0,7
SPIWrite 004c,fc,0,7 //lcmgen_sync_ena=0x0; Address(0x4c[0:0],)
SPIWrite 004c,fd,0,7 //lcmgen_sync_ena=0x1; Address(0x4c[0:0],)
```

WAIT 0.001

```
SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)
SPIWrite 004e,34,0,7 //lcmgen_spisysref=0x0; Address(0x4c[19:19],)
SPIWrite 004e,3c,0,7 //lcmgen_spisysref=0x1; Address(0x4c[19:19],)
SPIWrite 004e,34,0,7 //lcmgen_spisysref=0x0; Address(0x4c[19:19],)
SPIWrite 0014,08,0,7 //PAGE: pll=0x1; Address(0x14[7:3],)
SPIWrite 0051,0e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x0; Address(0x50[15:15],)
SPIWrite 0051,06,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x0; Address(0x50[11:11],)
SPIWrite 0014,10,0,7 //PAGE: pll=0x2; Address(0x14[7:3],)
SPIWrite 0051,0c,0,7 //SYS_REF_FROM_PIN_BYPAS=0x0; Address(0x50[15:15],)
SPIWrite 0051,04,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x0; Address(0x50[11:11],)
SPIWrite 0014,20,0,7 //PAGE: pll=0x4; Address(0x14[7:3],)
SPIWrite 0051,0e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x0; Address(0x50[15:15],)
SPIWrite 0051,06,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x0; Address(0x50[11:11],)
SPIWrite 0014,40,0,7 //PAGE: pll=0x8; Address(0x14[7:3],)
SPIWrite 0051,0e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x0; Address(0x50[15:15],)
SPIWrite 0051,06,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x0; Address(0x50[11:11],)
SPIWrite 0014,80,0,7 //PAGE: pll=0x10; Address(0x14[7:3],)
SPIWrite 0051,0e,0,7 //SYS_REF_FROM_PIN_BYPAS=0x0; Address(0x50[15:15],)
SPIWrite 0051,06,0,7 //EN_SYNC_TO_PLLDIG_DIV=0x0; Address(0x50[11:11],)
SPIWrite 0014,00,0,7 //PAGE: pll=0x0; Address(0x14[7:3],)
SPIWrite 0014,01,0,7 //PAGE: clktop_2t=0x1; Address(0x14[1:0],)
SPIWrite 002c,00,0,7 //TRIM_100N_INT_TXCMLLDVOREF1=0x0; Address(0x2c[7:5],)
```

\\START: Requesting/releasing SPI Access to PLL Pages

```
SPIWrite 0014,00,0,7 //PAGE: clktop_2t=0x0; Address(0x14[1:0],)
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning: ); Address(0x15[7:7],)
SPIWrite 01d4,00,0,7 //pll_reg_spi_req_a=0x0; Address(0x1d4[0:0],)
SPIWrite 0374,00,0,7 //pll_reg_spi_req_b1=0x0; (Meaning: ); Address(0x374[0:0],)
```

WAIT 0.01

\\END: Done requesting/releasing SPI Access to PLL Pages

\\END: Done sending Sysref to device from Pin/SPI

\\START: Staggered TDD Toggle

```
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0124,01,0,7 //use_reg_txon_AB=0x1; Address(0x124[0:0],)
SPIWrite 0126,01,0,7 //use_reg_rxon_AB=0x1; Address(0x124[16:16],)
SPIWrite 0128,01,0,7 //use_reg_fbon_AB=0x1; Address(0x128[0:0],)
SPIWrite 012a,01,0,7 //use_reg_txon_CD=0x1; Address(0x128[16:16],)
SPIWrite 012c,01,0,7 //use_reg_rxon_CD=0x1; Address(0x12c[0:0],)
SPIWrite 012e,01,0,7 //use_reg_fbon_CD=0x1; Address(0x12c[16:16],)
SPIWrite 0127,00,0,7 //reg_for_rxon_AB=0x0; Address(0x124[24:24],)
SPIWrite 012d,00,0,7 //reg_for_rxon_CD=0x0; Address(0x12c[8:8],)
SPIWrite 0127,01,0,7 //reg_for_rxon_AB=0x1; Address(0x124[24:24],)
SPIWrite 012d,01,0,7 //reg_for_rxon_CD=0x1; Address(0x12c[8:8],)
SPIWrite 0127,00,0,7 //reg_for_rxon_AB=0x0; Address(0x124[24:24],)
SPIWrite 012d,00,0,7 //reg_for_rxon_CD=0x0; Address(0x12c[8:8],)
SPIWrite 0125,00,0,7 //reg_for_txon_AB=0x0; Address(0x124[8:8],)
SPIWrite 012b,00,0,7 //reg_for_txon_CD=0x0; Address(0x128[24:24],)
SPIWrite 0129,00,0,7 //reg_for_fbon_AB=0x0; Address(0x128[8:8],)
SPIWrite 012f,00,0,7 //reg_for_fbon_CD=0x0; Address(0x12c[24:24],)
```

```
SPIWrite 0125,01,0,7 //reg_for_txon_AB=0x1; Address(0x124[8:8],)
SPIWrite 012b,01,0,7 //reg_for_txon_CD=0x1; Address(0x128[24:24],)
SPIWrite 0129,01,0,7 //reg_for_fbon_AB=0x1; Address(0x128[8:8],)
SPIWrite 012f,01,0,7 //reg_for_fbon_CD=0x1; Address(0x12c[24:24],)
```

\\END: Done Staggered TDD Toggle

```
SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
\\ STEP: calibration/step2
```

\\START: Enabling RX DC correction

```
SPIWrite 0010,0c,0,7 //PAGE: rx_top=0x3; Address(0x10[3:2],)
SPIWrite 0401,01,0,7 //dc_freeze_reg=0x1; (Meaning: ); Address(0x400[8:8],)
SPIWrite 0420,00,0,7 //alpha_update_signal=0x0; Address(0x420[0:0],)
SPIWrite 0420,01,0,7 //alpha_update_signal=0x1; Address(0x420[0:0],)
SPIWrite 0420,00,0,7 //alpha_update_signal=0x0; Address(0x420[0:0],)
SPIWrite 0401,00,0,7 //dc_freeze_reg=0x0; (Meaning: ); Address(0x400[8:8],)
SPIWrite 0400,00,0,7 //bypass=0x0; (Meaning: ); Address(0x400[0:0],)
SPIWrite 0411,01,0,7 //dc_freeze_reg=0x1; (Meaning: ); Address(0x410[8:8],)
SPIWrite 0421,00,0,7 //alpha_update_signal=0x0; Address(0x420[8:8],)
SPIWrite 0421,01,0,7 //alpha_update_signal=0x1; Address(0x420[8:8],)
SPIWrite 0421,00,0,7 //alpha_update_signal=0x0; Address(0x420[8:8],)
SPIWrite 0411,00,0,7 //dc_freeze_reg=0x0; (Meaning: ); Address(0x410[8:8],)
SPIWrite 0410,00,0,7 //bypass=0x0; (Meaning: ); Address(0x410[0:0],)
SPIWrite 0801,01,0,7 //dc_freeze_reg=0x1; (Meaning: ); Address(0x800[8:8],)
SPIWrite 0820,00,0,7 //alpha_update_signal=0x0; Address(0x820[0:0],)
SPIWrite 0820,01,0,7 //alpha_update_signal=0x1; Address(0x820[0:0],)
SPIWrite 0820,00,0,7 //alpha_update_signal=0x0; Address(0x820[0:0],)
SPIWrite 0801,00,0,7 //dc_freeze_reg=0x0; (Meaning: ); Address(0x800[8:8],)
SPIWrite 0800,00,0,7 //bypass=0x0; (Meaning: ); Address(0x800[0:0],)
SPIWrite 0811,01,0,7 //dc_freeze_reg=0x1; (Meaning: ); Address(0x810[8:8],)
SPIWrite 0821,00,0,7 //alpha_update_signal=0x0; Address(0x820[8:8],)
SPIWrite 0821,01,0,7 //alpha_update_signal=0x1; Address(0x820[8:8],)
SPIWrite 0821,00,0,7 //alpha_update_signal=0x0; Address(0x820[8:8],)
SPIWrite 0811,00,0,7 //dc_freeze_reg=0x0; (Meaning: ); Address(0x810[8:8],)
SPIWrite 0810,00,0,7 //bypass=0x0; (Meaning: ); Address(0x810[0:0],)
```

\\END: Done enabling RX DC correction

```
SPIWrite 0010,00,0,7 //PAGE: rx_top=0x0; Address(0x10[3:2],)
\\ STEP: gpioConfig/step0
```

\\Removing the force on Sync Pin.

```
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0555,fd,0,7 //ovr_sel_intpo_dac_sync_n_ab_0=0x0; Address(0x554[9:9],)
SPIWrite 0555,f5,0,7 //ovr_sel_intpo_dac_sync_n_ab_1=0x0; Address(0x554[11:11],)
SPIWrite 0555,d5,0,7 //ovr_sel_intpo_dac_sync_n_cd_0=0x0; Address(0x554[13:13],)
SPIWrite 0555,55,0,7 //ovr_sel_intpo_dac_sync_n_cd_1=0x0; Address(0x554[15:15],)
```

\\START: Configuring GPIO

```
SPIWrite 0538,05,0,7 //preferred_input_sel_gpio_78=0x1; Address(0x538[4:2],)
SPIWrite 0538,05,0,7 //buf_dir_ctrl_gpio_78=0x1; Address(0x538[1:0],)
SPIWrite 0550,2a,0,7 //ovr_sel_intpi_tdd_2t_en_cd=0x0; Address(0x550[7:7],)
SPIWrite 0450,05,0,7 //preferred_input_sel_gpio_20=0x1; Address(0x450[4:2],)
SPIWrite 0450,05,0,7 //buf_dir_ctrl_gpio_20=0x1; Address(0x450[1:0],)
SPIWrite 0552,22,0,7 //ovr_sel_intpi_fb_nco_sw=0x0; Address(0x550[19:19],)
SPIWrite 0500,21,0,7 //preferred_output_sel_gpio_64=0x1; Address(0x500[7:5],)
SPIWrite 0500,22,0,7 //buf_dir_ctrl_gpio_64=0x2; Address(0x500[1:0],)
SPIWrite 0592,00,0,7 //ovr_sel_intpo_rxd_ext_lnbypass=0x0; Address(0x590[23:23],)
SPIWrite 0510,21,0,7 //preferred_output_sel_gpio_68=0x1; Address(0x510[7:5],)
SPIWrite 0510,22,0,7 //buf_dir_ctrl_gpio_68=0x2; Address(0x510[1:0],)
SPIWrite 0592,00,0,7 //ovr_sel_intpo_rxc_ext_lnbypass=0x0; Address(0x590[21:21],)
SPIWrite 0458,21,0,7 //preferred_output_sel_gpio_22=0x1; Address(0x458[7:5],)
SPIWrite 0458,22,0,7 //buf_dir_ctrl_gpio_22=0x2; Address(0x458[1:0],)
SPIWrite 0571,00,0,7 //ovr_sel_intpo_alarm_2=0x0; Address(0x570[13:13],)
SPIWrite 0454,05,0,7 //preferred_input_sel_gpio_21=0x1; Address(0x454[4:2],)
```

```

SPIWrite 0454,05,0,7 //buf_dir_ctrl_gpio_21=0x1; Address(0x454[1:0],)
SPIWrite 056d,02,0,7 //ovr_sel_intpi_global_pdn=0x0; Address(0x56c[11:11],)
SPIWrite 0410,05,0,7 //preferred_input_sel_gpio_4=0x1; Address(0x410[4:2],)
SPIWrite 0410,05,0,7 //buf_dir_ctrl_gpio_4=0x1; Address(0x410[1:0],)
SPIWrite 057e,a2,0,7 //ovr_sel_intpi_tx_fb_loop_1=0x0; Address(0x57c[19:19],)
SPIWrite 0420,05,0,7 //preferred_input_sel_gpio_8=0x1; Address(0x420[4:2],)
SPIWrite 0420,05,0,7 //buf_dir_ctrl_gpio_8=0x1; Address(0x420[1:0],)
SPIWrite 0551,00,0,7 //ovr_sel_intpi_tdd_1f_en_ab=0x0; Address(0x550[9:9],)
SPIWrite 0468,05,0,7 //preferred_input_sel_gpio_26=0x1; Address(0x468[4:2],)
SPIWrite 0468,05,0,7 //buf_dir_ctrl_gpio_26=0x1; Address(0x468[1:0],)
SPIWrite 0550,22,0,7 //ovr_sel_intpi_tdd_2r_en_cd=0x0; Address(0x550[3:3],)
SPIWrite 0414,05,0,7 //preferred_input_sel_gpio_5=0x1; Address(0x414[4:2],)
SPIWrite 0414,05,0,7 //buf_dir_ctrl_gpio_5=0x1; Address(0x414[1:0],)
SPIWrite 057e,82,0,7 //ovr_sel_intpi_tx_fb_loop_2=0x0; Address(0x57c[21:21],)
SPIWrite 040c,05,0,7 //preferred_input_sel_gpio_3=0x1; Address(0x40c[4:2],)
SPIWrite 040c,05,0,7 //buf_dir_ctrl_gpio_3=0x1; Address(0x40c[1:0],)
SPIWrite 0550,02,0,7 //ovr_sel_intpi_tdd_2t_en_ab=0x0; Address(0x550[5:5],)
SPIWrite 0404,05,0,7 //preferred_input_sel_gpio_1=0x1; Address(0x404[4:2],)
SPIWrite 0404,05,0,7 //buf_dir_ctrl_gpio_1=0x1; Address(0x404[1:0],)
SPIWrite 057e,02,0,7 //ovr_sel_intpi_tx_fb_loop_3=0x0; Address(0x57c[23:23],)
SPIWrite 0444,05,0,7 //preferred_input_sel_gpio_17=0x1; Address(0x444[4:2],)
SPIWrite 0444,05,0,7 //buf_dir_ctrl_gpio_17=0x1; Address(0x444[1:0],)
SPIWrite 05b0,23,0,7 //ovr_sel_intpi_spib1_clk=0x0; Address(0x5b0[3:3],)
SPIWrite 046c,21,0,7 //preferred_output_sel_gpio_27=0x1; Address(0x46c[7:5],)
SPIWrite 046c,22,0,7 //buf_dir_ctrl_gpio_27=0x2; Address(0x46c[1:0],)
SPIWrite 0571,00,0,7 //ovr_sel_intpo_alarm_1=0x0; Address(0x570[11:11],)
SPIWrite 0520,22,0,7 //preferred_output_sel_gpio_72=0x1; Address(0x520[7:5],)
SPIWrite 0520,22,0,7 //buf_dir_ctrl_gpio_72=0x2; Address(0x520[1:0],)
SPIWrite 0555,55,0,7 //ovr_sel_intpo_dac_sync_n_cd_0=0x0; Address(0x554[13:13],)
SPIWrite 04c0,05,0,7 //preferred_input_sel_gpio_48=0x1; Address(0x4c0[4:2],)
SPIWrite 04c0,05,0,7 //buf_dir_ctrl_gpio_48=0x1; Address(0x4c0[1:0],)
SPIWrite 0554,fd,0,7 //ovr_sel_intpi_adc_sync_n_ab_0=0x0; Address(0x554[1:1],)
SPIWrite 0530,05,0,7 //preferred_input_sel_gpio_76=0x1; Address(0x530[4:2],)
SPIWrite 0530,05,0,7 //buf_dir_ctrl_gpio_76=0x1; Address(0x530[1:0],)
SPIWrite 0554,dd,0,7 //ovr_sel_intpi_adc_sync_n_cd_0=0x0; Address(0x554[5:5],)
SPIWrite 0448,05,0,7 //preferred_input_sel_gpio_18=0x1; Address(0x448[4:2],)
SPIWrite 0448,05,0,7 //buf_dir_ctrl_gpio_18=0x1; Address(0x448[1:0],)
SPIWrite 057e,00,0,7 //ovr_sel_intpi_tx_fb_loop_0=0x0; Address(0x57c[17:17],)
SPIWrite 04cc,05,0,7 //preferred_input_sel_gpio_51=0x1; Address(0x4cc[4:2],)
SPIWrite 04cc,05,0,7 //buf_dir_ctrl_gpio_51=0x1; Address(0x4cc[1:0],)
SPIWrite 0550,00,0,7 //ovr_sel_intpi_tdd_2r_en_ab=0x0; Address(0x550[1:1],)
SPIWrite 04b8,21,0,7 //preferred_output_sel_gpio_46=0x1; Address(0x4b8[7:5],)
SPIWrite 04b8,22,0,7 //buf_dir_ctrl_gpio_46=0x2; Address(0x4b8[1:0],)
SPIWrite 0592,00,0,7 //ovr_sel_intpo_rxb_ext_lnbypass=0x0; Address(0x590[19:19],)
SPIWrite 04a8,21,0,7 //preferred_output_sel_gpio_42=0x1; Address(0x4a8[7:5],)
SPIWrite 04a8,22,0,7 //buf_dir_ctrl_gpio_42=0x2; Address(0x4a8[1:0],)
SPIWrite 0592,00,0,7 //ovr_sel_intpo_rxa_ext_lnbypass=0x0; Address(0x590[17:17],)
SPIWrite 0430,05,0,7 //preferred_input_sel_gpio_12=0x1; Address(0x430[4:2],)
SPIWrite 0430,05,0,7 //buf_dir_ctrl_gpio_12=0x1; Address(0x430[1:0],)
SPIWrite 05b0,03,0,7 //ovr_sel_intbipi_spib1_sdi=0x0; Address(0x5b0[5:5],)
SPIWrite 0438,05,0,7 //preferred_input_sel_gpio_14=0x1; Address(0x438[4:2],)
SPIWrite 0438,05,0,7 //buf_dir_ctrl_gpio_14=0x1; Address(0x438[1:0],)
SPIWrite 05b0,01,0,7 //ovr_sel_intpi_spib1_cs_n=0x0; Address(0x5b0[1:1],)
SPIWrite 042c,21,0,7 //preferred_output_sel_gpio_11=0x1; Address(0x42c[7:5],)
SPIWrite 042c,22,0,7 //buf_dir_ctrl_gpio_11=0x2; Address(0x42c[1:0],)
SPIWrite 05b0,01,0,7 //ovr_sel_intpo_spib1_sdo=0x0; Address(0x5b0[7:7],)
SPIWrite 04bc,22,0,7 //preferred_output_sel_gpio_47=0x1; Address(0x4bc[7:5],)
SPIWrite 04bc,22,0,7 //buf_dir_ctrl_gpio_47=0x2; Address(0x4bc[1:0],)
SPIWrite 0555,55,0,7 //ovr_sel_intpo_dac_sync_n_ab_0=0x0; Address(0x554[9:9],)

```

\\END: Done Configuring GPIO

```

SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0114,01,0,7 //fb_ncosel_pin_valid_cha=0x1; Address(0x114[0:0],)
SPIWrite 0115,01,0,7 //fb_ncosel_pin_valid_chc=0x1; Address(0x114[8:8],)
SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0016,40,0,7 //PAGE: fb_top=0x1; Address(0x16[7:6],)
SPIWrite 0140,03,0,7 //FBNCOModeSel=0x3; (Meaning: ); Address(0x140[1:0],)

```

```
SPIWrite 0016,80,0,7    //PAGE: fb_top=0x2;      Address(0x16[7:6],)
SPIWrite 0140,03,0,7    //FBNCOModeSel=0x3;(Meaning: );      Address(0x140[1:0],)
```

\\START: Configuring Interrupt Pins

```
SPIWrite 0016,00,0,7    //PAGE: fb_top=0x0;      Address(0x16[7:6],)
SPIWrite 0015,80,0,7    //PAGE: DIGTOP_MISC=0x1;(Meaning: );      Address(0x15[7:7],)
SPIWrite 0119,1f,0,7    //alarm_mask_lsb_for_alarm0=0x1f1f;      Address(0x118[15:0],)
SPIWrite 0118,1f,0,7
SPIWrite 011b,00,0,7    //alarm_mask_msb_for_alarm0=0x0;      Address(0x118[31:16],)
SPIWrite 011a,00,0,7
SPIWrite 011d,00,0,7    //alarm_mask_lsb_for_alarm1=0x20;      Address(0x11c[15:0],)
SPIWrite 011c,20,0,7
SPIWrite 011f,00,0,7    //alarm_mask_msb_for_alarm1=0x0;      Address(0x11c[31:16],)
SPIWrite 011e,00,0,7
```

\\END: Done configuring Interrupt Pins

```
SPIWrite 0015,00,0,7    //PAGE: DIGTOP_MISC=0x0;(Meaning: );      Address(0x15[7:7],)
\\ STEP: rxIqMc/step0
```

\\START: Doing RX IQMC Correction

\\START: Doing RX IQMC Correction Configuration

```
SPIWrite 0018,10,0,7    //PAGE: rx_iqmc=0x1;      Address(0x18[4:4],)
SPIWrite 0348,04,0,7
SPIWrite 0348,44,0,7
SPIWrite 0018,00,4,4    //PAGE: rx_iqmc=0x0;      Address(0x18[4:4],)
SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1;(Meaning: );      Address(0x13[4:4],)
```

```
SPIPoll 00f0,0,0,1
```

```
SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,02,0,7
SPIWrite 0193,01,0,7
```

```
\\Read  macro_opcode_operand=0x1000000;      Address(0x190[31:0],)
```

```
SPIPoll 00f0,0,7,7
```

```
SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0;(Meaning: );      Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;      Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00
```

```
\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)
```

```
\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)
```

```
\\Read  MACRO_ERROR_IN_OPCODE=0x0;      Address(0x20[4:4],)
```

```
\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;      Address(0x20[5:5],)
```

```
\\Read  MACRO_ERROR_IN_OPERAND=0x0;      Address(0x20[6:6],)
```

```
\\Read  MACRO_ERROR_IN_EXECUTION=0x0;      Address(0x20[7:7],)
```

```
\\Read  MACRO_STATUS_RESULT_0=0x11119e;      Address(0x34[31:0],)
```



```
SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;   Address(0x13[5:5],)
SPIWrite 0018,10,4,4    //PAGE: rx_iqmc=0x1;    Address(0x18[4:4],)
SPIWrite 036c,08,3,3
SPIWrite 036c,0a,0,2
SPIWrite 0381,00,7,7
SPIWrite 0305,00,0,0
SPIWrite 0305,02,1,1
SPIWrite 0341,00,0,3
SPIWrite 0340,04,0,7
SPIWrite 0343,40,6,6
SPIWrite 0334,03,0,6
SPIWrite 0334,83,7,7
SPIWrite 037e,7f,0,6
SPIWrite 037e,ff,7,7
SPIWrite 0335,1c,0,6
SPIWrite 0335,9c,7,7
SPIWrite 0320,14,0,6
SPIWrite 0320,94,7,7
SPIWrite 0321,14,0,6
SPIWrite 0321,94,7,7
SPIWrite 0324,04,0,2
SPIWrite 0324,0c,3,3
SPIWrite 0324,4c,4,6
SPIWrite 0324,cc,7,7
SPIWrite 0342,00,0,7
SPIWrite 0341,c0,4,7
SPIWrite 0343,c0,7,7
SPIWrite 0327,02,0,6
SPIWrite 0327,82,7,7
SPIWrite 0322,02,0,6
SPIWrite 0322,82,7,7
SPIWrite 0318,08,0,6
SPIWrite 0318,88,7,7
SPIWrite 0348,44,2,2
SPIWrite 0348,44,6,6
SPIWrite 031c,00,0,5
SPIWrite 031c,80,7,7
SPIWrite 030f,03,0,5
SPIWrite 030f,83,7,7
SPIWrite 0350,00,7,7
SPIWrite 034f,00,7,7
SPIWrite 0359,3c,2,6
SPIWrite 035a,01,0,6
SPIWrite 035b,02,0,6
SPIWrite 0359,bc,7,7
SPIWrite 035a,81,7,7
SPIWrite 035b,82,7,7
```

\\END: Done configuring RX IQMC Correction

\\END: Did RX IQMC Correction

```
SPIWrite 0018,00,0,7    //PAGE: rx_iqmc=0x0;    Address(0x18[4:4],)
\\ STEP: txIqMc/step0
SPIWrite 0014,04,2,2    //PAGE: TIMING_CON=0x1;(Meaning: );    Address(0x14[2:2],)
SPIWrite 0108,01,0,0    //en_bypass_fb_mux_sel=0x1;    Address(0x108[0:0],)
SPIWrite 0109,04,0,3    //reg_bypass_fb_mux_sel=0x4;    Address(0x108[11:8],)
SPIWrite 0014,00,2,2    //PAGE: TIMING_CON=0x0;(Meaning: );    Address(0x14[2:2],)
SPIWrite 0012,10,4,4    //PAGE: sys_calib_macro_memory=0x1;(Meaning: );    Address(0x12[4:4],)
SPIWrite 0020,3c,0,7
SPIWrite 0021,0f,0,7
SPIWrite 0022,3c,0,7
SPIWrite 0023,0f,0,7
SPIWrite 0024,3c,0,7
SPIWrite 0025,0f,0,7
SPIWrite 0026,3c,0,7
SPIWrite 0027,0f,0,7
SPIWrite 0012,00,4,4    //PAGE: sys_calib_macro_memory=0x0;(Meaning: );    Address(0x12[4:4],)
```

SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1; (Meaning:); Address (0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7

SPIWrite 00a2,08,0,7

SPIWrite 00a1,0f,0,7

SPIWrite 00a0,12,0,7

SPIWrite 0193,35,0,7

\\Read macro_opcode_operand=0x35000000; Address (0x190[31:0],)

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0; (Meaning:); Address (0x13[4:4],)

SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address (0x13[5:5],)

SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address (0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address (0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address (0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address (0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address (0x20[6:6],)

\\Read MACRO_ERROR_IN_EXECUTION=0x0; Address (0x20[7:7],)

SPIWrite 0013,00,5,5 //PAGE: cm4_macro=0x0; Address (0x13[5:5],)

SPIWrite 0014,04,2,2 //PAGE: TIMING_CON=0x1; (Meaning:); Address (0x14[2:2],)

SPIWrite 0108,01,0,0 //en_bypass_fb_mux_sel=0x1; Address (0x108[0:0],)

SPIWrite 0109,04,0,3 //reg_bypass_fb_mux_sel=0x4; Address (0x108[11:8],)

SPIWrite 010b,00,0,0 //singlefb_cha_chc=0x0; Address (0x108[24:24],)

SPIWrite 010c,02,0,1 //ch0_msb_fbmuxsel=0x2; Address (0x10c[1:0],)

SPIWrite 010d,00,0,1 //ch0_lsb_fbmuxsel=0x0; Address (0x10c[9:8],)

SPIWrite 010e,03,0,1 //ch1_msb_fbmuxsel=0x3; Address (0x10c[17:16],)

SPIWrite 010f,01,0,1 //ch1_lsb_fbmuxsel=0x1; Address (0x10c[25:24],)

SPIWrite 010a,00,0,0 //dual_fbmode=0x0; Address (0x108[16:16],)

SPIWrite 071f,90,0,1 //override_fb_muxsel=0x0; Address (0x71c[25:24],)

SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning:); Address (0x14[2:2],)

\\ STEP: agcConfig/step0

\\START: Doing AGC Config

SPIWrite 0010,0c,0,7 //PAGE: rx_top=0x3; Address (0x10[3:2],)

SPIWrite 04cd,00,0,7 //blank_time_rf_det=0x64; Address (0x4cc[15:0],)

SPIWrite 04cc,64,0,7

SPIWrite 04cf,01,0,7 //blank_time=0x1f4; Address (0x4cc[31:16],)

SPIWrite 04ce,f4,0,7

SPIWrite 0275,03,0,7 //blank_time_for_ext_comp_change=0x3e8; Address (0x274[15:0],)

SPIWrite 0274,e8,0,7

SPIWrite 0cd1,02,0,7 //disable_hysterisis=0x1; (Meaning:); Address (0xcd0[9:9],)

SPIWrite 0cd1,02,0,7 //dig_gain_dis=0x0; Address (0xcd0[8:8],)

SPIWrite 0cd1,02,0,7 //dig_gain_dis=0x0; Address (0xcd0[8:8],)

SPIWrite 0a80,b3,0,7 //gain_comp_stg_2_3_dec_chain=0xb3; Address (0xa80[7:0],)

\\START: Configuring DGC

SPIWrite 0334,07,0,7 //DgcEnRxMode=0x1; (Meaning:); Address (0x334[0:0],)

SPIWrite 0337,00,0,7 //FltPtMode=0x0; Address (0x334[24:24],)

SPIWrite 0337,00,0,7 //FltPtFmt=0x0; (Meaning:); Address (0x334[26:25],)

```
SPIWrite 0335,1b,0,7 //DgcMode=0x3;(Meaning: ); Address(0x334[10:8],)
SPIWrite 033c,24,0,7 //NBitsCoarseIdx=0x4;(Meaning: ); Address(0x33c[2:0],)
SPIWrite 033c,1c,0,7 //CoarseStep=0x3;(Meaning: ); Address(0x33c[6:3],)
SPIWrite 04f9,00,0,7 //AttnCodeDelay=0x2; Address(0x4f8[13:0],)
SPIWrite 04f8,02,0,7
SPIWrite 04f1,01,0,7 //UseMinAttnFromAgc=0x1;(Meaning: ); Address(0x4f0[8:8],)
SPIWrite 033d,00,0,7 //CoarseIndexSwapIandQ=0x0;(Meaning: ); Address(0x33c[9:9],)
SPIWrite 033d,00,0,7 //CoarseIndexInvert=0x0;(Meaning: ); Address(0x33c[8:8],)
```

\\END: Done configuring DGC

\\START: Configuring Small Step Attack Detector

```
SPIWrite 0209,01,0,7 //small_step_attack_step_size=0x1; Address(0x208[13:8],)
SPIWrite 0239,0b,0,7 //small_step_attack_sig_th_high=0xb53; Address(0x238[11:0],)
SPIWrite 0238,53,0,7
SPIWrite 023b,0b,0,7 //small_step_attack_sig_th_low=0xb53; Address(0x238[27:16],)
SPIWrite 023a,53,0,7
SPIWrite 021a,00,0,7 //small_step_attack_win_len=0x80; Address(0x218[23:0],)
SPIWrite 0219,00,0,7
SPIWrite 0218,80,0,7
SPIWrite 024e,00,0,7 //small_step_attack_num_hits=0x8; Address(0x24c[23:0],)
SPIWrite 024d,00,0,7
SPIWrite 024c,08,0,7
SPIWrite 0200,4a,0,7 //small_step_attack_en=0x1;(Meaning: ); Address(0x200[1:1],)
SPIWrite 0204,0a,0,7 //small_step_attack_det_en=0x1;(Meaning: ); Address(0x204[1:1],)
```

\\END: Done configuring Small Step Attack Detector

\\START: Configuring Small Step Decay Detector

```
SPIWrite 020b,01,0,7 //small_step_decay_step_size=0x1; Address(0x208[29:24],)
SPIWrite 0241,05,0,7 //small_step_decay_sig_th_high=0x50f; Address(0x240[11:0],)
SPIWrite 0240,0f,0,7
SPIWrite 0243,05,0,7 //small_step_decay_sig_th_low=0x50f; Address(0x240[27:16],)
SPIWrite 0242,0f,0,7
SPIWrite 0222,02,0,7 //small_step_decay_win_len=0x20000; Address(0x220[23:0],)
SPIWrite 0221,00,0,7
SPIWrite 0220,00,0,7
SPIWrite 0256,00,0,7 //small_step_decay_num_hits=0x8; Address(0x254[23:0],)
SPIWrite 0255,00,0,7
SPIWrite 0254,08,0,7
SPIWrite 0200,4a,0,7 //small_step_decay_en=0x1;(Meaning: ); Address(0x200[3:3],)
SPIWrite 0204,0a,0,7 //small_step_decay_det_en=0x1;(Meaning: ); Address(0x204[3:3],)
```

\\END: Done configuring Small Step Decay Detector

\\START: Configuring Big Step Attack Detector

```
SPIWrite 0208,03,0,7 //big_step_attack_step_size=0x3; Address(0x208[5:0],)
SPIWrite 0235,0e,0,7 //big_step_attack_sig_th_high=0xe42; Address(0x234[11:0],)
SPIWrite 0234,42,0,7
SPIWrite 0237,0e,0,7 //big_step_attack_sig_th_low=0xe42; Address(0x234[27:16],)
SPIWrite 0236,42,0,7
SPIWrite 0216,00,0,7 //big_step_attack_win_len=0x20; Address(0x214[23:0],)
SPIWrite 0215,00,0,7
SPIWrite 0214,20,0,7
SPIWrite 024a,00,0,7 //big_step_attack_num_hits=0x8; Address(0x248[23:0],)
SPIWrite 0249,00,0,7
SPIWrite 0248,08,0,7
SPIWrite 0200,4a,0,7 //big_step_attack_en=0x0;(Meaning: ); Address(0x200[0:0],)
SPIWrite 0204,0a,0,7 //big_step_attack_det_en=0x0;(Meaning: ); Address(0x204[0:0],)
```

\\END: Done configuring Big Step Attack Detector

\\START: Configuring Big Step Decay Detector

```
SPIWrite 0cd7,20,0,7 //big_step_decay_syncmode=0x0; Address(0xcd4[26:26],)
SPIWrite 020a,03,0,7 //big_step_decay_step_size=0x3; Address(0x208[21:16],)
SPIWrite 023d,04,0,7 //big_step_decay_sig_th_high=0x404; Address(0x23c[11:0],)
SPIWrite 023c,04,0,7
SPIWrite 023f,04,0,7 //big_step_decay_sig_th_low=0x404; Address(0x23c[27:16],)
SPIWrite 023e,04,0,7
SPIWrite 021e,00,0,7 //big_step_decay_win_len=0x8000; Address(0x21c[23:0],)
SPIWrite 021d,80,0,7
SPIWrite 021c,00,0,7
SPIWrite 0252,00,0,7 //big_step_decay_num_hits=0x8; Address(0x250[23:0],)
SPIWrite 0251,00,0,7
SPIWrite 0250,08,0,7
SPIWrite 0200,4a,0,7 //big_step_decay_en=0x0; (Meaning: ); Address(0x200[2:2],)
SPIWrite 0204,0a,0,7 //big_step_decay_det_en=0x0; (Meaning: ); Address(0x204[2:2],)
```

\\END: Done configuring Big Step Decay Detector

```
SPIWrite 08cd,00,0,7 //blank_time_rf_det=0x64; Address(0x8cc[15:0],)
SPIWrite 08cc,64,0,7
SPIWrite 08cf,01,0,7 //blank_time=0x1f4; Address(0x8cc[31:16],)
SPIWrite 08ce,f4,0,7
SPIWrite 0675,03,0,7 //blank_time_for_ext_comp_change=0x3e8; Address(0x674[15:0],)
SPIWrite 0674,e8,0,7
SPIWrite 0ed1,02,0,7 //disable_hysteresis=0x1; (Meaning: ); Address(0xed0[9:9],)
SPIWrite 0ed1,02,0,7 //dig_gain_dis=0x0; Address(0xed0[8:8],)
SPIWrite 0ed1,02,0,7 //dig_gain_dis=0x0; Address(0xed0[8:8],)
SPIWrite 0a81,b3,0,7 //gain_comp_stg_2_3_dec_chain=0xb3; Address(0xa80[15:8],)
```

\\START: Configuring DGC

```
SPIWrite 0734,07,0,7 //DgcEnRxMode=0x1; (Meaning: ); Address(0x734[0:0],)
SPIWrite 0737,00,0,7 //FltPtMode=0x0; Address(0x734[24:24],)
SPIWrite 0737,00,0,7 //FltPtFmt=0x0; (Meaning: ); Address(0x734[26:25],)
SPIWrite 0735,1b,0,7 //DgcMode=0x3; (Meaning: ); Address(0x734[10:8],)
SPIWrite 073c,24,0,7 //NBitsCoarseIdx=0x4; (Meaning: ); Address(0x73c[2:0],)
SPIWrite 073c,1c,0,7 //CoarseStep=0x3; (Meaning: ); Address(0x73c[6:3],)
SPIWrite 08f9,00,0,7 //AttnCodeDelay=0x2; Address(0x8f8[13:0],)
SPIWrite 08f8,02,0,7
SPIWrite 08f1,01,0,7 //UseMinAttnFromAgc=0x1; (Meaning: ); Address(0x8f0[8:8],)
SPIWrite 073d,00,0,7 //CoarseIndexSwapIandQ=0x0; (Meaning: ); Address(0x73c[9:9],)
SPIWrite 073d,00,0,7 //CoarseIndexInvert=0x0; (Meaning: ); Address(0x73c[8:8],)
```

\\END: Done configuring DGC

\\START: Configuring Small Step Attack Detector

```
SPIWrite 0609,01,0,7 //small_step_attack_step_size=0x1; Address(0x608[13:8],)
SPIWrite 0639,0b,0,7 //small_step_attack_sig_th_high=0xb53; Address(0x638[11:0],)
SPIWrite 0638,53,0,7
SPIWrite 063b,0b,0,7 //small_step_attack_sig_th_low=0xb53; Address(0x638[27:16],)
SPIWrite 063a,53,0,7
SPIWrite 061a,00,0,7 //small_step_attack_win_len=0x80; Address(0x618[23:0],)
SPIWrite 0619,00,0,7
SPIWrite 0618,80,0,7
SPIWrite 064e,00,0,7 //small_step_attack_num_hits=0x8; Address(0x64c[23:0],)
SPIWrite 064d,00,0,7
SPIWrite 064c,08,0,7
SPIWrite 0600,4a,0,7 //small_step_attack_en=0x1; (Meaning: ); Address(0x600[1:1],)
SPIWrite 0604,0a,0,7 //small_step_attack_det_en=0x1; (Meaning: ); Address(0x604[1:1],)
```

\\END: Done configuring Small Step Attack Detector

\\START: Configuring Small Step Decay Detector

```
SPIWrite 060b,01,0,7 //small_step_decay_step_size=0x1; Address(0x608[29:24],)
```

```
SPIWrite 0641,05,0,7    //small_step_decay_sig_th_high=0x50f;    Address(0x640[11:0],)
SPIWrite 0640,0f,0,7
SPIWrite 0643,05,0,7    //small_step_decay_sig_th_low=0x50f;    Address(0x640[27:16],)
SPIWrite 0642,0f,0,7
SPIWrite 0622,02,0,7    //small_step_decay_win_len=0x20000;    Address(0x620[23:0],)
SPIWrite 0621,00,0,7
SPIWrite 0620,00,0,7
SPIWrite 0656,00,0,7    //small_step_decay_num_hits=0x8;    Address(0x654[23:0],)
SPIWrite 0655,00,0,7
SPIWrite 0654,08,0,7
SPIWrite 0600,4a,0,7    //small_step_decay_en=0x1;(Meaning: );    Address(0x600[3:3],)
SPIWrite 0604,0a,0,7    //small_step_decay_det_en=0x1;(Meaning: );    Address(0x604[3:3],)
```

\\END: Done configuring Small Step Decay Detector

\\START: Configuring Big Step Attack Detector

```
SPIWrite 0608,03,0,7    //big_step_attack_step_size=0x3;    Address(0x608[5:0],)
SPIWrite 0635,0e,0,7    //big_step_attack_sig_th_high=0xe42;    Address(0x634[11:0],)
SPIWrite 0634,42,0,7
SPIWrite 0637,0e,0,7    //big_step_attack_sig_th_low=0xe42;    Address(0x634[27:16],)
SPIWrite 0636,42,0,7
SPIWrite 0616,00,0,7    //big_step_attack_win_len=0x20;    Address(0x614[23:0],)
SPIWrite 0615,00,0,7
SPIWrite 0614,20,0,7
SPIWrite 064a,00,0,7    //big_step_attack_num_hits=0x8;    Address(0x648[23:0],)
SPIWrite 0649,00,0,7
SPIWrite 0648,08,0,7
SPIWrite 0600,4a,0,7    //big_step_attack_en=0x0;(Meaning: );    Address(0x600[0:0],)
SPIWrite 0604,0a,0,7    //big_step_attack_det_en=0x0;(Meaning: );    Address(0x604[0:0],)
```

\\END: Done configuring Big Step Attack Detector

\\START: Configuring Big Step Decay Detector

```
SPIWrite 0ed7,20,0,7    //big_step_decay_syncmode=0x0;    Address(0xed4[26:26],)
SPIWrite 060a,03,0,7    //big_step_decay_step_size=0x3;    Address(0x608[21:16],)
SPIWrite 063d,04,0,7    //big_step_decay_sig_th_high=0x404;    Address(0x63c[11:0],)
SPIWrite 063c,04,0,7
SPIWrite 063f,04,0,7    //big_step_decay_sig_th_low=0x404;    Address(0x63c[27:16],)
SPIWrite 063e,04,0,7
SPIWrite 061e,00,0,7    //big_step_decay_win_len=0x8000;    Address(0x61c[23:0],)
SPIWrite 061d,80,0,7
SPIWrite 061c,00,0,7
SPIWrite 0652,00,0,7    //big_step_decay_num_hits=0x8;    Address(0x650[23:0],)
SPIWrite 0651,00,0,7
SPIWrite 0650,08,0,7
SPIWrite 0600,4a,0,7    //big_step_decay_en=0x0;(Meaning: );    Address(0x600[2:2],)
SPIWrite 0604,0a,0,7    //big_step_decay_det_en=0x0;(Meaning: );    Address(0x604[2:2],)
```

\\END: Done configuring Big Step Decay Detector

\\START: Enabling or disabling Internal AGC

```
SPIWrite 0264,00,0,7    //ext_agc_mode=0x0;    Address(0x264[1:1],)
SPIWrite 0264,00,0,7    //internal_agc_en=0x0;(Meaning: );    Address(0x264[0:0],)
SPIWrite 0264,01,0,7    //internal_agc_en=0x1;(Meaning: );    Address(0x264[0:0],)
```

\\END: Done enabling or disabling Internal AGC

\\START: Enabling or disabling Internal AGC

```
SPIWrite 0664,00,0,7    //ext_agc_mode=0x0;    Address(0x664[1:1],)
SPIWrite 0664,00,0,7    //internal_agc_en=0x0;(Meaning: );    Address(0x664[0:0],)
SPIWrite 0664,01,0,7    //internal_agc_en=0x1;(Meaning: );    Address(0x664[0:0],)
```

\\END: Done enabling or disabling Internal AGC

SPIWrite 0010,00,0,7 //PAGE: rx_top=0x0; Address(0x10[3:2],)
SPIWrite 0016,30,0,7 //PAGE: dsa=0x3; Address(0x16[5:4],)
SPIWrite 0040,02,0,7

\\END: Completed AGC Config

SPIWrite 0016,00,0,7 //PAGE: dsa=0x0; Address(0x16[5:4],)
\\ STEP: attnSet/step0

\\START: Setting TX DSA Attenuation

SPIWrite 0016,30,0,7 //PAGE: dsa=0x3; Address(0x16[5:4],)
SPIWrite 0100,01,0,7
SPIWrite 0102,00,0,7
SPIWrite 0102,01,0,7
SPIWrite 0102,00,0,7

\\END: Done setting TX DSA Attenuation

\\START: Setting TX DSA Attenuation

SPIWrite 0120,01,0,7
SPIWrite 0122,00,0,7
SPIWrite 0122,01,0,7
SPIWrite 0122,00,0,7

\\END: Done setting TX DSA Attenuation

\\START: Setting TX DSA Attenuation

SPIWrite 0100,0d,0,7
SPIWrite 0102,00,0,7
SPIWrite 0102,01,0,7
SPIWrite 0102,00,0,7

\\END: Done setting TX DSA Attenuation

SPIWrite 0050,00,0,7

\\START: Setting TX DSA Attenuation

SPIWrite 0120,0d,0,7
SPIWrite 0122,00,0,7
SPIWrite 0122,01,0,7
SPIWrite 0122,00,0,7

\\END: Done setting TX DSA Attenuation

SPIWrite 00b0,00,0,7
SPIWrite 0150,01,0,7

\\START: Setting TX DSA Attenuation

SPIWrite 0100,0d,0,7
SPIWrite 0102,00,0,7
SPIWrite 0102,01,0,7
SPIWrite 0102,00,0,7

\\END: Done setting TX DSA Attenuation

SPIWrite 0050,00,0,7

\\START: Setting TX DSA Attenuation

```
SPIWrite 0120,0d,0,7
SPIWrite 0122,00,0,7
SPIWrite 0122,01,0,7
SPIWrite 0122,00,0,7
```

```
\\END: Done setting TX DSA Attenuation
```

```
SPIWrite 00b0,00,0,7
SPIWrite 0150,01,0,7
SPIWrite 0016,00,0,7 //PAGE: dsa=0x0; Address(0x16[5:4],)
\\ STEP: dacJesdLinkUp/step0
```

```
\\START: Clearing All Alarms
```

```
SPIWrite 001a,ff,0,7 //alarms_clear=0x1ff; Address(0x1a[7:0],0x1b[0:0],)
SPIWrite 001b,01,0,7
SPIWrite 001a,00,0,7 //alarms_clear=0x0; Address(0x1a[7:0],0x1b[0:0],)
SPIWrite 001b,00,0,7
SPIWrite 0013,20,0,7 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)
SPIWrite 02e5,20,0,7
SPIWrite 02e5,00,0,7
```

```
\\START: Clearing JESD RX Data and alarms
```

```
SPIWrite 0013,00,0,7 //PAGE: cm4_macro=0x0; Address(0x13[5:5],)
SPIWrite 0015,02,0,7 //PAGE: tx_jesd=0x3; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,22,0,7
SPIWrite 0081,ff,0,7 //jesd_clear_data=0xff; Address(0x80[15:8],)
SPIWrite 0081,00,0,7 //jesd_clear_data=0x0; Address(0x80[15:8],)
SPIWrite 002d,db,0,7 //serdes_fifo_err_clear=0x1; Address(0x2c[14:14],)
SPIWrite 002d,9b,0,7 //serdes_fifo_err_clear=0x0; Address(0x2c[14:14],)
SPIWrite 0190,01,0,7 //clear_all_alarms=0x1; Address(0x190[0:0],)
SPIWrite 0190,00,0,7 //clear_all_alarms=0x0; Address(0x190[0:0],)
SPIWrite 0190,04,0,7 //clear_all_alarms_to_pap=0x1; Address(0x190[2:2],)
SPIWrite 0190,00,0,7 //clear_all_alarms_to_pap=0x0; Address(0x190[2:2],)
```

```
\\END: Done clearing JESD RX Data and alarms
```

```
\\START: Clearing JESD TX Data and alarms
```

```
SPIWrite 0015,20,0,7 //PAGE: tx_jesd=0x0; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,00,0,7
SPIWrite 0015,01,0,7 //PAGE: rx_jesd=0x3; Address(0x15[0:0],0x15[4:4],)
SPIWrite 0015,11,0,7
SPIWrite 00b1,0f,0,7 //alarms_serdes_fifo_errors_clear=0xf; Address(0xb0[11:8],)
SPIWrite 00b1,00,0,7 //alarms_serdes_fifo_errors_clear=0x0; Address(0xb0[11:8],)
SPIWrite 0026,0f,0,7 //jesd_clear_data=0xf; Address(0x24[19:16],)
SPIWrite 0026,00,0,7 //jesd_clear_data=0x0; Address(0x24[19:16],)
SPIWrite 0045,c0,0,7 //fifo_init_state=0x0; Address(0x44[13:13],)
```

```
\\END: Done clearing JESD TX Data and alarms
```

```
\\START: Requesting/releasing SPI Access to PLL Pages
```

```
SPIWrite 0015,10,0,7 //PAGE: rx_jesd=0x0; Address(0x15[0:0],0x15[4:4],)
SPIWrite 0015,00,0,7
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning: ); Address(0x15[7:7],)
SPIWrite 01d4,01,0,7 //pll_reg_spi_req_a=0x1; Address(0x1d4[0:0],)
SPIWrite 0374,00,0,7 //pll_reg_spi_req_b1=0x0; (Meaning: ); Address(0x374[0:0],)
```

```
SPIPoll 01d5,0,0,1
```

```
\\Read pll_reg_spi_a_ack=0x1; Address(0x1d4[8:8],)
```

```
\\END: Done requesting/releasing SPI Access to PLL Pages
```

```

SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0014,f8,0,7 //PAGE: pll=0x1f; Address(0x14[7:3],)
SPIWrite 0066,08,0,7 //lock_lost_rst=0x1; Address(0x64[19:19],)
SPIWrite 0066,00,0,7 //lock_lost_rst=0x0; Address(0x64[19:19],)

\\START: Requesting/releasing SPI Access to PLL Pages

SPIWrite 0014,00,0,7 //PAGE: pll=0x0; Address(0x14[7:3],)
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning: ); Address(0x15[7:7],)
SPIWrite 01d4,00,0,7 //pll_reg_spi_req_a=0x0; Address(0x1d4[0:0],)
SPIWrite 0374,00,0,7 //pll_reg_spi_req_b1=0x0; (Meaning: ); Address(0x374[0:0],)

WAIT 0.01

WAIT 0.1

\\END: Done requesting/releasing SPI Access to PLL Pages

SPIWrite 064a,0f,0,7 //pap_alarm_clear=0xf; Address(0x648[19:16],)
SPIWrite 064a,00,0,7 //pap_alarm_clear=0x0; Address(0x648[19:16],)

\\END: Done clearing All Alarms

SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning: ); Address(0x15[7:7],)

WAIT 0.1

\\ STEP: dacJesdLinkUp/step1
SPIWrite 0013,10,4,4 //PAGE: customer_macro=0x1; (Meaning: ); Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,4a,0,7
SPIWrite 0193,31,0,7
SPIWrite 0192,00,0,7
SPIWrite 0191,00,0,7
SPIWrite 0190,00,0,7

\\Read macro_opcode_operand=0x31000000; Address(0x190[31:0],)

WAIT 0.1

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4 //PAGE: customer_macro=0x0; (Meaning: ); Address(0x13[4:4],)
SPIWrite 0013,20,5,5 //PAGE: cm4_macro=0x1; Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR=0x0; Address(0x20[3:3],)

\\Read MACRO_ERROR_IN_OPCODE=0x0; Address(0x20[4:4],)

\\Read MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0; Address(0x20[5:5],)

\\Read MACRO_ERROR_IN_OPERAND=0x0; Address(0x20[6:6],)

```



```
\\Read  MACRO_ERROR_IN_EXECUTION=0x0;    Address(0x20[7:7],)

\\Read  MACRO_READY=0x1;    Address(0x20[0:0],)

\\Read  MACRO_ACK=0x1;    Address(0x20[1:1],)

\\Read  MACRO_DONE=0x1;    Address(0x20[2:2],)

SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\START: Enabling RX IQMC Correction

SPIWrite 0013,00,5,5    //PAGE: cm4_macro=0x0;    Address(0x13[5:5],)

WAIT 0.1

SPIWrite 0013,10,4,4    //PAGE: customer_macro=0x1;(Meaning: );    Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,02,0,7
SPIWrite 00a1,00,0,7
SPIWrite 00a0,00,0,7
SPIWrite 00a7,00,0,7
SPIWrite 00a6,00,0,7
SPIWrite 00a5,00,0,7
SPIWrite 00a4,0f,0,7
SPIWrite 0193,38,0,7
SPIWrite 0192,00,0,7
SPIWrite 0191,00,0,7
SPIWrite 0190,00,0,7

\\Read  macro_opcode_operand=0x38000000;    Address(0x190[31:0],)

WAIT 0.1

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,4,4    //PAGE: customer_macro=0x0;(Meaning: );    Address(0x13[4:4],)
SPIWrite 0013,20,5,5    //PAGE: cm4_macro=0x1;    Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR=0x0;    Address(0x20[3:3],)

\\Read  MACRO_ERROR_IN_OPCODE=0x0;    Address(0x20[4:4],)

\\Read  MACRO_ERROR_OPCODE_NOT_ALLOWED=0x0;    Address(0x20[5:5],)

\\Read  MACRO_ERROR_IN_OPERAND=0x0;    Address(0x20[6:6],)

\\Read  MACRO_ERROR_IN_EXECUTION=0x0;    Address(0x20[7:7],)
```

```

\\Read  MACRO_READY=0x1;      Address(0x20[0:0],)

\\Read  MACRO_ACK=0x1;   Address(0x20[1:1],)

\\Read  MACRO_DONE=0x1;      Address(0x20[2:2],)

SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)

WAIT 0.5

SPIWrite 0013,00,5,5      //PAGE: cm4_macro=0x0;  Address(0x13[5:5],)
SPIWrite 0010,0c,2,3      //PAGE: rx_top=0x3;      Address(0x10[3:2],)
SPIWrite 0501,00,0,0      //bypass_mode=0x0;(Meaning: );  Address(0x500[8:8],)
SPIWrite 0901,00,0,0      //bypass_mode=0x0;(Meaning: );  Address(0x900[8:8],)
SPIWrite 0500,00,0,0      //bypass_mode=0x0;(Meaning: );  Address(0x500[0:0],)
SPIWrite 0900,00,0,0      //bypass_mode=0x0;(Meaning: );  Address(0x900[0:0],)

\\END: Done enabling RX IQMC Correction

SPIWrite 0010,00,0,7      //PAGE: rx_top=0x0;      Address(0x10[3:2],)

WAIT 0.1

SPIWrite 0013,10,0,7      //PAGE: customer_macro=0x1;(Meaning: );      Address(0x13[4:4],)

SPIPoll 00f0,0,0,1

SPIWrite 00a3,00,0,7
SPIWrite 00a2,00,0,7
SPIWrite 00a1,ff,0,7
SPIWrite 00a0,05,0,7
SPIWrite 0193,35,0,7
SPIWrite 0192,00,0,7
SPIWrite 0191,00,0,7
SPIWrite 0190,00,0,7

SPIPoll 00f0,0,7,7

SPIWrite 0013,00,0,7      //PAGE: customer_macro=0x0;(Meaning: );      Address(0x13[4:4],)

WAIT 0.1

SPIWrite 0013,20,0,7      //PAGE: cm4_macro=0x1;  Address(0x13[5:5],)
SPIReadCheck 0020,3,3,00

\\Read  MACRO_ERROR=0x0;      Address(0x20[3:3],)

SPIWrite 0013,00,0,7      //PAGE: cm4_macro=0x0;  Address(0x13[5:5],)
SPIWrite 0014,04,0,7      //PAGE: TIMING_CON=0x1;(Meaning: );      Address(0x14[2:2],)
SPIWrite 0108,01,0,7      //en_bypass_fb_mux_sel=0x1;      Address(0x108[0:0],)
SPIWrite 0109,04,0,7      //reg_bypass_fb_mux_sel=0x4;      Address(0x108[11:8],)

\\START: Disabling TDD overrides

SPIWrite 0124,00,0,7      //use_reg_txon_AB=0x0;  Address(0x124[0:0],)
SPIWrite 0126,00,0,7      //use_reg_rxon_AB=0x0;  Address(0x124[16:16],)
SPIWrite 0128,00,0,7      //use_reg_fbon_AB=0x0;  Address(0x128[0:0],)
SPIWrite 012a,00,0,7      //use_reg_txon_CD=0x0;  Address(0x128[16:16],)
SPIWrite 012c,00,0,7      //use_reg_rxon_CD=0x0;  Address(0x12c[0:0],)
SPIWrite 012e,00,0,7      //use_reg_fbon_CD=0x0;  Address(0x12c[16:16],)

\\END: Done disabling TDD overrides

SPIWrite 0014,00,0,7      //PAGE: TIMING_CON=0x0;(Meaning: );      Address(0x14[2:2],)

```

\\END: Device Config Complete

```
SPIWrite 0010,04,0,7 //PAGE: rx_top=0x1; Address(0x10[3:2],)
SPIWrite 033d,02,0,7 //CoarseIndexSwapIandQ=0x1;(Meaning: ); Address(0x33c[9:9],)
SPIWrite 073d,02,0,7 //CoarseIndexSwapIandQ=0x1;(Meaning: ); Address(0x73c[9:9],)
SPIWrite 04f9,00,0,7 //AttnCodeDelay=0x6; Address(0x4f8[13:0],)
SPIWrite 04f8,06,0,7
SPIWrite 08f9,00,0,7 //AttnCodeDelay=0x6; Address(0x8f8[13:0],)
SPIWrite 08f8,06,0,7
SPIWrite 033e,04,0,7 //FineOffset=0x4; Address(0x33c[18:16],)
SPIWrite 073e,04,0,7 //FineOffset=0x4; Address(0x73c[18:16],)
SPIWrite 0010,08,0,7 //PAGE: rx_top=0x2; Address(0x10[3:2],)
SPIWrite 033d,02,0,7 //CoarseIndexSwapIandQ=0x1;(Meaning: ); Address(0x33c[9:9],)
SPIWrite 073d,02,0,7 //CoarseIndexSwapIandQ=0x1;(Meaning: ); Address(0x73c[9:9],)
SPIWrite 04f9,00,0,7 //AttnCodeDelay=0x6; Address(0x4f8[13:0],)
SPIWrite 04f8,06,0,7
SPIWrite 08f9,00,0,7 //AttnCodeDelay=0x6; Address(0x8f8[13:0],)
SPIWrite 08f8,06,0,7
SPIWrite 033e,04,0,7 //FineOffset=0x4; Address(0x33c[18:16],)
SPIWrite 073e,04,0,7 //FineOffset=0x4; Address(0x73c[18:16],)
SPIWrite 0010,00,0,7 //PAGE: rx_top=0x0; Address(0x10[3:2],)
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1;(Meaning: ); Address(0x15[7:7],)
SPIWrite 0914,00,0,7 //mask_pdn_high_intr=0x0; Address(0x914[0:0],)
SPIWrite 0918,00,0,7 //mask_pdn_fall_intr=0x0; Address(0x918[0:0],)
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0;(Meaning: ); Address(0x15[7:7],)
SPIWrite 0013,10,0,7
SPIWrite 00a3,00,0,7
SPIWrite 00a2,08,0,7
SPIWrite 00a1,03,0,7
SPIWrite 00a0,ff,0,7
SPIWrite 0193,39,0,7
SPIWrite 0013,00,0,7
SPIWrite 0013,10,0,7
SPIWrite 00a3,00,0,7
SPIWrite 00a2,08,0,7
SPIWrite 00a1,01,0,7
SPIWrite 00a0,ff,0,7
SPIWrite 0193,3a,0,7
SPIWrite 0013,00,0,7
SPIWrite 0015,02,0,7 //PAGE: tx_jesd=0x1; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,02,0,7
SPIWrite 0173,00,0,7 //alarms_mask=0xffff; Address(0x170[31:0],0x174[31:0],)
SPIWrite 0172,00,0,7
SPIWrite 0171,0f,0,7
SPIWrite 0170,ff,0,7
SPIWrite 0177,00,0,7
SPIWrite 0176,00,0,7
SPIWrite 0175,00,0,7
SPIWrite 0174,00,0,7
SPIWrite 0015,00,0,7 //PAGE: tx_jesd=0x2; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,20,0,7
SPIWrite 0173,00,0,7 //alarms_mask=0xffff; Address(0x170[31:0],0x174[31:0],)
SPIWrite 0172,00,0,7
SPIWrite 0171,0f,0,7
SPIWrite 0170,ff,0,7
SPIWrite 0177,00,0,7
SPIWrite 0176,00,0,7
SPIWrite 0175,00,0,7
SPIWrite 0174,00,0,7
SPIWrite 0015,22,0,7 //PAGE: tx_jesd=0x1; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,02,0,7
SPIWrite 017b,00,0,7 //alarms_to_pap_mask=0xffff; Address(0x178[31:0],0x17c[31:0],)
SPIWrite 017a,00,0,7
SPIWrite 0179,0f,0,7
SPIWrite 0178,ff,0,7
SPIWrite 017f,00,0,7
SPIWrite 017e,00,0,7
SPIWrite 017d,00,0,7
```

```

SPIWrite 017c,00,0,7
SPIWrite 0015,00,0,7 //PAGE: tx_jesd=0x2; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,20,0,7
SPIWrite 017b,00,0,7 //alarms_to_pap_mask=0xffff; Address(0x178[31:0],0x17c[31:0],)
SPIWrite 017a,00,0,7
SPIWrite 0179,0f,0,7
SPIWrite 0178,ff,0,7
SPIWrite 017f,00,0,7
SPIWrite 017e,00,0,7
SPIWrite 017d,00,0,7
SPIWrite 017c,00,0,7
SPIWrite 0015,20,0,7 //PAGE: tx_jesd=0x0; Address(0x15[1:1],0x15[5:5],)
SPIWrite 0015,00,0,7
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0278,03,0,7 //pull_ctrl_gpio_30=0x3; Address(0x278[2:0],)
SPIWrite 0254,03,0,7 //pull_ctrl_gpio_21=0x3; Address(0x254[2:0],)
SPIWrite 016d,02,0,7 //pol_intpi_global_pdn=0x1; Address(0x16c[9:9],)

SPIWrite 016d,00,0,7 //pol_intpi_global_pdn=0x0; Address(0x16c[9:9],)

SPIWrite 0550,02,0,7 //ovr_sel_intpi_tdd_2r_en_ab=0x1; Address(0x550[1:1],)
SPIWrite 0550,03,0,7 //ovr_intpi_tdd_2r_en_ab=0x1; Address(0x550[0:0],)
SPIWrite 0550,23,0,7 //ovr_sel_intpi_tdd_2t_en_ab=0x1; Address(0x550[5:5],)
SPIWrite 0550,33,0,7 //ovr_intpi_tdd_2t_en_ab=0x1; Address(0x550[4:4],)
SPIWrite 0550,3b,0,7 //ovr_sel_intpi_tdd_2r_en_cd=0x1; Address(0x550[3:3],)
SPIWrite 0550,3f,0,7 //ovr_intpi_tdd_2r_en_cd=0x1; Address(0x550[2:2],)
SPIWrite 0550,bf,0,7 //ovr_sel_intpi_tdd_2t_en_cd=0x1; Address(0x550[7:7],)
SPIWrite 0550,ff,0,7 //ovr_intpi_tdd_2t_en_cd=0x1; Address(0x550[6:6],)
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0124,01,0,7 //use_reg_txon_AB=0x1; Address(0x124[0:0],)
SPIWrite 0126,01,0,7 //use_reg_rxon_AB=0x1; Address(0x124[16:16],)
SPIWrite 012a,01,0,7 //use_reg_txon_CD=0x1; Address(0x128[16:16],)
SPIWrite 012c,01,0,7 //use_reg_rxon_CD=0x1; Address(0x12c[0:0],)
SPIWrite 0125,01,0,7 //reg_for_txon_AB=0x1; Address(0x124[8:8],)
SPIWrite 0127,01,0,7 //reg_for_rxon_AB=0x1; Address(0x124[24:24],)
SPIWrite 012b,01,0,7 //reg_for_txon_CD=0x1; Address(0x128[24:24],)
SPIWrite 012d,01,0,7 //reg_for_rxon_CD=0x1; Address(0x12c[8:8],)
SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0015,80,0,7 //PAGE: DIGTOP_MISC=0x1; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0100,00,0,7 //auxadc_powerup=0x0; Address(0x100[0:0],)
SPIWrite 0100,00,0,7 //auxadc_en_conv=0x0; Address(0x100[1:1],)
SPIWrite 0100,00,0,7 //auxadc_soft_reset=0x0; Address(0x100[3:3],)
SPIWrite 0106,01,0,7 //auxadc_sar_bit_mode=0x1; (Meaning: ); Address(0x104[16:16],)
SPIWrite 0102,20,0,7 //auxadc_INP_MUX=0x1; (Meaning: ); Address(0x100[23:21],)
SPIWrite 0102,20,0,7 //auxadc_INM_MUX=0x0; (Meaning: ); Address(0x100[20:18],)
SPIWrite 0733,04,0,7 //aux_adc_reg_sar_delay=0x4; Address(0x730[26:24],)
SPIWrite 0101,08,0,7 //auxadc_clk_div=0x1; (Meaning: ); Address(0x100[12:11],)
SPIWrite 0101,0a,0,7 //auxadc_duty_cycle_clk=0x2; (Meaning: ); Address(0x100[10:8],)
SPIWrite 0108,00,0,7 //auxadc_offset_estim_phase_en=0x0; Address(0x108[5:5],)
SPIWrite 0105,a0,0,7 //auxadc_sar_averaging=0x5; (Meaning: ); Address(0x104[15:13],)
SPIWrite 0105,a1,0,7 //auxadc_sar_logic_niter=0x1; Address(0x104[9:8],)
SPIWrite 0106,05,0,7 //auxadc_inp_buf_chop=0x2; (Meaning: ); Address(0x104[18:17],)
SPIWrite 0106,15,0,7 //auxadc_incm_buf_chop=0x2; (Meaning: ); Address(0x104[20:19],)
SPIWrite 0106,95,0,7 //auxadc_inp_buf_mux_ctrl=0x4; (Meaning: ); Address(0x104[23:21],)
SPIWrite 0105,a1,0,7 //auxadc_sar_logic_state_start=0x0; Address(0x104[12:10],)
SPIWrite 0108,00,0,7 //auxadc_offset_data_out=0x0; Address(0x108[6:6],)
SPIWrite 0108,00,0,7 //auxadc_offsetvlaue_force_en=0x0; Address(0x108[4:4],)
SPIWrite 0107,00,0,7
SPIWrite 0108,00,0,7 //auxadc_offset_corr_value_msb=0x0; Address(0x108[3:0],)
SPIWrite 0730,00,0,7 //aux_adc_reg_cap_code_force_en=0x0; Address(0x730[6:6],)
SPIWrite 0730,00,0,7 //aux_adc_reg_cap_code_p_force_val=0x0; Address(0x730[5:0],)
SPIWrite 0731,00,0,7 //aux_adc_reg_res_code1_force_en=0x0; Address(0x730[13:13],)
SPIWrite 0731,00,0,7 //aux_adc_reg_res_code2_force_en=0x0; Address(0x730[14:14],)
SPIWrite 0731,00,0,7 //aux_adc_reg_res_code3_force_en=0x0; Address(0x730[15:15],)
SPIWrite 0731,00,0,7 //aux_adc_reg_res_code1_p_force_val=0x0; Address(0x730[12:8],)
SPIWrite 0732,00,0,7 //aux_adc_reg_res_code2_p_force_val=0x0; Address(0x730[20:16],)
SPIWrite 0732,00,0,7 //aux_adc_reg_res_code3_p_force_val=0x0; Address(0x730[23:21],)
SPIWrite 0103,00,0,7 //auxadc_inp_res_term_imeas=0x0; (Meaning: ); Address(0x100[31:30],)

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SPIWrite 0733,04,0,7 //aux_adc_EN_TP=0x0; (Meaning: ); Address(0x730[31:28],)
SPIWrite 0737,00,0,7 //temp_sense2_cfg1=0x0; Address(0x734[31:0],)
SPIWrite 0736,00,0,7
SPIWrite 0735,00,0,7
SPIWrite 0734,00,0,7
SPIWrite 0104,00,0,7 //auxadc_TM_IN_MUX_SEL=0x0; (Meaning: ); Address(0x104[2:0],)
SPIWrite 0104,00,0,7 //auxadc_TP_IN_MUX_SEL=0x0; (Meaning: ); Address(0x104[5:3],)
SPIWrite 0102,20,0,7
SPIWrite 0100,00,0,7 //auxadc_soft_reset=0x0; Address(0x100[3:3],)
SPIWrite 0100,02,0,7 //auxadc_en_conv=0x1; Address(0x100[1:1],)
SPIWrite 0015,00,0,7 //PAGE: DIGTOP_MISC=0x0; (Meaning: ); Address(0x15[7:7],)
SPIWrite 0014,04,0,7 //PAGE: TIMING_CON=0x1; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0108,00,0,7 //en_bypass_fb_mux_sel=0x0; Address(0x108[0:0],)
SPIWrite 0014,00,0,7 //PAGE: TIMING_CON=0x0; (Meaning: ); Address(0x14[2:2],)
SPIWrite 0015,04,0,7 //PAGE: SERDES=0x1; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,04,0,7
SPIWrite 49eb,14,0,7 //PU_TX_PLL=0x0; Address(0x84f5[7:7],)
SPIWrite 49ea,34,0,7
SPIWrite 49ed,8c,0,7 //PU_TX_BANDGAP=0x0; Address(0x84f6[12:12],)
SPIWrite 49ec,40,0,7
SPIWrite 47eb,0d,0,7 //PU_TX_LANE=0x0; Address(0x80f5[15:15],)
SPIWrite 47ea,b0,0,7
SPIWrite 49e3,70,0,7 //PU_TX_DRV_LANE0=0x0; Address(0x84f1[15:15],)
SPIWrite 49e2,00,0,7
SPIWrite 45eb,0d,0,7 //PU_TX_LANE=0x0; Address(0x80f5[15:15],)
SPIWrite 45ea,b0,0,7
SPIWrite 49e3,30,0,7 //PU_TX_DRV_LANE1=0x0; Address(0x84f1[14:14],)
SPIWrite 49e2,00,0,7
SPIWrite 41eb,0d,0,7 //PU_TX_LANE=0x0; Address(0x80f5[15:15],)
SPIWrite 41ea,b0,0,7
SPIWrite 49e3,10,0,7 //PU_TX_DRV_LANE2=0x0; Address(0x84f1[13:13],)
SPIWrite 49e2,00,0,7
SPIWrite 43eb,0d,0,7 //PU_TX_LANE=0x0; Address(0x80f5[15:15],)
SPIWrite 43ea,b0,0,7
SPIWrite 49e3,00,0,7 //PU_TX_DRV_LANE3=0x0; Address(0x84f1[12:12],)
SPIWrite 49e2,00,0,7
SPIWrite 49f3,2a,0,7 //PU_RX_ADC_MASTER=0x0; Address(0x84f9[15:15],)
SPIWrite 49f2,80,0,7
SPIWrite 49fd,a5,0,7 //PU_RX_PLL=0x0; Address(0x84fe[7:7],)
SPIWrite 49fc,34,0,7
SPIWrite 49ff,80,0,7 //PU_RX_BANDGAP=0x0; Address(0x84ff[12:12],)
SPIWrite 49fe,20,0,7
SPIWrite 49ff,80,0,7 //PU_RX_AGC_MASTER=0x0; Address(0x84ff[5:5],)
SPIWrite 49fe,00,0,7
SPIWrite 49f1,12,0,7 //PU_RX_INTP_LANE0=0x0; Address(0x84f8[15:15],)
SPIWrite 49f0,40,0,7
SPIWrite 49f3,0a,0,7 //PU_RX_ADC_LANE0=0x0; Address(0x84f9[13:13],)
SPIWrite 49f2,80,0,7
SPIWrite 47f7,6f,0,7 //PU_RX_ADC_LANE=0x0; Address(0x80fb[2:2],)
SPIWrite 47f6,00,0,7
SPIWrite 47fd,23,0,7 //PU_RX_INTP_LANE=0x0; Address(0x80fe[2:2],)
SPIWrite 47fc,00,0,7
SPIWrite 47ff,00,0,7 //PU_RX_AGC_LANE=0x0; Address(0x80ff[15:15],)
SPIWrite 47fe,b6,0,7
SPIWrite 49f1,02,0,7 //PU_RX_INTP_LANE1=0x0; Address(0x84f8[12:12],)
SPIWrite 49f0,40,0,7
SPIWrite 49f3,02,0,7 //PU_RX_ADC_LANE1=0x0; Address(0x84f9[11:11],)
SPIWrite 49f2,80,0,7
SPIWrite 45f7,6f,0,7 //PU_RX_ADC_LANE=0x0; Address(0x80fb[2:2],)
SPIWrite 45f6,00,0,7
SPIWrite 45fd,23,0,7 //PU_RX_INTP_LANE=0x0; Address(0x80fe[2:2],)
SPIWrite 45fc,00,0,7
SPIWrite 45ff,00,0,7 //PU_RX_AGC_LANE=0x0; Address(0x80ff[15:15],)
SPIWrite 45fe,b6,0,7
SPIWrite 49f1,00,0,7 //PU_RX_INTP_LANE2=0x0; Address(0x84f8[9:9],)
SPIWrite 49f0,40,0,7
SPIWrite 49f3,00,0,7 //PU_RX_ADC_LANE2=0x0; Address(0x84f9[9:9],)
SPIWrite 49f2,80,0,7
SPIWrite 41f7,6f,0,7 //PU_RX_ADC_LANE=0x0; Address(0x80fb[2:2],)

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SPIWrite 41f6,00,0,7
SPIWrite 41fd,23,0,7 //PU_RX_INTP_LANE=0x0; Address(0x80fe[2:2],)
SPIWrite 41fc,00,0,7
SPIWrite 41ff,00,0,7 //PU_RX_AGC_LANE=0x0; Address(0x80ff[15:15],)
SPIWrite 41fe,b6,0,7
SPIWrite 49f1,00,0,7 //PU_RX_INTP_LANE3=0x0; Address(0x84f8[6:6],)
SPIWrite 49f0,00,0,7
SPIWrite 49f3,00,0,7 //PU_RX_ADC_LANE3=0x0; Address(0x84f9[7:7],)
SPIWrite 49f2,00,0,7
SPIWrite 43f7,6f,0,7 //PU_RX_ADC_LANE=0x0; Address(0x80fb[2:2],)
SPIWrite 43f6,00,0,7
SPIWrite 43fd,23,0,7 //PU_RX_INTP_LANE=0x0; Address(0x80fe[2:2],)
SPIWrite 43fc,00,0,7
SPIWrite 43ff,00,0,7 //PU_RX_AGC_LANE=0x0; Address(0x80ff[15:15],)
SPIWrite 43fe,b6,0,7
SPIWrite 0015,00,0,7 //PAGE: SERDES=0x2; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,40,0,7
SPIWrite 45eb,0d,0,7 //PU_TX_LANE=0x0; Address(0x80f5[15:15],)
SPIWrite 45ea,b0,0,7
SPIWrite 49e3,d0,0,7 //PU_TX_DRV_LANE2=0x0; Address(0x84f1[13:13],)
SPIWrite 49e2,00,0,7
SPIWrite 47eb,0d,0,7 //PU_TX_LANE=0x0; Address(0x80f5[15:15],)
SPIWrite 47ea,b0,0,7
SPIWrite 49e3,c0,0,7 //PU_TX_DRV_LANE3=0x0; Address(0x84f1[12:12],)
SPIWrite 49e2,00,0,7
SPIWrite 49f1,90,0,7 //PU_RX_INTP_LANE2=0x0; Address(0x84f8[9:9],)
SPIWrite 49f0,40,0,7
SPIWrite 49f3,a8,0,7 //PU_RX_ADC_LANE2=0x0; Address(0x84f9[9:9],)
SPIWrite 49f2,80,0,7
SPIWrite 45f7,6f,0,7 //PU_RX_ADC_LANE=0x0; Address(0x80fb[2:2],)
SPIWrite 45f6,00,0,7
SPIWrite 45fd,23,0,7 //PU_RX_INTP_LANE=0x0; Address(0x80fe[2:2],)
SPIWrite 45fc,00,0,7
SPIWrite 45ff,00,0,7 //PU_RX_AGC_LANE=0x0; Address(0x80ff[15:15],)
SPIWrite 45fe,b6,0,7
SPIWrite 49f1,90,0,7 //PU_RX_INTP_LANE3=0x0; Address(0x84f8[6:6],)
SPIWrite 49f0,00,0,7
SPIWrite 49f3,a8,0,7 //PU_RX_ADC_LANE3=0x0; Address(0x84f9[7:7],)
SPIWrite 49f2,00,0,7
SPIWrite 47f7,6f,0,7 //PU_RX_ADC_LANE=0x0; Address(0x80fb[2:2],)
SPIWrite 47f6,00,0,7
SPIWrite 47fd,23,0,7 //PU_RX_INTP_LANE=0x0; Address(0x80fe[2:2],)
SPIWrite 47fc,00,0,7
SPIWrite 47ff,00,0,7 //PU_RX_AGC_LANE=0x0; Address(0x80ff[15:15],)
SPIWrite 47fe,b6,0,7
SPIWrite 0015,40,0,7 //PAGE: SERDES=0x0; Address(0x15[2:2],0x15[6:6],)
SPIWrite 0015,00,0,7

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\\Device Bringup is Completed