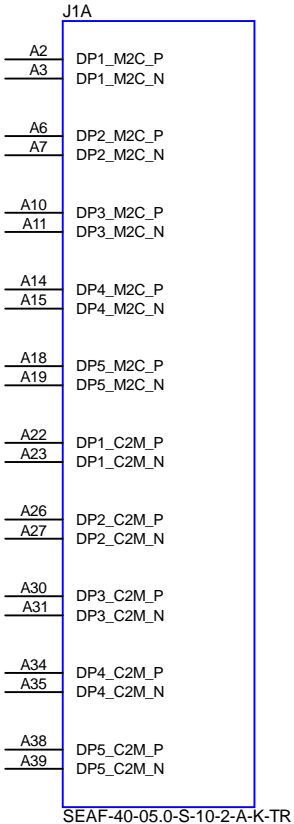


# ADC35XX To TSW1400 Interposer - LVDS

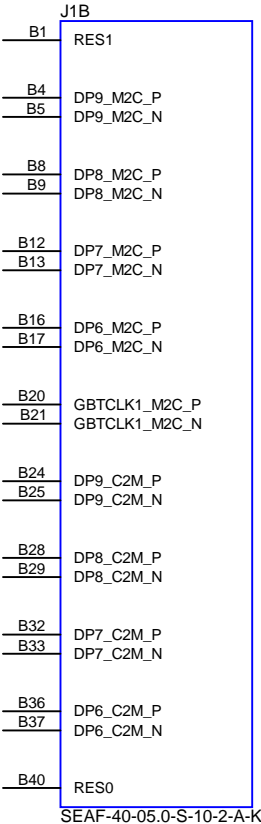
Orderable: <a href="#">ChangeMe in variant</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 6/12/2019
TID #: <a href="#">N/A</a>	Project Title: <a href="#">ADC35XX LVDS Interposer</a>	
Number: <a href="#">35XX_L_INT</a>	Rev: <a href="#">A</a>	Sheet Title:
SVN Rev: <a href="#">Not in version control</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">6</a> of <a href="#">9</a>
Drawn By:	File: <a href="#">ADC35XX_TSW1400_Interposer_CoverSheet_SizesSchDoc</a>	<a href="#">http://www.ti.com</a>
Engineer: <a href="#">Daniel Brock</a>	Contact: <a href="#">http://www.ti.com/support</a>	© Texas Instruments 2020



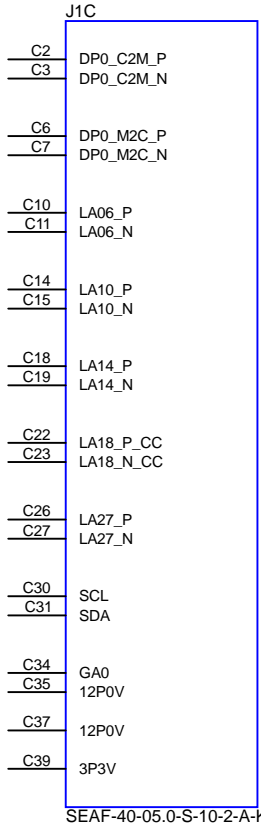
Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



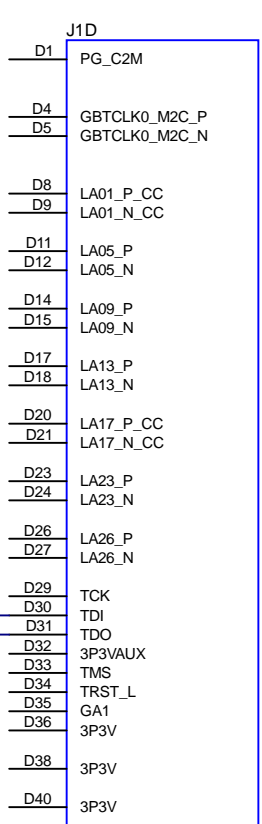
SEAF-40-05.0-S-10-2-A-K-TR



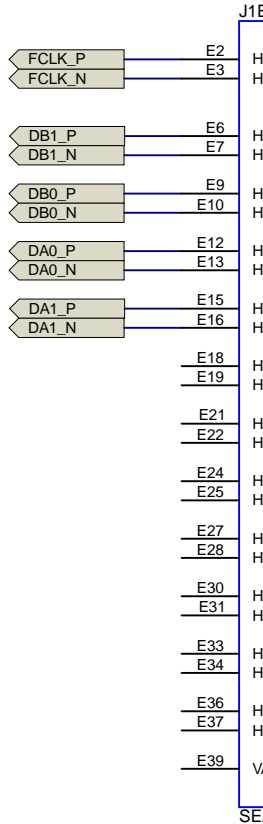
SEAF-40-05.0-S-10-2-A-K-TR



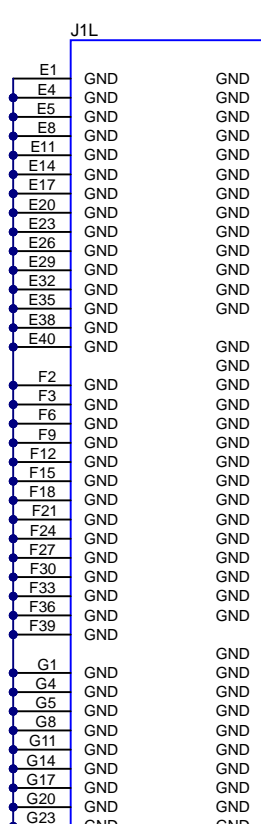
SEAF-40-05.0-S-10-2-A-K-TR



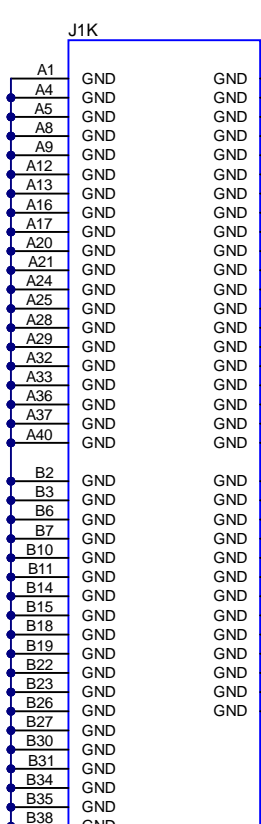
SEAF-40-05.0-S-10-2-A-K-TR



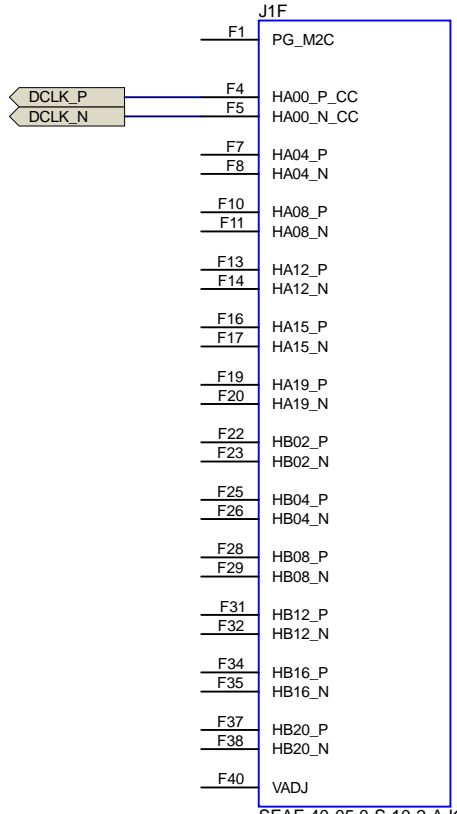
SEAF-40-05.0-S-10-2-A-K-TR



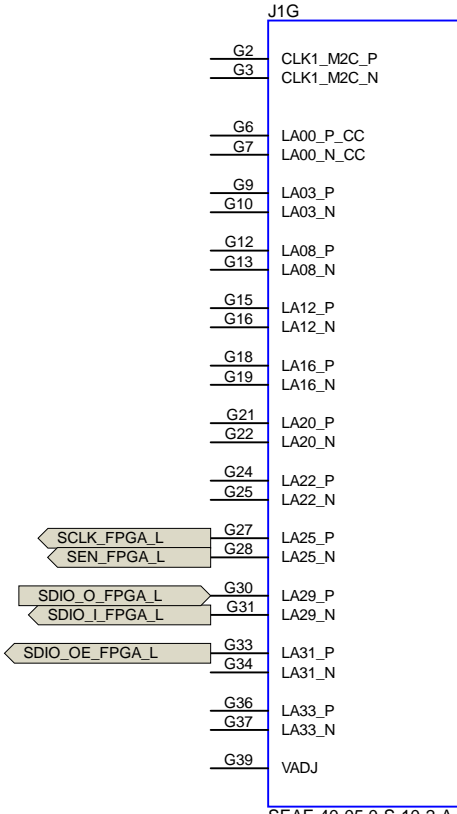
SEAF-40-05.0-S-10-2-A-K-TR



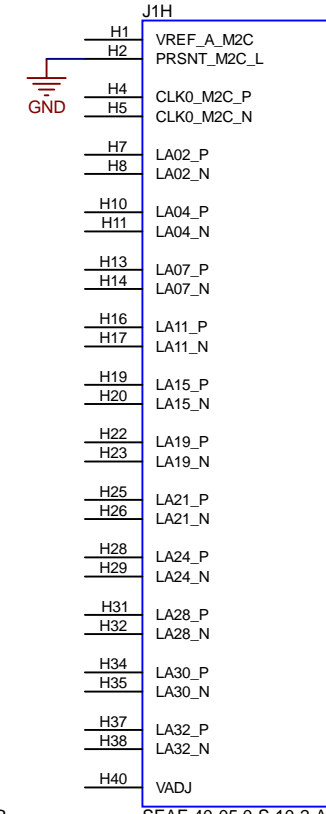
SEAF-40-05.0-S-10-2-A-K-TR



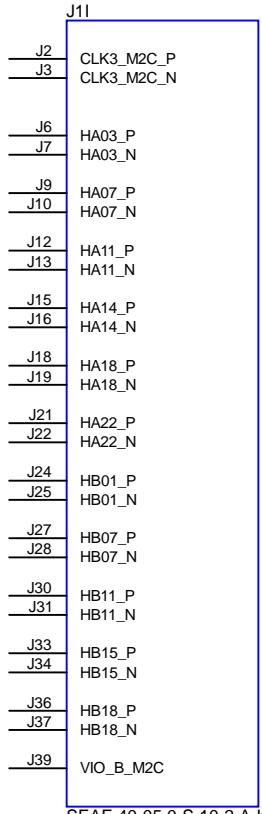
SEAF-40-05.0-S-10-2-A-K-TR



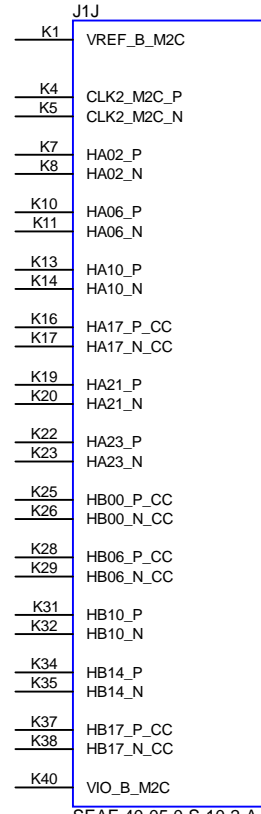
SEAF-40-05.0-S-10-2-A-K-TR



SEAF-40-05.0-S-10-2-A-K-TR

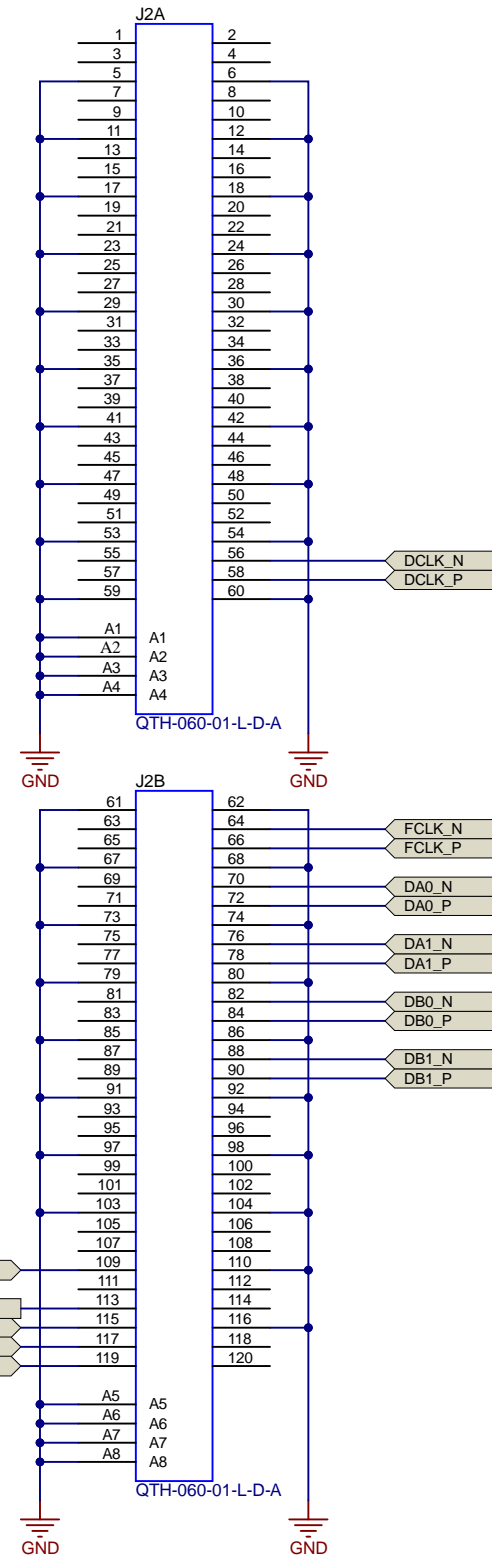


SEAF-40-05.0-S-10-2-A-K-TR



SEAF-40-05.0-S-10-2-A-K-TR

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.



Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.

Orderable: <a href="#">ChangeMe in variant</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 6/22/2020
TID #: N/A	Project Title: <a href="#">ADC35XX LVDS Interposer</a>	
Number: <a href="#">35XX_L_INT</a>	Rev: <a href="#">A</a>	Sheet Title:
SVN Rev: Not in version control	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">8</a> of <a href="#">9</a>
Drawn By:	File: <a href="#">ADC35XX_TSW1400_Interposer_HSMC_LVDS_SchDoc</a>	Size: <a href="#">1000</a> x <a href="#">1000</a>
Engineer: <a href="#">Daniel Brock</a>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	



© Texas Instruments 2020

H1 NY PMS 440 0025 PH  
 H2 NY PMS 440 0025 PH  
 H3 NY PMS 440 0025 PH  
 H4 NY PMS 440 0025 PH

H5 1902C  
 H6 1902C  
 H7 1902C  
 H8 1902C

PCB Number: 35XX\_L\_INT  
 PCB Rev: A

PCB LOGO  
 Texas Instruments



PCB LOGO  
 FCC disclaimer

PCB LOGO  
 WEEE logo

LBL1  
 PCB Label  
 THT-14-423-10  
 Size: 0.65" x 0.20 "

Variant/Label Table	
Label	Label Text
LBL1	ADC35XX_LVDS_Interposer

Orderable: <a href="#">ChangeMe in variant</a>	Designed for: <a href="#">Public Release</a>	Mod. Date: 6/22/2020
TID #: <a href="#">N/A</a>	Project Title: <a href="#">ADC35XX LVDS Interposer</a>	
Number: <a href="#">35XX_L_INT</a>	Rev: <a href="#">A</a>	Sheet Title:
SVN Rev: <a href="#">Not in version control</a>	Assembly Variant: <a href="#">001</a>	Sheet: <a href="#">9</a> of <a href="#">9</a>
Drawn By:	File: <a href="#">ADC35XX_TSW1400_Interposer_Hardware_LVDS68B.Doc</a>	
Engineer: <a href="#">Daniel Brock</a>	Contact: <a href="http://www.ti.com/support">http://www.ti.com/support</a>	

Texas Instruments and/or its licensors do not warrant the accuracy or completeness of this specification or any information contained therein. Texas Instruments and/or its licensors do not warrant that this design will meet the specifications, will be suitable for your application or fit for any particular purpose, or will operate in an implementation. Texas Instruments and/or its licensors do not warrant that the design is production worthy. You should completely validate and test your design implementation to confirm the system functionality for your application.