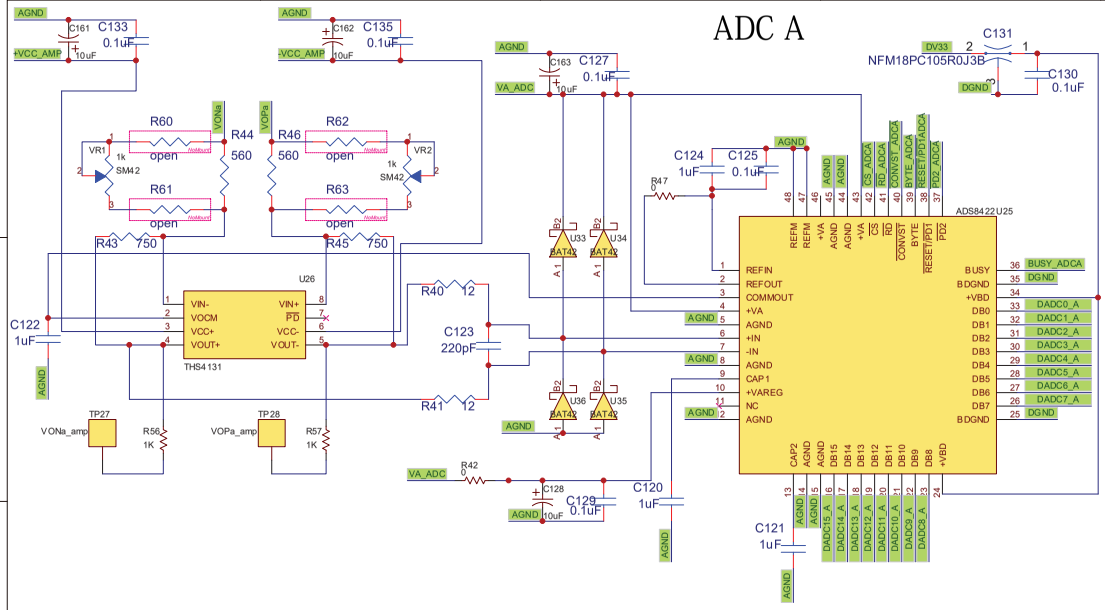

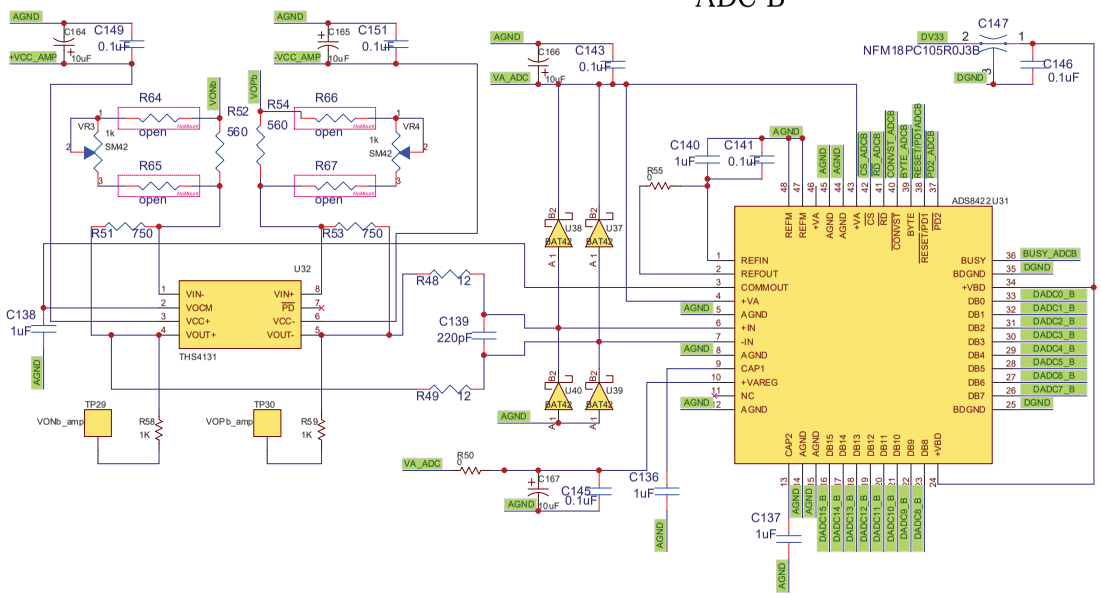

VA_ADC = 5V
+VCC_AMP = +5V
-VCC_AMP = -5V
DV33 = 3.3V

VONa, VOPa and VONb, VOPb is analog that need to be convert to digital.

CS_ADC,RD_ADC, CONVST_ADC, BYTE_ADC, RESET/ PDA1ADC, PD2ADC, BUSY_ADC and DADC[15:0] for both ADC A and ADC B is connected to FPGA.

DESIGN	D.W.G	CHECK	APPROVAL

A3

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