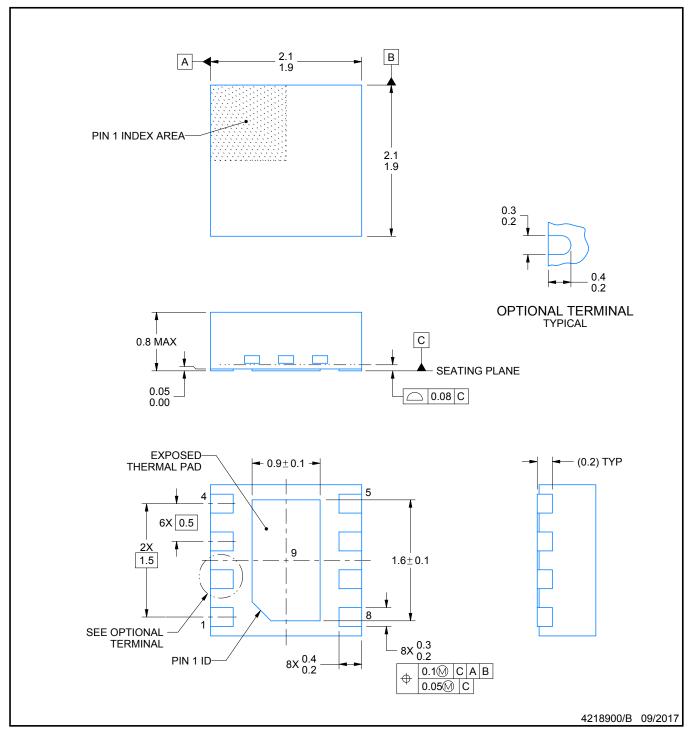
DATA BOOK PACKAGE OUTLINE

LEADFRAME EXAMPLE 4211158

DRAFTER:	K.SINCERBOX	DATE:	08/03/2016		DIMENSIONS IN MILLIMETERS		
DESIGNER:		DATE:			TEXAS INSTRUMENTS CODE IDENTIF		
CHECKER:		DATE:			SEMICONDUCTOR OPERATIONS		1295
ENGINEER:	H. WIESNER	DATE:	09/19/2017		ePOD, DSG0008A / WS0	ON.	
APPROVED:	D. CHIN & E. LEE	DATE:	09/19/2017		8 PIN, 0.5 MM PITCH	•	
RELEASED:	WDM	DATE:	09/19/2017		,		
TEMPLATE INFO: EDGE# 4218519		DATE:	04/07/2016	NTS A	4218900	B	PAGE 1 of 5



PLASTIC SMALL OUTLINE - NO LEAD

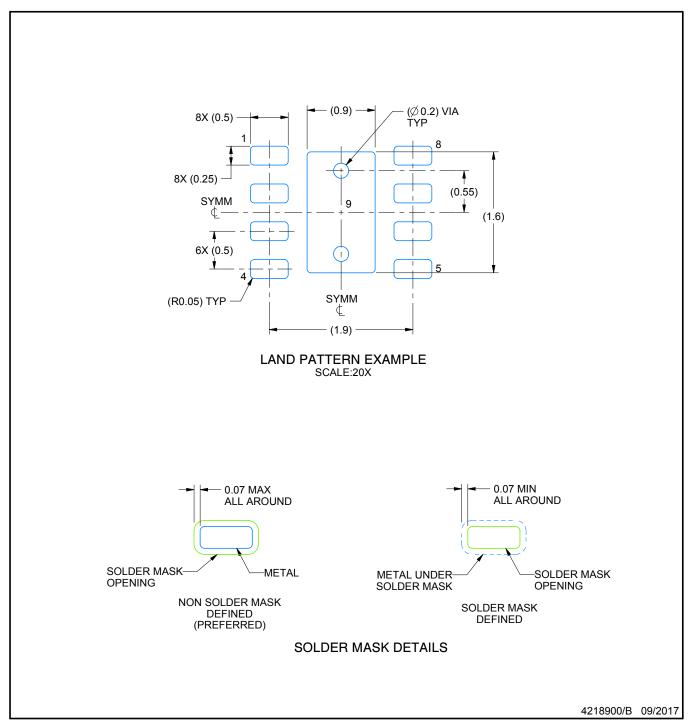


NOTES:

- All linear dimensions are in millimeters. Any dimensions in parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
- 2. This drawing is subject to change without notice.
- 3. The package thermal pad must be soldered to the printed circuit board for thermal and mechanical performance.



PLASTIC SMALL OUTLINE - NO LEAD

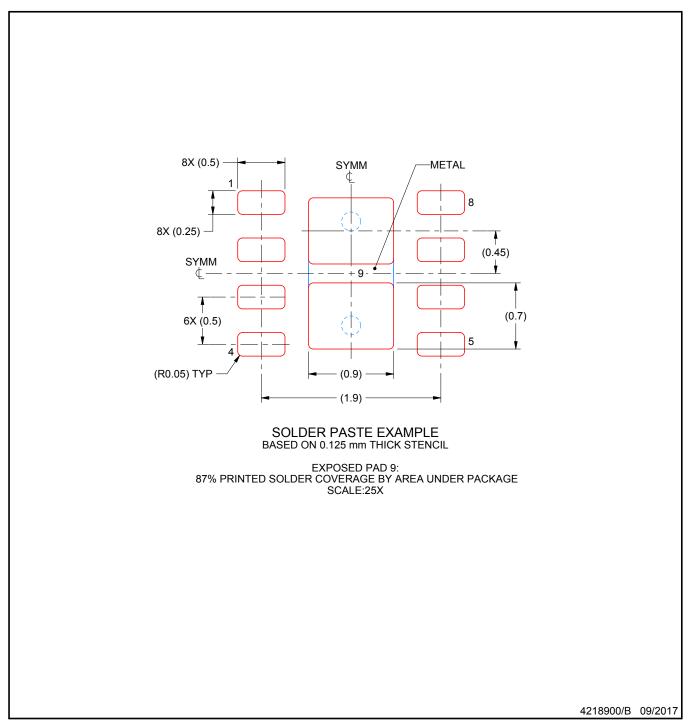


NOTES: (continued)

- 4. This package is designed to be soldered to a thermal pad on the board. For more information, see Texas Instruments literature number SLUA271 (www.ti.com/lit/slua271).
- Vias are optional depending on application, refer to device data sheet. If any vias are implemented, refer to their locations shown on this view. It is recommended that vias under paste be filled, plugged or tented.



PLASTIC SMALL OUTLINE - NO LEAD



NOTES: (continued)

6. Laser cutting apertures with trapezoidal walls and rounded corners may offer better paste release. IPC-7525 may have alternate design recommendations.



REVISIONS									
EV	DESCRIPTION		ECR	DATE	ENGINEER / D				
۹ 3	RELEASE NEW DRAWING REMOVE "OPTIONAL" FROM PIN 1 ID CALLOUT		2159281 2168827	08/03/2016 09/19/2017	E.REY / K.SINO H. WIESNER / K.S				
'	REMOVE OF HOMAL I ROWLFIN I ID GALLOUT		2100027	09/19/2017	11. WILSNER / K.S	DINCERBOX			
	T so	CALE SIZE				REV PAG			
		A		421890)	B 5 or			